



Schottky Barrier Diode

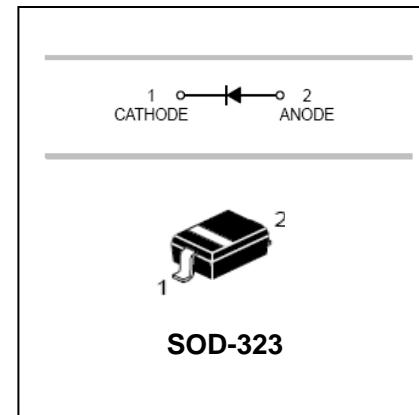
BAT54J

FEATURES

- Very small conduction losses
- Negligible switching losses
- Low forward voltage drop
- Surface mount device



Lead-free



APPLICATIONS

- Schottky barrier diodes
- Single and double diodes with different pinning are available

ORDERING INFORMATION

Type No.	Marking	Package Code
BAT54J	86	SOD-323

MAXIMUM RATING @ Ta=25°C unless otherwise specified

Parameter	Symbol	Limits	Unit
Peak Repetitive reverse voltage	V _{RRM}	30	V
Forward continuous current	I _F	0.3	A
Surge non repetitive forward current tp=10ms	I _{FSM}	1	A
Power Dissipation	P _d	230	mW
Thermal resistance,junction to ambient air	R _{θjA}	550	°C/W
Junction temperature	T _j	150	°C
Storage temperature range	T _{stg}	-65 to +150	°C



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ELECTRICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward voltage	V_{F1}	$I_F=0.1\text{mA}$			240	mV
	V_{F2}	$I_F=1.0\text{mA}$			320	mV
	V_{F3}	$I_F=10\text{mA}$			400	mV
	V_{F4}	$I_F=30\text{mA}$			500	mV
	V_{F5}	$I_F=100\text{mA}$			900	mV
Reverse leakage current	I_R	$V_R=30\text{V} \quad T_j=25^\circ\text{C}$		1		μA
		$V_R=30\text{V} \quad T_j=100^\circ\text{C}$			100	μA
Reverse recovery time	t_{rr}	$I_F=10\text{mA}, I_R=10\text{mA}$ to 1mA $R_L=100\Omega$			5.0	ns
Junction capacitance	C_J	$V_R=1.0\text{V}, f=1.0\text{MHz}$			10	pF

TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

Fig. 1-1: Forward voltage drop versus forward current (typical values, low level).

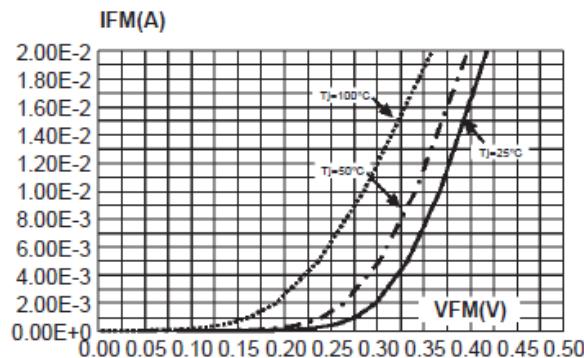


Fig. 2: Reverse leakage current versus reverse voltage applied (typical values).

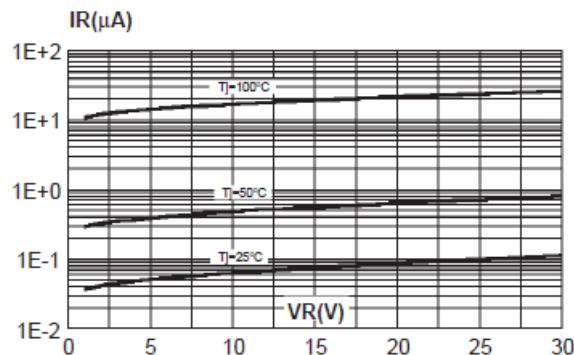


Fig. 1-2: Forward voltage drop versus forward current (typical values, high level).

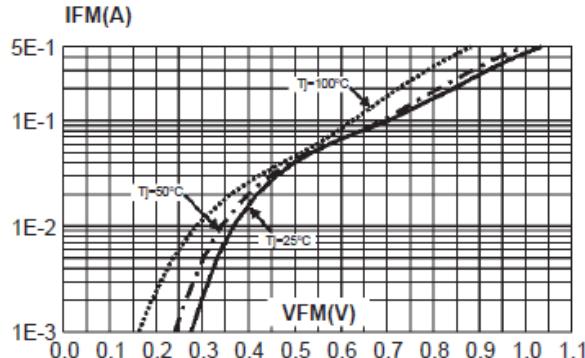
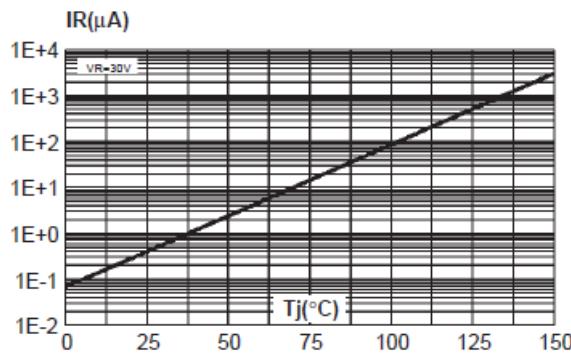


Fig. 3: Reverse leakage current versus junction temperature.



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Fig. 4: Junction capacitance versus reverse voltage applied (typical values).

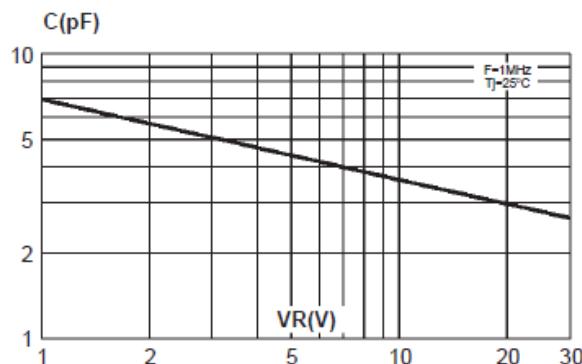


Fig. 5: Relative variation of thermal impedance junction to ambient versus pulse duration (epoxy FR4 with recommended pad layout, $e(Cu)=35\mu m$)

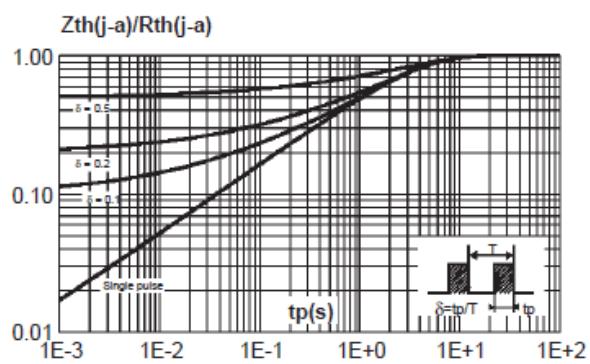
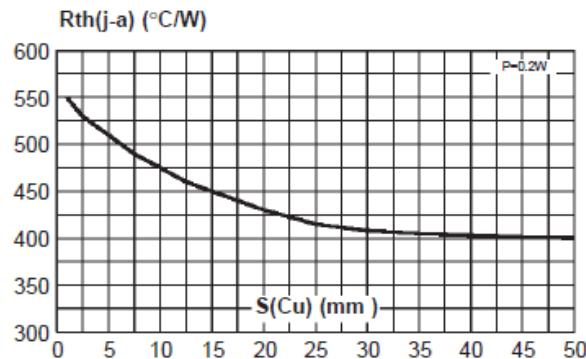


Fig. 6: Thermal resistance junction to ambient versus copper surface under each lead (Epoxy printed circuit board FR4, copper thickness: $35\mu m$.)





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PACKAGE OUTLINE

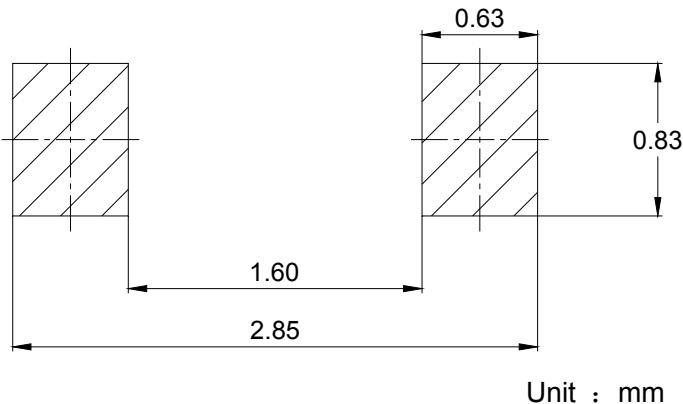
Plastic surface mounted package

SOD-323

SOD-323		
Dim	Min	Max
A	1.60	1.80
B	1.20	1.40
C	0.9 Max	
D	0.30 Typical	
E	0.22	0.42
H	0.02	0.1
J	0.1 Typical	
K	2.55	2.75

All Dimensions in mm

SOLDERING FOOTPRINT



Unit : mm

PACKAGE INFORMATION

Device	Package	Shipping
BAT54J	SOD-323	3000/Tape&Reel