Schottky barrier diodes in small packages Rev. 06 — 21 December 2006

Product data sheet

1. Product profile

1.1 General description

Planar Schottky barrier diodes with an integrated guard ring for stress protection. Encapsulated in small Surface-Mounted Device (SMD) plastic packages.

Table 1.Product overview

| Type number | Package | | Configuration |
|-------------|---------|-------|---------------------|
| | NXP | JEITA | |
| 1PS76SB21 | SOD323 | SC-76 | single |
| BAT721 | SOT23 | - | single |
| BAT721A | SOT23 | - | dual common anode |
| BAT721C | SOT23 | - | dual common cathode |
| BAT721S | SOT23 | - | dual series |

1.2 Features

- Low forward voltage
- Small SMD plastic packages
- Low capacitance

1.3 Applications

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Reverse polarity protection

1.4 Quick reference data

| Table 2. | Quick reference data | | | | | |
|----------------|----------------------|-------------------------|--------------|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| Per diode |) | | | | | |
| l _F | forward current | | - | - | 200 | mA |
| V _R | reverse voltage | | - | - | 40 | V |
| V _F | forward voltage | I _F = 200 mA | <u>[1]</u> _ | - | 550 | mV |



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2. Pinning information

| Pin | Description | Simplified outline | Symbol |
|-----------|---|--------------------|--|
| 1PS76SB21 | l i i i i i i i i i i i i i i i i i i i | | |
| 1 | cathode | <u>[1]</u> | _ / |
| 2 | anode | | 1 <u>-</u> 2 sym001 |
| BAT721 | | | |
| 1 | anode | | |
| 2 | not connected | 3 | 3 |
| 3 | cathode | 1 2 006aaa144 | 1 n.c. 006aaa436 |
| BAT721A | | | |
| 1 | cathode (diode 1) | | |
| 2 | cathode (diode 2) | 3 | 3 |
| 3 | anode (diode 1), anode (diode 2) | 1 2 006aaa144 | 1 - 5 - 2 006aaa439 |
| BAT721C | | | |
| 1 | anode (diode 1) | | |
| 2 | anode (diode 2) | 3 | 3 |
| 3 | cathode (diode 1), cathode (diode 2) | 1 2 006aaa144 | 1 - J - J - 2 006aaa438 |
| BAT721S | | | |
| 1 | anode (diode 1) | _ | |
| 2 | cathode (diode 2) | 3 | 3 |
| 3 | cathode (diode 1), anode (diode 2) | 1 2 006aaa144 | 1 - D 2 006aaa437 |

[1] The marking bar indicates the cathode.

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3. Ordering information

| Table 4. Orde | ering inform | ation | | | | | |
|---------------|--------------|--|---------|--|--|--|--|
| Type number | Package | Package | | | | | |
| | Name | Description | Version | | | | |
| 1PS76SB21 | SC-76 | plastic surface-mounted package; 2 leads | SOD323 | | | | |
| BAT721 | - | plastic surface-mounted package; 3 leads | SOT23 | | | | |
| BAT721A | | | | | | | |
| BAT721C | | | | | | | |
| BAT721S | | | | | | | |

4. Marking

| Table 5. Marking codes | |
|--------------------------|-----------------------------|
| Type number | Marking code ^[1] |
| 1PS76SB21 | S1 |
| BAT721 | L7* |
| BAT721A | L8* |
| BAT721C | L9* |
| BAT721S | L0* |

[1] * = -: made in Hong Kong

* = p: made in Hong Kong

- * = t: made in Malaysia
- * = W: made in China

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|-------------------------------------|---|-----|------|------|
| Per diode | | | | | |
| V _R | reverse voltage | | - | 40 | V |
| I _F | forward current | | - | 200 | mA |
| I _{FSM} | non-repetitive peak forward current | half sine wave; JEDEC method; t _p = 8.3 ms | - | 1 | A |
| Tj | junction temperature | | - | 125 | °C |
| T _{amb} | ambient temperature | | -65 | +150 | °C |
| T _{stg} | storage temperature | | -65 | +150 | °C |

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6. Thermal characteristics

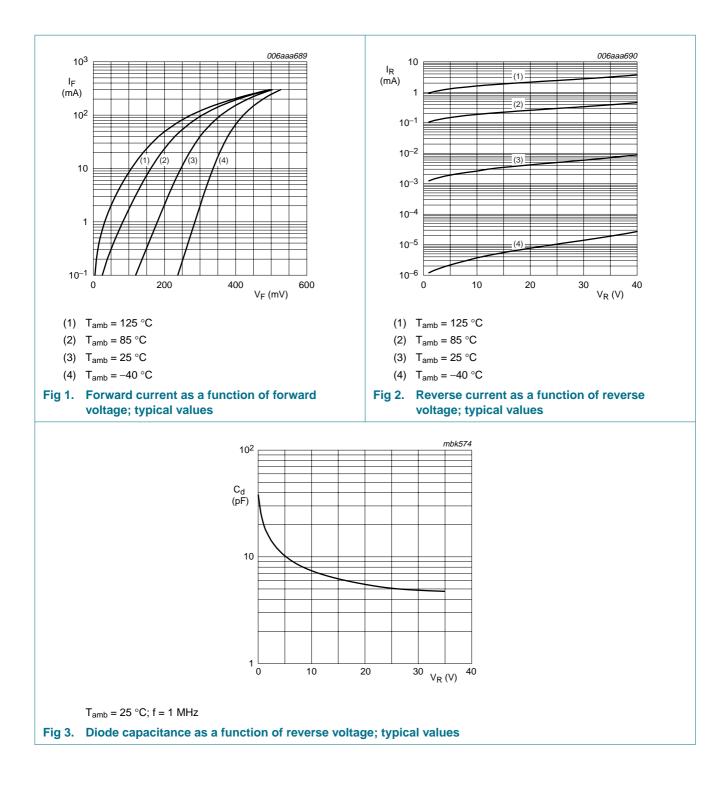
| Table 7. | Thermal characteristics | | | | | |
|----------------------|---|-------------|------------|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| Per diode |) | | | | | |
| R _{th(j-a)} | thermal resistance from junction to ambient | in free air | <u>[1]</u> | | | |
| | 1PS76SB21 | | - | - | 450 | K/W |
| | BAT721 | | - | - | 500 | K/W |
| | BAT721A | | - | - | 500 | K/W |
| | BAT721C | | - | - | 500 | K/W |
| | BAT721S | | - | - | 500 | K/W |
| | | | | | | |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

7. Characteristics

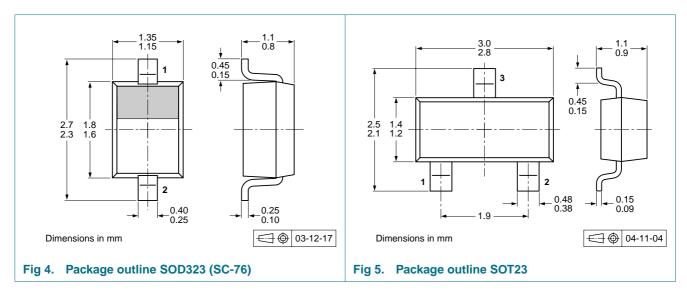
| | | | | specified. | Characteristics °C unless otherwise s | Table 8. <i>T_{amb} = 25</i> |
|------|-----|-----|--------------|--|--|--|
| Unit | Max | Тур | Min | Conditions | Parameter | Symbol |
| | | | | |) | Per diode |
| mV | 300 | - | <u>[1]</u> _ | I _F = 10 mA | V _F forward voltage | V _F |
| mV | 420 | - | <u>[1]</u> _ | I _F = 100 mA | | |
| mV | 550 | - | <u>[1]</u> - | I _F = 200 mA | | |
| μΑ | 15 | - | - | V _R = 30 V | reverse current | I _R |
| mA | 3 | - | - | $V_R = 30 \text{ V}; \text{ T}_j = 100 ^{\circ}\text{C}$ | | |
| pF | 50 | 40 | - | V _R = 0 V; f = 1 MHz | diode capacitance | C _d |
| | 50 | 40 | - | , | diode capacitance | C _d |

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8. Package outline



9. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

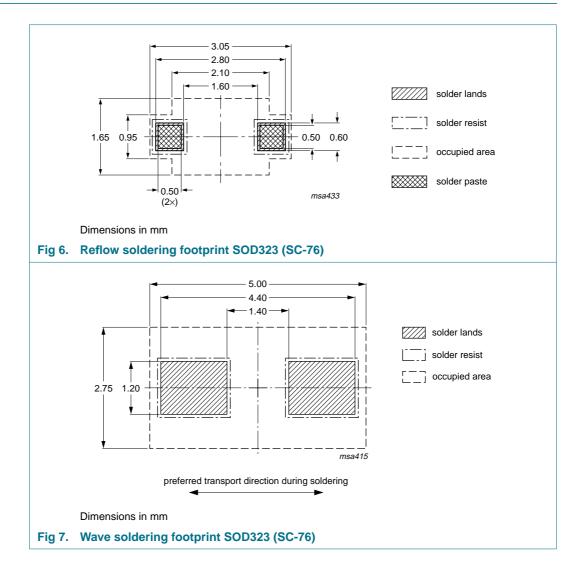
| Type number | Package | je Description | | Packing quantity | | |
|-------------|---------|--------------------------------|------|------------------|--|--|
| | | | 3000 | 10000 | | |
| 1PS76SB21 | SOD323 | 4 mm pitch, 8 mm tape and reel | -115 | -135 | | |
| BAT721 | SOT23 | 4 mm pitch, 8 mm tape and reel | -215 | -235 | | |
| BAT721A | | | | | | |
| BAT721C | | | | | | |
| BAT721S | | | | | | |

[1] For further information and the availability of packing methods, see Section 13.

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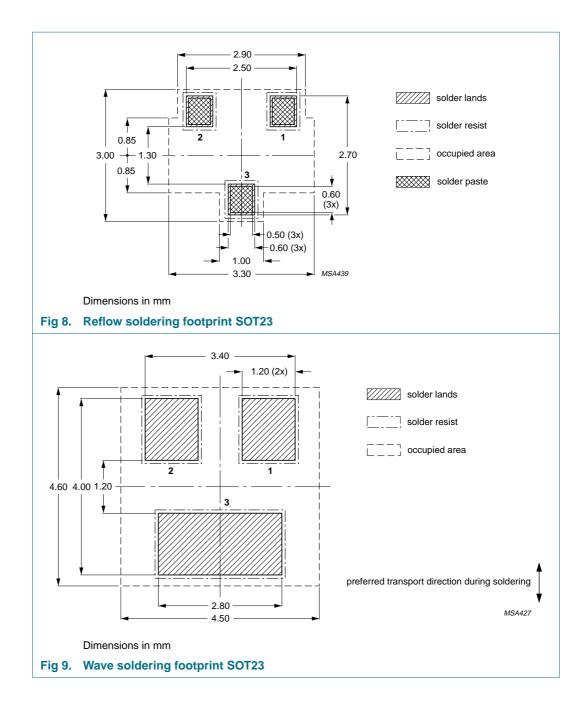
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10. Soldering



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11. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes | | | |
|------------------------|---|---|-------------------------|--------------------------------|--|--|--|
| 1PS76SB21_BAT721_SER_6 | 20061221 | Product data sheet | - | 1PS76SB21_BAT721 _SER_5 | | | |
| Modifications: | Amended T | able 10 "Revision history" | | | | | |
| 1PS76SB21_BAT721_SER_5 | 20061205 | Product data sheet | - | BAT721_SERIES_4 1PS76SB21_3 | | | |
| Modifications: | | of this data sheet has been of NXP Semiconductors. | n redesigned to comply | y with the new identity | | | |
| | Legal texts | have been adapted to the | new company name w | here appropriate. | | | |
| | This data sl 1PS76SB2⁻ | heet is a combination of da 1_3. | ata sheets BAT721_SE | RIES_4 and | | | |
| | Table 1 "Property of the second se | oduct overview": added | | | | | |
| | Section 1.2 | "Features": amended | | | | | |
| | Section 1.3 | "Applications": amended | | | | | |
| | Table 2 "Qu | iick reference data": added | | | | | |
| | <u>Table 5 "Marking codes"</u>: for 1PS76SB21 amended | | | | | | |
| | • Table 5 "Ma | arking codes": enhanced ta | ble note section | | | | |
| | Table 6 "Lin | <u>Table 6 "Limiting values"</u>: indication per diode added | | | | | |
| | • Table 6 "Lin | <u>Table 6 "Limiting values"</u>: for 1PS76SB21 I_{FSM} condition amended | | | | | |
| | <u>Table 6 "Limiting values"</u>: T_{amb} ambient temperature added | | | | | | |
| | Table 7 "The | <u>Table 7 "Thermal characteristics</u>": indication per diode added | | | | | |
| | • Table 7: R _{th} | _(j-a) thermal resistance fror | n junction to ambient c | ondition amended | | | |
| | <u>Table 8 "Characteristics</u>": indication per diode added | | | | | | |
| | <u>Table 8 "Characteristics"</u>: reference to <u>Table note 1</u> amended | | | | | | |
| | • <u>Table 8</u> : for | 1PS76SB21 C _d minimum | value changed to typic | al value | | | |
| | Figure 1 an | d <u>2</u> : amended | | | | | |
| | Figure 4 an | d <u>5</u> : superseded by minimi | ized package outlines | | | | |
| | | Packing information": adde | d | | | | |
| | | "Soldering": added | | | | | |
| | Section 12 | "Legal information": update | ed | | | | |
| BAT721_SERIES_4 | 20040315 | Product specification | - | BAT721_SERIES_3 | | | |
| 1PS76SB21_3 | 20040126 | Product specification | - | 1PS76SB21_2 | | | |

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12. Legal information

12.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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