



# BAT754L-Q

## Schottky barrier triple diode

4 November 2024

Product data sheet

## 1. General description

Three internal isolated planar Schottky barrier diodes with an integrated guard ring for stress protection, encapsulated in very small SOT363 Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Low forward voltage
- Low capacitance
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- Ultra high-speed switching
- Line termination
- Voltage clamping
- Reverse polarity protection

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
$V_R$	reverse voltage		-	-	30	V
$V_F$	forward voltage	$I_F = 100 \text{ mA}$ ; pulsed; $t_p \leq 300 \mu\text{s}$ ; $\delta \leq 0.02$ ; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	750	mV
$I_R$	reverse current	$V_R = 25 \text{ V}$ ; pulsed; $t_p \leq 300 \mu\text{s}$ ; $\delta \leq 0.02$ ; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	2	$\mu\text{A}$

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	<p>TSSOP6 (SOT363)</p>	<p>aaa-005704</p>
2	A2	anode (diode 2)		
3	A3	anode (diode 3)		
4	K3	cathode (diode 3)		
5	K2	cathode (diode 2)		
6	K1	cathode (diode 1)		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BAT754L-Q	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BAT754L-Q	L1%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode						
V <sub>R</sub>	reverse voltage			-	30	V
I <sub>F</sub>	forward current			-	200	mA
I <sub>FRM</sub>	repetitive peak forward current	t <sub>p</sub> < 1 s; δ < 0.5		-	300	mA
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> < 10 ms; T <sub>j(init)</sub> = 25 °C		-	600	mA
T <sub>j</sub>	junction temperature			-	125	°C
T <sub>amb</sub>	ambient temperature			-55	125	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

9. Thermal characteristics

Table 6. Thermal characteristics

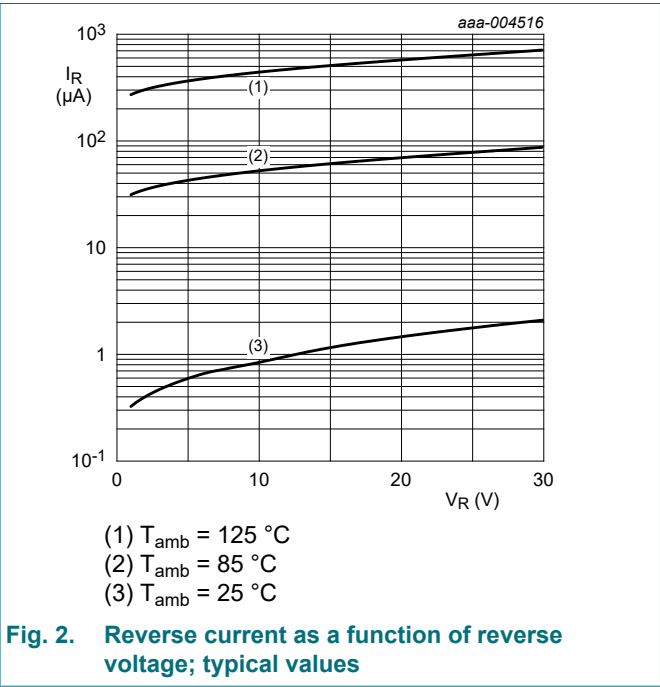
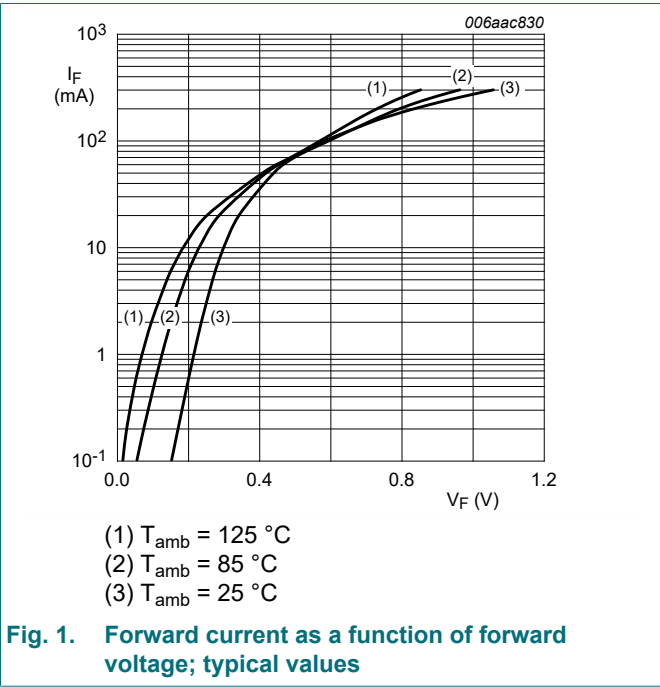
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

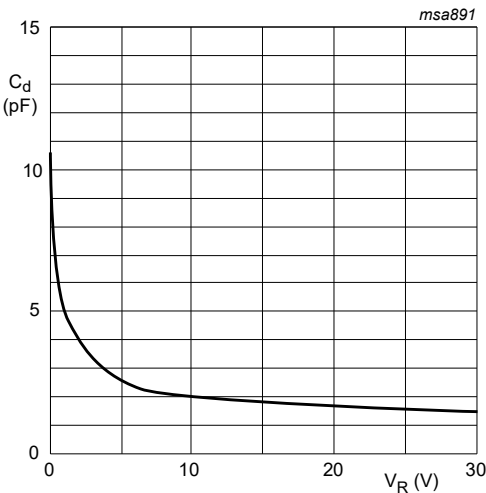
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per diode							
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-	200	mV
		I <sub>F</sub> = 1 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-	260	mV
		I <sub>F</sub> = 10 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-	340	mV
		I <sub>F</sub> = 30 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-	420	mV
		I <sub>F</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-	750	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 25 V; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-	2	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	-	10	pF





$f = 1\text{ MHz}$ ;  $T_{\text{amb}} = 25\text{ }^{\circ}\text{C}$

Fig. 3. Diode capacitance as a function of reverse voltage; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

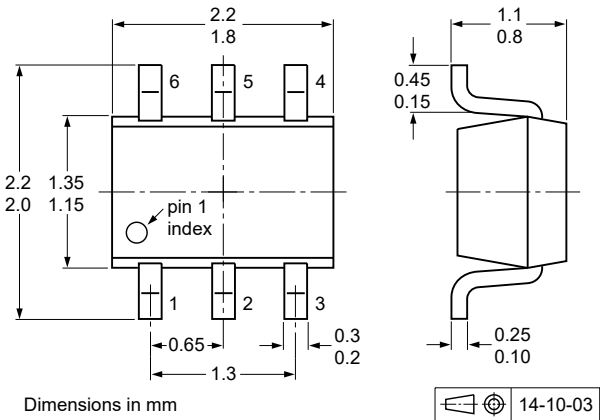


Fig. 4. Package outline TSSOP6 (SOT363)

13. Soldering

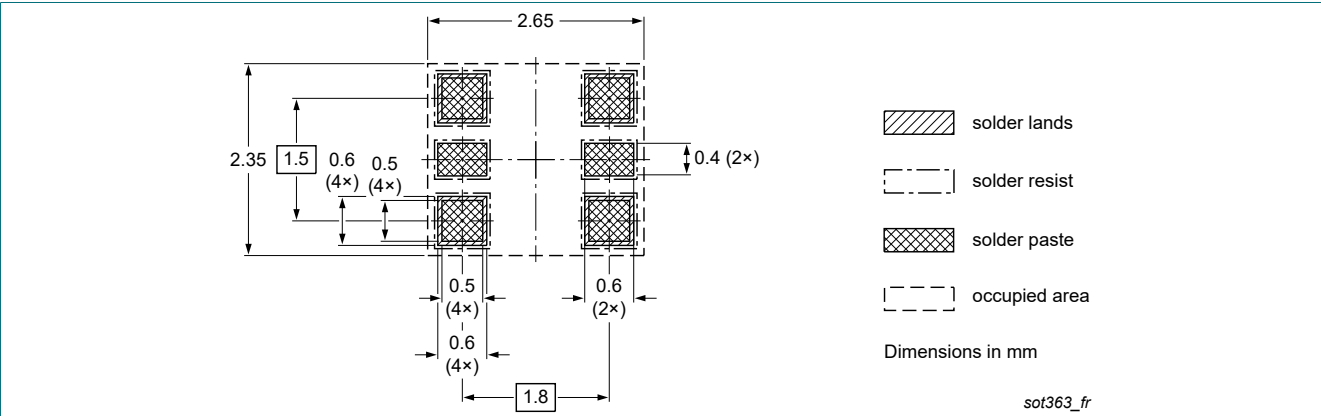


Fig. 5. Reflow soldering footprint for TSSOP6 (SOT363)

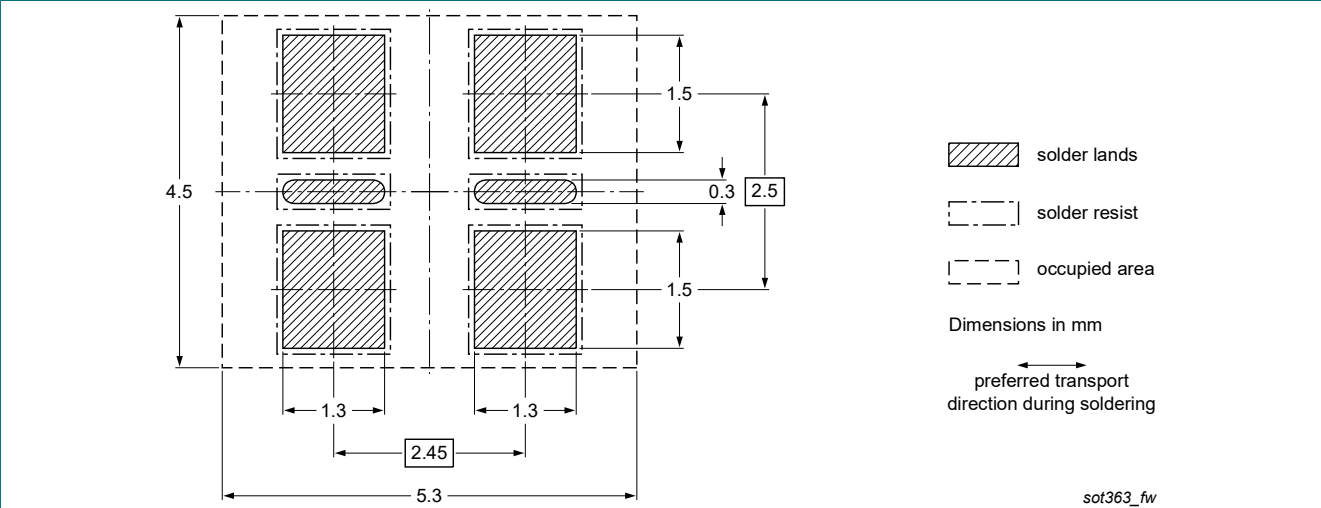


Fig. 6. Wave soldering footprint for TSSOP6 (SOT363)

## 14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT754L-Q v.1	20241104	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 4 November 2024