# **Amplifier Transistors**

## **PNP Silicon**

### **Features**

• Pb-Free Packages are Available\*

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit	
Collector – Emitter Voltage	V <sub>CEO</sub>	-45	Vdc	
Collector – Base Voltage	V <sub>CBO</sub>	-50	Vdc	
Emitter – Base Voltage	V <sub>EBO</sub>	-5.0	Vdc	
Collector Current – Continuous	Ic	-100	mAdc	
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	350 2.8	mW mW/°C	
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	1.0 8.0	W mW/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

#### THERMAL CHARACTERISTICS

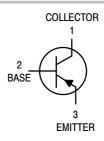
Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	357	°C/W	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	125	°C/W	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



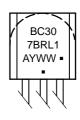
### ON Semiconductor®

### http://onsemi.com





### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week ■ = Pb-Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BC307BRL1G	TO-92 (Pb-Free)	2000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = -2.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	-45	_	_	Vdc
Emitter – Base Breakdown Voltage (I <sub>E</sub> = –100 μAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	-5.0	_	-	Vdc
Collector–Emitter Leakage Current $(V_{CES} = -50 \text{ V}, V_{BE} = 0)$ $(V_{CES} = -50 \text{ V}, V_{BE} = 0) \text{ T}_A = 125^{\circ}\text{C}$	I <sub>CES</sub>	_ _	-0.2 -0.2	-15 -4.0	nAdc μA
ON CHARACTERISTICS					
DC Current Gain $ \begin{array}{l} (I_C = -10 \; \mu \text{Adc}, \; V_{CE} = -5.0 \; \text{Vdc}) \\ (I_C = -2.0 \; \text{mAdc}, \; V_{CE} = -5.0 \; \text{Vdc}) \\ (I_C = -100 \; \text{mAdc}, \; V_{CE} = -5.0 \; \text{Vdc}) \end{array} $	h <sub>FE</sub>	_ 200 _	150 290 180	- 460 -	_
	V <sub>CE(sat)</sub>	- - -	-0.10 -0.30 -0.25	-0.3 -0.6 -	Vdc
Base – Emitter Saturation Voltage ( $I_C = -10$ mAdc, $I_B = -0.5$ mAdc) ( $I_C = -100$ mAdc, $I_B = -5.0$ mAdc)	V <sub>BE(sat)</sub>	- -	-0.7 -1.0	- -	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = -2.0 mAdc, V <sub>CE</sub> = -5.0 Vdc)	V <sub>BE(on)</sub>	-0.55	-0.62	-0.7	Vdc
DYNAMIC CHARACTERISTICS					
Current – Gain – Bandwidth Product (I <sub>C</sub> = –10 mAdc, V <sub>CE</sub> = –5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	_	280	_	MHz
Common Base Capacitance (V <sub>CB</sub> = -10 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>cbo</sub>	_	_	6.0	pF
Noise Figure (I <sub>C</sub> = $-0.2$ mAdc, V <sub>CE</sub> = $-5.0$ Vdc, R <sub>S</sub> = $2.0$ k $\Omega$ , f = $1.0$ kHz)	NF	_	2.0	10	dB

<sup>1.</sup>  $I_C = -10$  mAdc on the constant base current characteristic, which yields the point  $I_C = -11$  mAdc,  $V_{CE} = -1.0$  V.

### **TYPICAL CHARACTERISTICS**

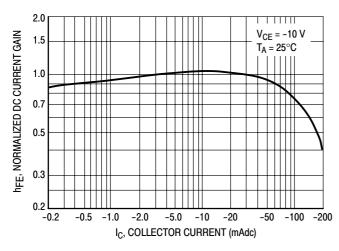


Figure 1. Normalized DC Current Gain

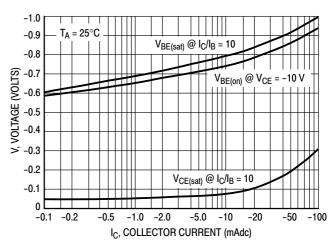


Figure 2. "Saturation" and "On" Voltages

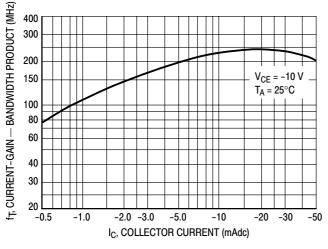


Figure 3. Current-Gain — Bandwidth Product

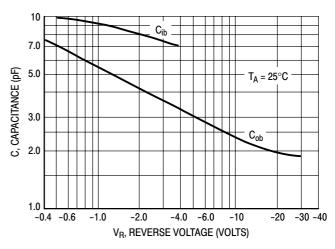


Figure 4. Capacitances

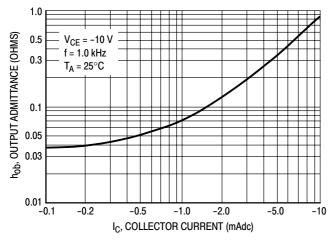


Figure 5. Output Admittance

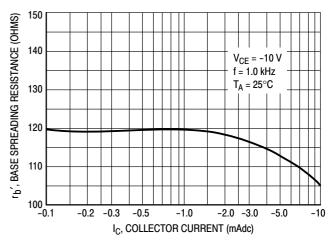
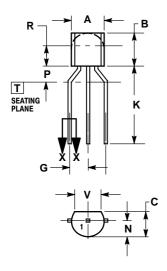


Figure 6. Base Spreading Resistance

#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AM** 





#### NOTES

- DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS		
DIM	MIN MAX		
Α	4.45	5.20	
В	4.32	5.33	
С	3.18	4.19	
D	0.40	0.54	
G	2.40	2.80	
J	0.39	0.50	
K	12.70		
N	2.04	2.66	
P	1.50	4.00	
R	2.93		
V	3 //3		

STYLE 17:

PIN 1. COLLECTOR

BASE 3. EMITTER

ON Semiconductor and una are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and are registered readerlands of semiconductor Components industries, Ltc (SCILLC). Solitude services the inject that changes without further holice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications. intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative