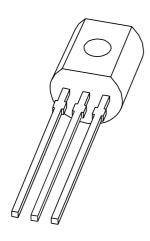
DISCRETE SEMICONDUCTORS

DATA SHEET



BC618NPN Darlington transistor

Product specification Supersedes data of 2003 Oct 16 2004 Nov 05





NPN Darlington transistor

BC618

FEATURES

- Low current (max. 500 mA)
- Low voltage (max. 55 V)
- High DC current gain.

APPLICATIONS

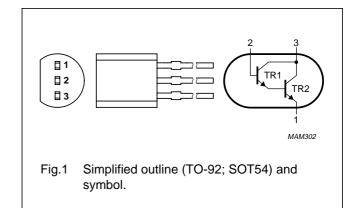
- General purpose low frequency
- · Relay drivers.

DESCRIPTION

NPN Darlington transistor in a TO-92; SOT54 plastic package.

PINNING

PIN	DESCRIPTION
1	emitter
2	base
3	collector



ORDERING INFORMATION

TYPE NUMBER		PACKAGE				
I TPE NOWIDER	NAME DESCRIPTION VE					
BC618	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54			

NPN Darlington transistor

BC618

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	80	V
V _{CES}	collector-emitter voltage	V _{BE} = 0 V	_	55	V
V _{EBO}	emitter-base voltage	open collector	_	12	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	800	mA
I _B	base current (DC)		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	625	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	200	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

^{1.} Transistor mounted on an FR4 printed-circuit board.

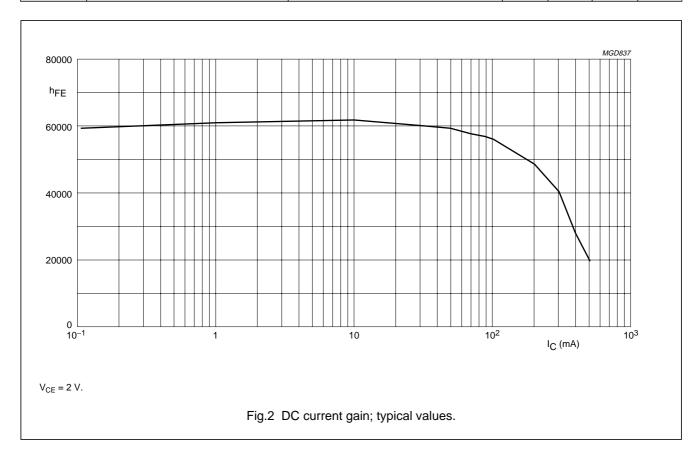
NPN Darlington transistor

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CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	PARAMETER CONDITIONS		TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 60 V; I _E = 0 A	_	_	50	nA
I _{CES}	collector-emitter cut-off current	$V_{BE} = 0 \text{ V}; V_{CE} = 60 \text{ V}$	_	_	50	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = 10 \text{ V}; I_C = 0 \text{ A}$	-	_	50	nA
h _{FE}	DC current gain	V _{CE} = 5 V; see Fig.2				
		$I_C = 1 \text{ mA}$	2000	_	_	
		I _C = 10 mA	4000	_	_	
		$I_C = 200 \text{ mA}$	10000	_	70000	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 200 \text{ mA}; I_B = 0.2 \text{ mA}$	-	_	1.1	V
V _{BEsat}	base-emitter saturation voltage	$I_C = 200 \text{ mA}; I_B = 0.2 \text{ mA}$	-	_	1.6	٧
C _c	collector capacitance	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}$	-	3.5	_	pF
f _T	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 500 \text{ mA}; f = 100 \text{ MHz}$	155	_	_	MHz



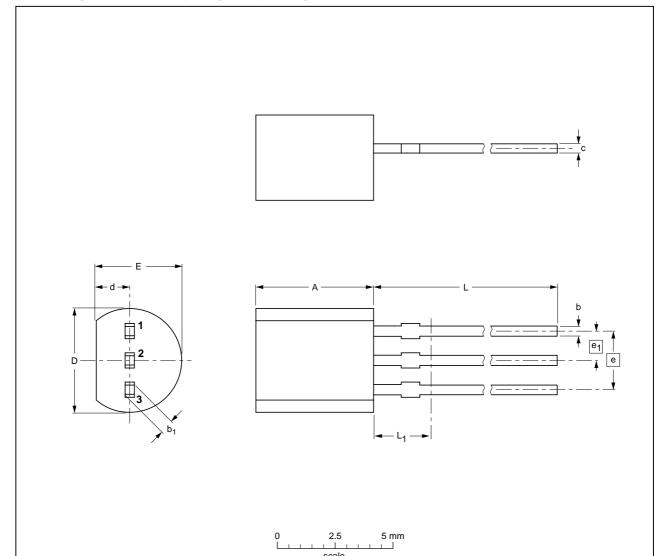
NPN Darlington transistor

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max.
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	1330E DATE
SOT54		TO-92	SC-43A			97-02-28 04-06-28

NPN Darlington transistor

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DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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