# 1 Product profile

#### 1.1 General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

**Table 1. Product overview** 

Type number	Package	NPN		
	Nexperia	JEDEC	JEITA	complement
BC807	SOT23	TO-236AB	-	BC817
BC807-16				BC817-16
BC807-25				BC817-25
BC807-40				BC817-40

#### 1.2 Features and benefits

- High current
- Three current gain selections
- AEC-Q101 qualified

## 1.3 Applications

· General-purpose switching and amplification



#### 1.4 Quick reference data

Table 2. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base		-	-	-45	V
Ic	collector current			-	-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		_	-	-1	Α
h <sub>FE</sub>	DC current gain	$V_{CE} = -1 \text{ V}; I_{C} = -100 \text{ mA}$					
	BC807		[1]	100	-	600	
	BC807-16		[1]	100	-	250	
	BC807-25		[1]	160	-	400	
	BC807-40		[1]	250	-	600	

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# 2 Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
SOT23	'			
1	В	base		
2	E	emitter	3	C
3	С	collector		В
				   E
			1	sym132

# 3 Ordering information

**Table 4. Ordering information** 

Type number	Package					
	Name	Description	Version			
BC807	TO-236AB	Plastic surface-mounted package; 3 leads	SOT23			
BC807-16						
BC807-25						
BC807-40						

# **Marking**

#### Table 5. Marking

Type number		Marking code
BC807	[1]	5D%
BC807-16	[1]	5A%
BC807-25	[1]	5B%
BC807-40	[1]	5C%

<sup>[1] % =</sup> placeholder for manufacturing site code

# **Limiting values**

#### Table 6. Limiting values

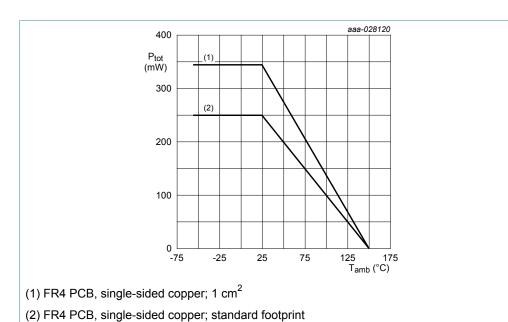
In accordance with the Absolute Maximum Rating System (IEC 60134).

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-50	V
$V_{CEO}$	collector-emitter voltage	open base		-	-45	V
$V_{EBO}$	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-1	Α
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	250	mW
			[3] [2]	-	345	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

 <sup>[2]</sup> Valid for all available selection groups.
 [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



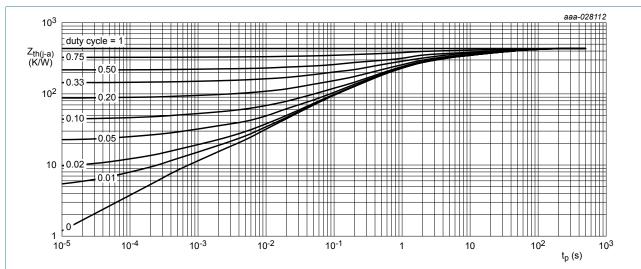
#### Thermal characteristics

#### **Table 7. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

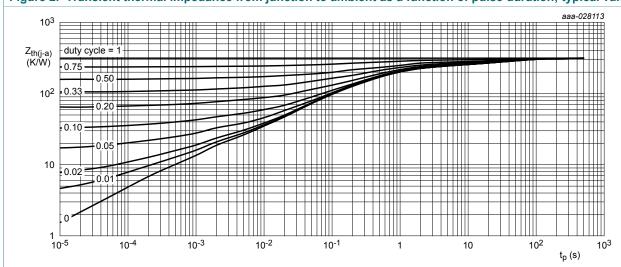
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	· · · · · · · · · · · · · · · · · · ·	in free air	[1] [2]	-	-	500	K/W
to ambient	to ambient		[3] [2]	-	-	362	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Valid for all available selection groups.
- [2] Valid for all available selection groups.
   [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



FR4 PCB; single-sided copper; tin-plated and standard footprint

Figure 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>

Figure 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

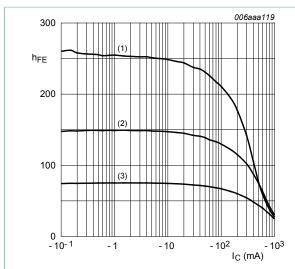
## **Characteristics**

#### **Table 8. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	I <sub>C</sub> = -100 μA; I <sub>E</sub> = 0 A		-50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_C = -10 \text{ mA}; I_B = 0 \text{ A}$		-45	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_E = -100 \ \mu A; \ I_C = 0 \ A$		-5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A		-	-	-100	nA
	cut-off current	$V_{CB}$ = -20 V; $I_{E}$ = 0 A; $T_{j}$ = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A		-	-	-100	nA
h <sub>FE</sub>	DC current gain					<u> </u>	
	BC807	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	100	-	600	
	BC807-16	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	100	-	250	
	BC807-25	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	160	-	400	
	BC807-40	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	250	-	600	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	[1]	-	-	-700	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA	[1] [2]	-	-	-1.2	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz		80	-	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	5	-	pF

 $<sup>\</sup>begin{array}{ll} [1] & \text{pulsed; } t_p \leq 300 \ \mu s; \ \delta \leq 0.02 \\ [2] & V_{BE} \ decreases \ by \ approxymately \ 2 \ mV/K \ with \ increasing \ temperature. \end{array}$ 



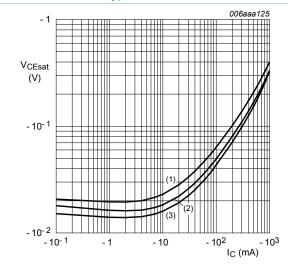
$$V_{CE} = -1 V$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = -55 °C$$

Figure 4. BC807-16: DC current gain as a function of collector current; typical values



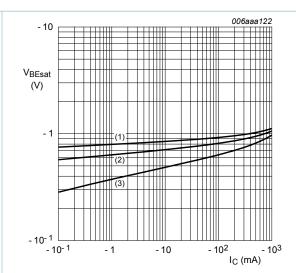
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Figure 6. BC807-16: Collector-emitter saturation voltage as a function of collector current; typical values



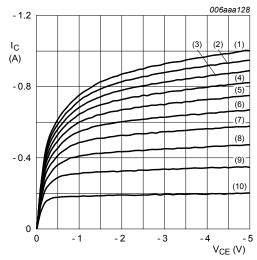
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Figure 5. BC807-16: Base-emitter saturation voltage as a function of collector current; typical values



(1) 
$$I_B = -16.0 \text{ mA}$$

(2) 
$$I_B = -14.4 \text{ mA}$$

(3) 
$$I_B = -12.8 \text{ mA}$$

(4) 
$$I_B = -11.2 \text{ mA}$$

$$(5) I_B = -9.6 \text{ mA}$$

(6) 
$$I_B = -8.0 \text{ mA}$$

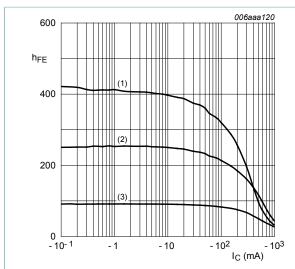
$$(7) I_B = -6.4 \text{ mA}$$

(8) 
$$I_B = -4.8 \text{ mA}$$

(9) 
$$I_B = -3.2 \text{ mA}$$

$$(10) I_B = -1.6 mA$$

Figure 7. BC807-16: Collector current as a function of collector-emitter voltage; typical values



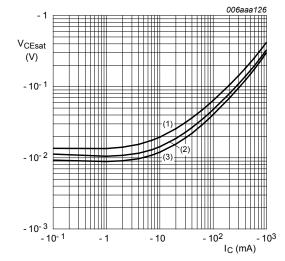
$$V_{CE} = -1 V$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Figure 8. BC807-25: DC current gain as a function of collector current; typical values



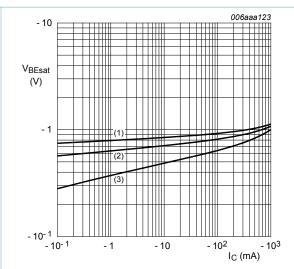
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Figure 10. BC807-25: Collector-emitter saturation voltage as a function of collector current; typical values



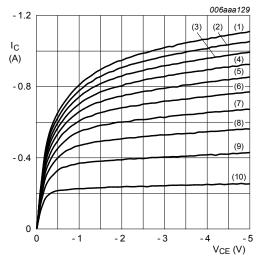
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Figure 9. BC807-25: Base-emitter saturation voltage as a function of collector current; typical values



T<sub>amb</sub> = 25 °C

(1) 
$$I_B = -13.0 \text{ mA}$$

(2) 
$$I_B = -11.7 \text{ mA}$$

(3) 
$$I_B = -10.4 \text{ mA}$$

(4) 
$$I_B = -9.1 \text{ mA}$$

$$(5) I_B = -7.8 \text{ mA}$$

(6) 
$$I_B = -6.5 \text{ mA}$$

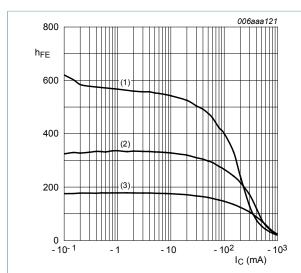
$$(7) I_B = -5.2 \text{ mA}$$

(8) 
$$I_B = -3.9 \text{ mA}$$

(9) 
$$I_B = -2.6 \text{ mA}$$

$$(10) I_B = -1.3 \text{ mA}$$

Figure 11. BC807-25: Collector current as a function of collector-emitter voltage; typical values



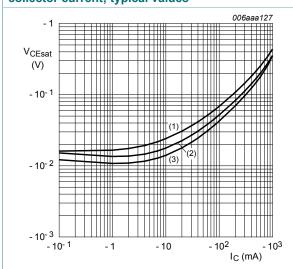
$$V_{CE} = -1 V$$

(1) 
$$T_{amb}$$
 = 150 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Figure 12. BC807-40: DC current gain as a function of collector current; typical values



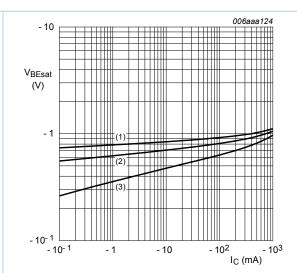
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Figure 14. BC807-40: Collector-emitter saturation voltage as a function of collector current; typical values



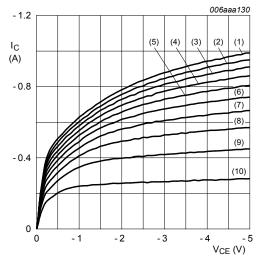
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Figure 13. BC807-40: Base-emitter saturation voltage as a function of collector current; typical values



(1) 
$$I_B = -12.0 \text{ mA}$$

(2) 
$$I_B = -10.8 \text{ mA}$$

(3) 
$$I_B = -9.6 \text{ mA}$$

(4) 
$$I_B = -8.4 \text{ mA}$$

$$(5) I_B = -7.2 \text{ mA}$$

(6) 
$$I_B = -6.0 \text{ mA}$$

$$(7) I_B = -4.8 \text{ mA}$$

(8) 
$$I_B = -3.6 \text{ mA}$$

(9) 
$$I_B = -2.4 \text{ mA}$$

$$(10) I_B = -1.2 mA$$

Figure 15. BC807-40: Collector current as a function of collector-emitter voltage; typical values

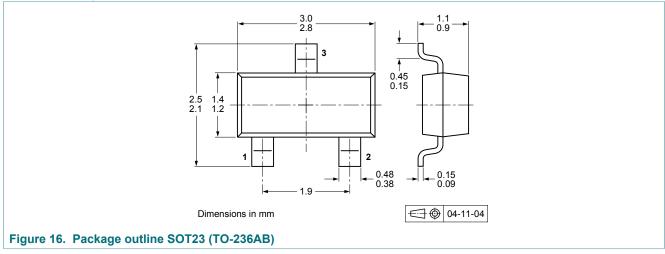
## 8 Test information

## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

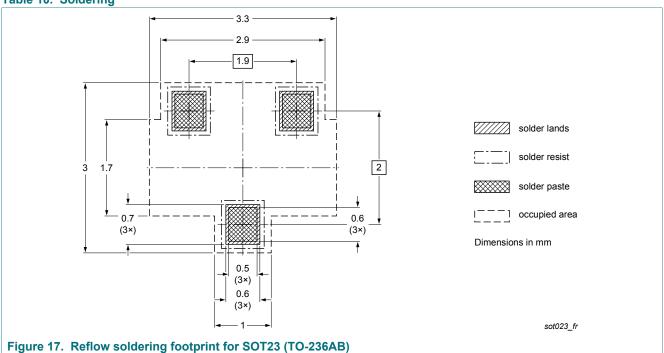
# 9 Package outline

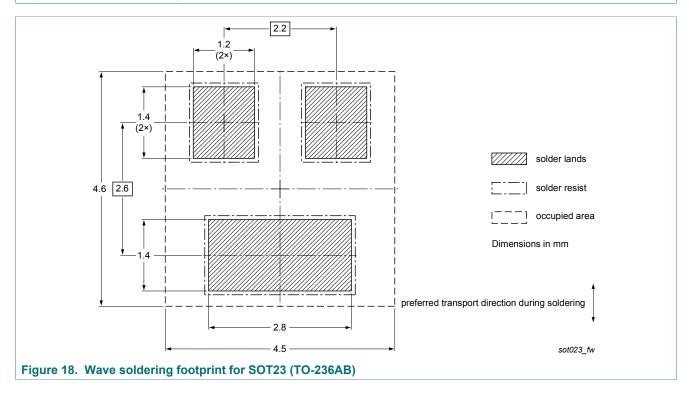




# 10 Soldering

Table 10. Soldering





# 11 Revision history

#### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BC807_SER v.7	20180615	Product data sheet	-	BC807_BC807W_BC327 v.6		
Modifications:	guidelines Legal tex Removed Added Figure Hermal g Graphs ir Added se	s of Nexperia.  Its have been adapted to basic types: BC327 ar gures: Fig 1. Power der raphs as Fig 2. and Fig section "Characteristic ctions 8 "Test informati I Section "Packing infor	thas been redesigned to comply with the identity  ed to the new company name where appropriate.  7 and BC807W (separate data sheet).  derating curves in section "Limiting values" and the Fig 3. in section "Thermal characteristics".  ristics" are sorted in new order.  mation" and 9 "Soldering".			
BC807_BC807W_BC327 v.6	20091117	Product data sheet	-	BC807_BC807W_BC327 v.5		
BC807_BC807W_BC327 v.5	20050221	Product data sheet	CPCN200302007F CPCN200405006F	BC807 v.4; BC807W v.3; BC327 v.3		
BC807 v.4	20040116	Product Specification	-	BC807 v.3		
BC807W v.3	19990518	Product Specification	-	BC807W_808W_CNV v.2		

## 12 Legal information

#### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- The term 'short data sheet' is explained in section "Definitions". [2] [3]
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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Nexperia BC807 series

#### 45 V, 500 mA PNP general-purpose transistors

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

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#### 12.4 Trademarks

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# **BC807** series

# 45 V, 500 mA PNP general-purpose transistors

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Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.