45 V, 500 mA PNP general-purpose transistors
Rev. 1 — 5 January 2018

Product data sheet

Product profile 1

1.1 General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) or SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package					
	Nexperia	JEITA	JEDEC			
BC807-16L	SOT23	-	TO-236AB			
BC807-25L						
BC807-40L						
BC807-16LW	SOT323	SC70	-			
BC807-25LW						
BC807-40LW						

1.2 Features and benefits

- High current
- Three current gain selections
- AEC-Q101 qualified

1.3 Applications

· General-purpose switching and amplification

1.4 Quick reference data

Table 2. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-1	Α



Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -100 mA					
	BC807-16L; BC807-16LW		[1]	100	-	250	-
	BC807-25L; BC807-25LW		[1]	160	-	400	-
	BC807-40L; BC807-40LW		[1]	250	-	600	-

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$

2 Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
SOT23				
1	В	base		
2	Е	emitter	3	C
3	С	collector	1 2	B E sym132
SOT323				
1	В	base		
2	Е	emitter	3	C
3	С	collector	1 2	B — E sym132

3 Ordering information

Table 4. Ordering information

Type number	Package	Package						
	Name	Description	Version					
BC807-16L	TO-236AB	Plastic surface-mounted package; 3 leads	SOT23					
BC807-25L								
BC807-40L								
BC807-16LW	SC70		SOT323					
BC807-25LW								
BC807-40LW								

4 Marking

Table 5. Marking

	Marking code
[1]	HL%
[1]	HM%
[1]	HN%
[1]	C3%
[1]	C4%
[1]	C5%
	[1] [1] [1]

^{[1] % =} placeholder for manufacturing site code

5 Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-50	V
V_{CEO}	collector-emitter voltage	open base		-	-45	V
V_{EBO}	emitter-base voltage	open collector		-	-7	V
I _C	collector current			-	-500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		-1	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-200	mA
P _{tot}	total power dissipation BC807L (SOT23)	T _{amb} ≤ 25 °C	[1]	-	250	mW
	total power dissipation BC807LW (SOT323)		[1]	-	200	mW

Symbol	Parameter	Conditions	Min	Max	Unit
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	150	°C
T _{stg}	storage temperature		-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

6 Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient SOT23 in free air	[1]	-	-	500	K/W	
	thermal resistance from junction to ambient SOT323		[1]	-	-	625	K/W

^[1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.

7 Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -10 mA; I _B = 0 A	$I_C = -10 \text{ mA}; I_B = 0 \text{ A}$		-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -100 μA; I _C = 0 A		-7	-	-	V
I _{CBO}	collector-base	V _{CB} = -40 V; I _E = 0 A		-	-	-100	nA
	cut-off current	V_{CB} = -40 V; I_{E} = 0 A; T_{j} = 150 °C		-	-	-5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A		-	-	-100	nA
h _{FE}	DC current gain				-	'	
	BC807-16L, BC807-16LW	V _{CE} = -1 V; I _C = -100 mA	[1]	100	-	250	
	BC807-25L, BC807-25LW		[1]	160	-	400	
	BC807-40L, BC807-40LW		[1]	250	-	600	
	DC current gain	V _{CE} = -1 V; I _C = -500 mA	[1]	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$		-	-	-700	mV
V _{BE}	base-emitter voltage	$V_{CE} = -1 \text{ V; } I_{C} = -500 \text{ mA}$	[1]	-	-	-1.2	V

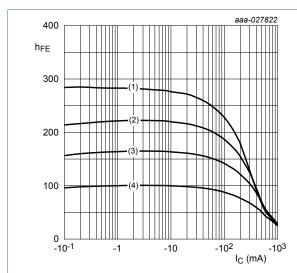
BC807L_BC807LW

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
f _T	transition frequency	$V_{CE} = -5 \text{ V}; I_{C} = -10 \text{ mA}; f = 100 \text{ MHz}$	80	-	-	MHz
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	-	5.5	-	pF

[1] pulsed; $t_p \le 300 \,\mu s$; $\delta \le 0.02$



 $V_{CE} = -1 V$

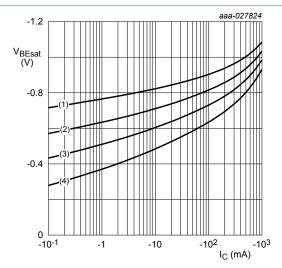
(1) T_{amb} = 150 °C

(2) T_{amb} = 85 °C

(3) $T_{amb} = 25 \, ^{\circ}C$

(4) $T_{amb} = -40 \, ^{\circ}C$

Figure 1. BC807-16L, BC807-16LW: DC current gain as a Figure 2. BC807-16L, BC807-16LW: Base-emitter function of collector current; typical values



IC/IB = 10

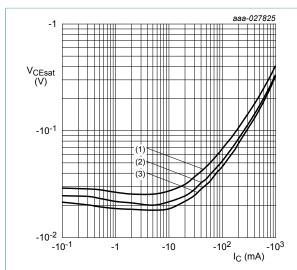
(1) $T_{amb} = -40 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

 $(3) T_{amb} = 85 °C$

(4) T_{amb} = 150 °C

saturation voltage as a function of collector current; typical values



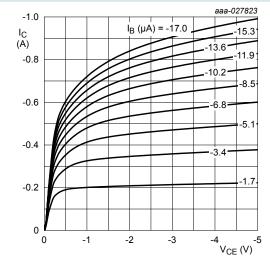
IC/IB = 10

(1) T_{amb} = 150 °C

(2) $T_{amb} = 25 \, ^{\circ}C$

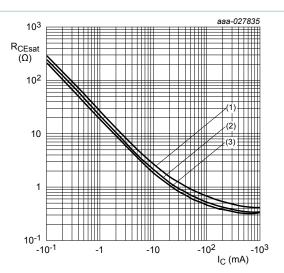
 $(3) T_{amb} = -40 °C$

Figure 3. BC807-16L, BC807-16LW: Collector-emitter saturation voltage as a function of collector current; typical values



T_{amb} = 25 °C

Figure 5. BC807-16L, BC807-16LW: Collector current as a function of collector-emitter voltage; typical values



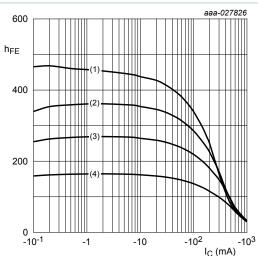
IC/IB = 10

(1) $T_{amb} = 150 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = -40 \, ^{\circ}C$

Figure 4. BC807-16L, BC807-16LW: Collector-emitter saturation resistance as a function of collector current; typical values



 $V_{CE} = -1 V$

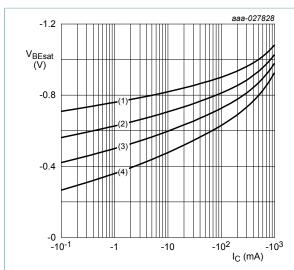
(1) $T_{amb} = 150 \, ^{\circ}C$

(2) T_{amb} = 85 °C

(3) $T_{amb} = 25 \, ^{\circ}C$

(4) $T_{amb} = -40 \, ^{\circ}C$

Figure 6. BC807-25L, BC807-25LW: DC current gain as a function of collector current; typical values



$$IC/IB = 10$$

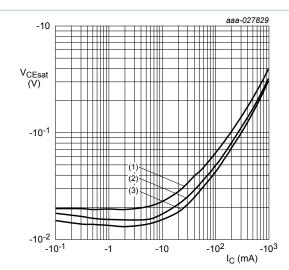
(1)
$$T_{amb} = -40 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = 85 °C$$

(4)
$$T_{amb}$$
 = 150 °C

Figure 7. BC807-25L, BC807-25LW: Base-emitter saturation voltage as a function of collector current; typical values

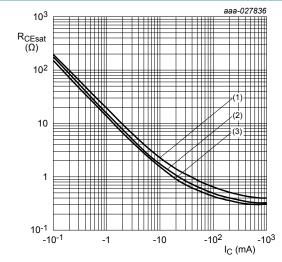


(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -40 \, ^{\circ}C$$

Figure 8. BC807-25L, BC807-25LW: Collector-emitter saturation voltage as a function of collector current; typical values



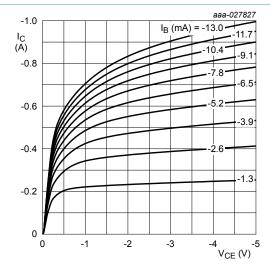
IC/IB = 10

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

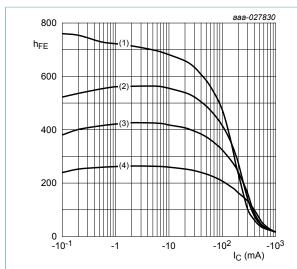
(3)
$$T_{amb}$$
 = -40 °C

Figure 9. BC807-25L, BC807-25LW: Collector-emitter saturation resistance as a function of collector current; typical values



 $T_{amb} = 25 \, ^{\circ}C$

Figure 10. BC807-25L, BC807-25LW: Collector current as a function of collector-emitter voltage; typical values



$$V_{CE} = -1 V$$

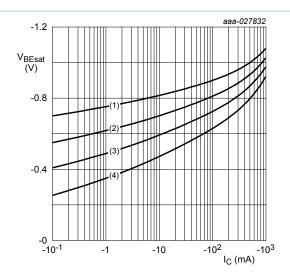
(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 85 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 25 °C

(4)
$$T_{amb} = -40 \, ^{\circ}C$$

Figure 11. BC807-40L, BC807-40LW: DC current gain as as a function of collector current; typical values



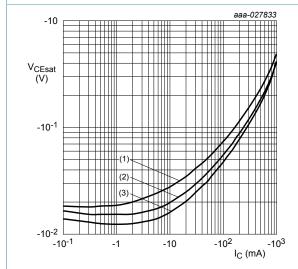
(1)
$$T_{amb} = -40 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 85 °C

(4)
$$T_{amb} = 150 \, ^{\circ}C$$

Figure 12. BC807-40L, BC807-40LW: Base-emitter saturation voltage as a function of collector current; typical values



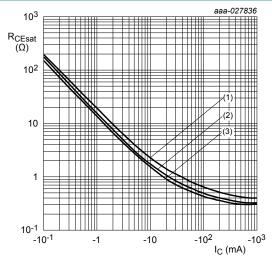
IC/IB = 10

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -40 \, ^{\circ}C$$

Figure 13. BC807-40L, BC807-40LW: Collector-emitter saturation voltage as a function of collector current; typical values

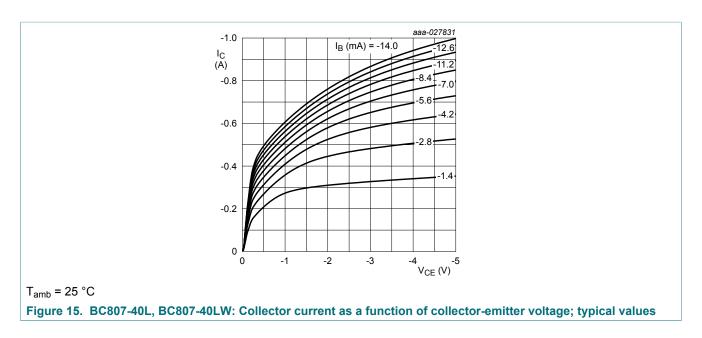


(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -40 \, ^{\circ}C$$

Figure 14. BC807-40L, BC807-40LW: Collector-emitter saturation resistance as a function of collector current; typical values



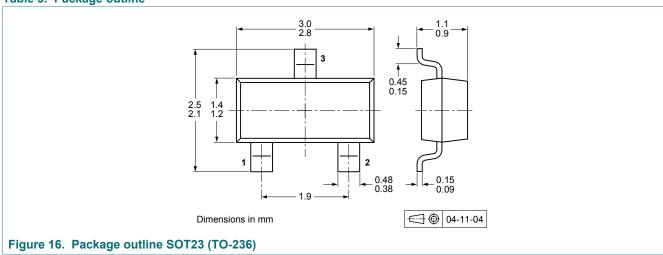
8 Test information

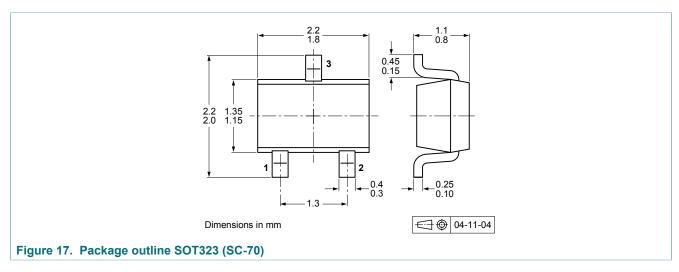
8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

9 Package outline

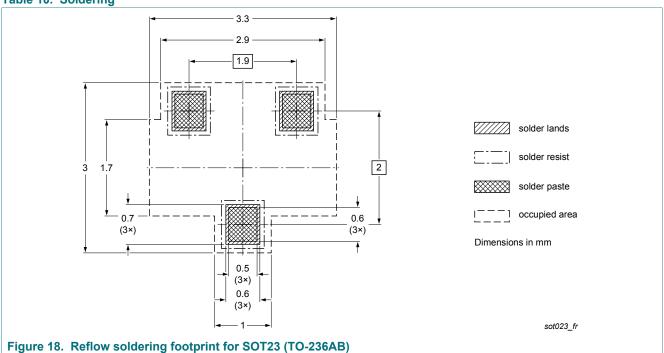
Table 9. Package outline

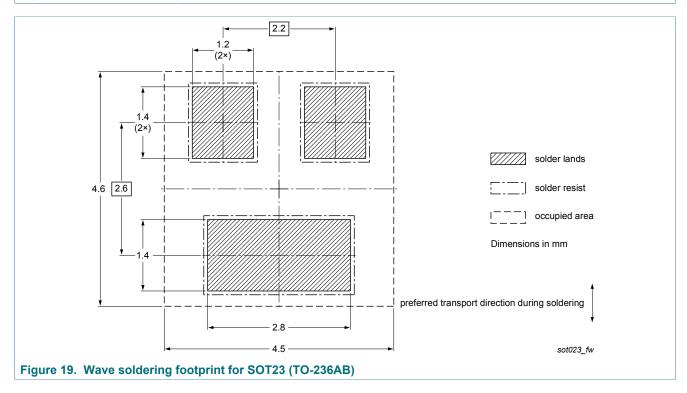


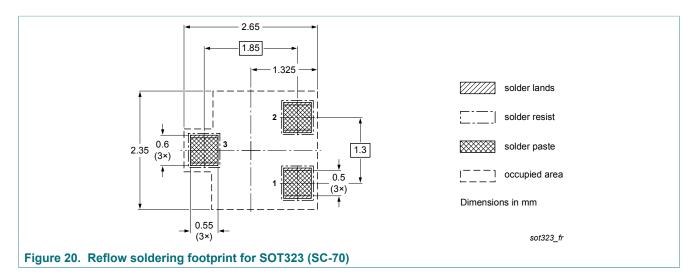


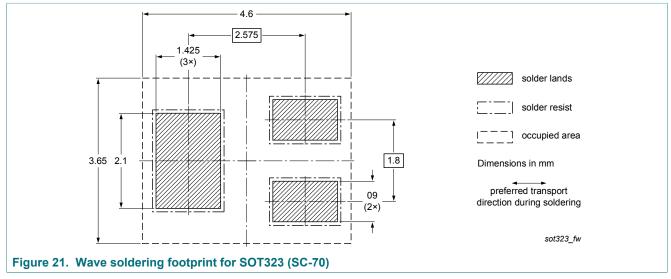
10 Soldering

Table 10. Soldering









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11 Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC807L_BC807LW v.1	20180105	Product data sheet	-	-

12 Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition	
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.	
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.	
Product [short] data sheet	Production	This document contains the product specification.	

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