**BC817K series** 45 V, 500 mA NPN general-purpose transistors Rev. 2 — 6 March 2018

**Product data sheet** 

#### **Product profile** 1

## **1.1 General description**

NPN general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

#### Table 1. Product overview

Type number	Package	PNP complement	
	Nexperia	JEDEC	
BC817K-16	SOT23	TO-236AB	BC807K-16
BC817K-25			BC807K-25
BC817K-40			BC807K-40

## 1.2 Features and benefits

- Three current gain selections
- · High power dissipation capability
- AEC-Q101 qualified

## **1.3 Applications**

· General-purpose switching and amplification



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## 1.4 Quick reference data

#### Table 2. Quick reference data

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	1	А
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA					
	BC817K-16		[1]	100	-	250	-
	BC817K-25		[1]	160	-	400	-
	BC817K-40		[1]	250	-	600	-

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# 2 **Pinning information**

Table 3. Pinning							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	В	base					
2	E	emitter		C			
3	С	collector		B E sym123			

# **3** Ordering information

# Table 4. Ordering information Type number Package Name Description Version BC817K-16 TO-236AB Plastic surface-mounted package; 3 leads SOT23 BC817K-25 BC817K-40 Version Plastic surface-mounted package; 3 leads SOT23

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#### Marking 4

Table 5. Marking		
Type number		Marking code
BC817K-16	[1]	HD%
BC817K-25	[1]	HE%
BC817K-40	[1]	HF%

[1] % = placeholder for manufacturing site code

#### **Limiting values** 5

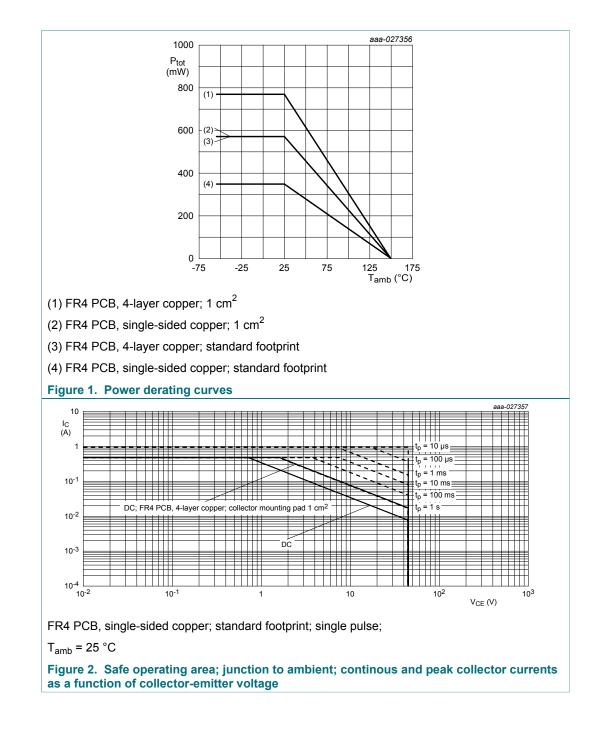
#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Conditions		Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	open emitter		50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current				500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	single pulse; t <sub>p</sub> ≤ 1 ms		1	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms	single pulse; t <sub>p</sub> ≤ 1 ms		200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	350	mW
			[2]	-	575	mW
			[3]	-	575	mW
			[4]	-	775	mW
Т <sub>ј</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
 Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
 Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint.
 Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

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#### **Thermal characteristics** 6

#### Table 7. Thermal characteristics

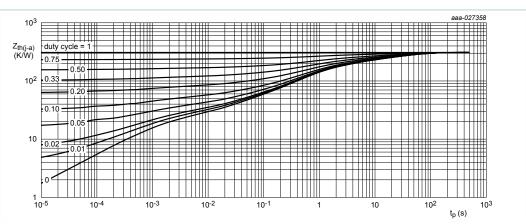
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from to ambient	· · · · · · · <b>,</b> · · · ·	[]	[1]	-	-	358	K/W
	to ambient		[2]	-	-	218	K/W
			[3]	-	-	218	K/W
			[4]	-	-	162	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	60	K/W

Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint. [1]

Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint. [2]

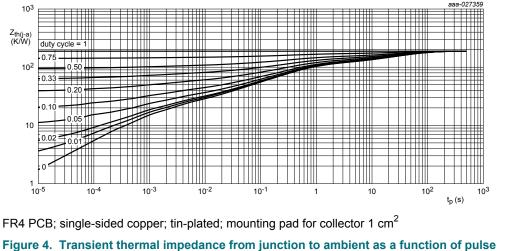
[4]

Device mounted on an FR4 PCB; 4-layer copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

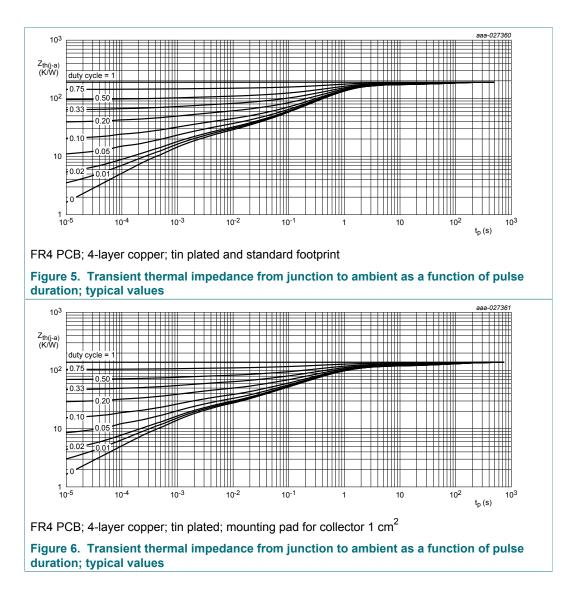


FR4 PCB; single-sided copper; tin-plated and standard footprint

Figure 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



duration; typical values



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# 7 Characteristics

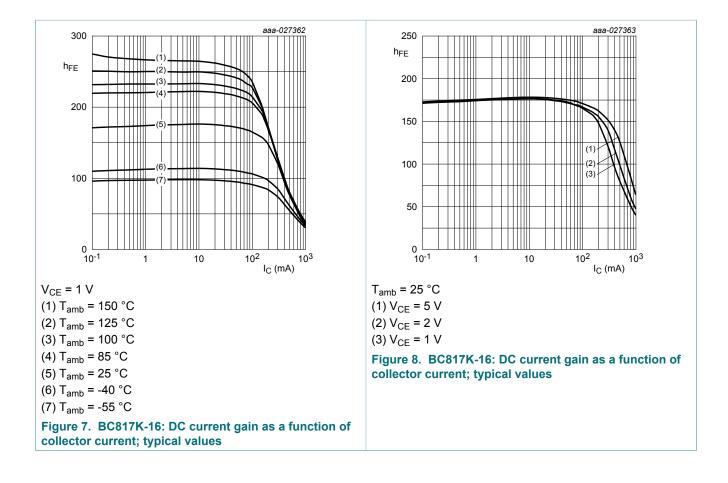
#### Table 8. Characteristics

 $T_{amb}$  = 25 °C unless otherwise specified.

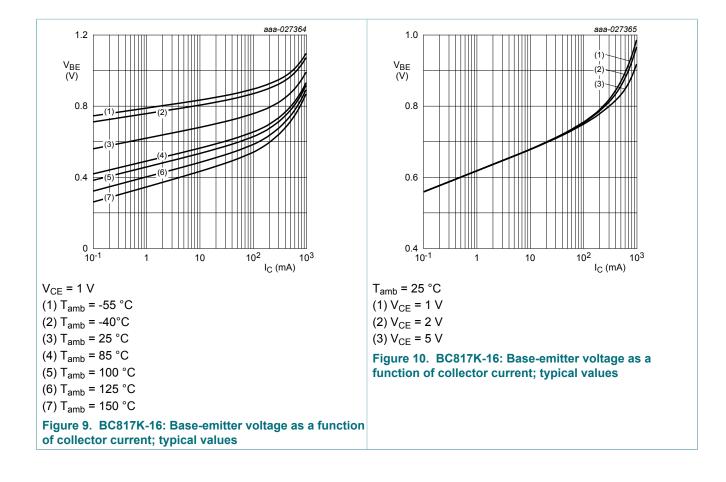
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0 A 4		45	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A		5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	$V_{CB}$ = 25 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A		-	-	100	nA
h <sub>FE</sub>	DC current gain	·					
	BC817K-16	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	100	-	250	
	BC817K-25	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	160	-	400	
	BC817K-40	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	250	-	600	
	BC817K-16, -25, -40	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	700	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	1.2	V
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	-	-	1.2	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	3	-	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = 0.5 V; I <sub>C</sub> = i <sub>c</sub> = 0 A; f = 1 MHz					
	BC817K-16			-	44	-	pF
	BC817K-25			-	39	-	pF
	BC817K-40	-		-	39	-	pF
	- I	1					

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

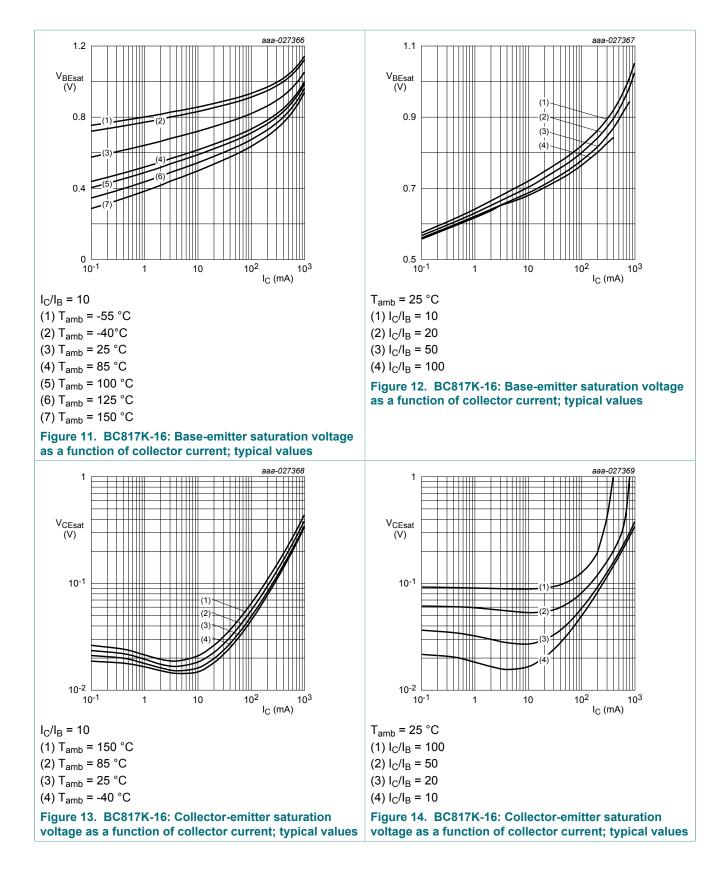
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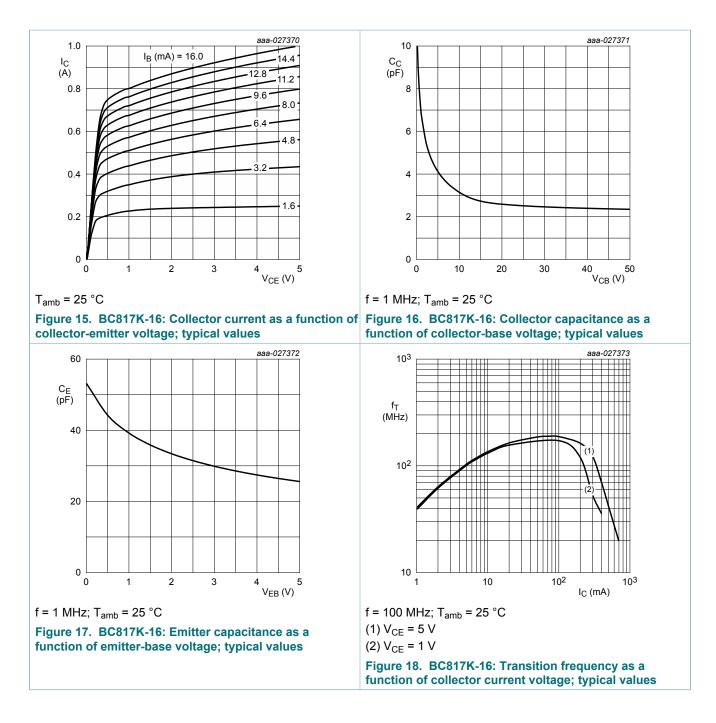
# **BC817K series**



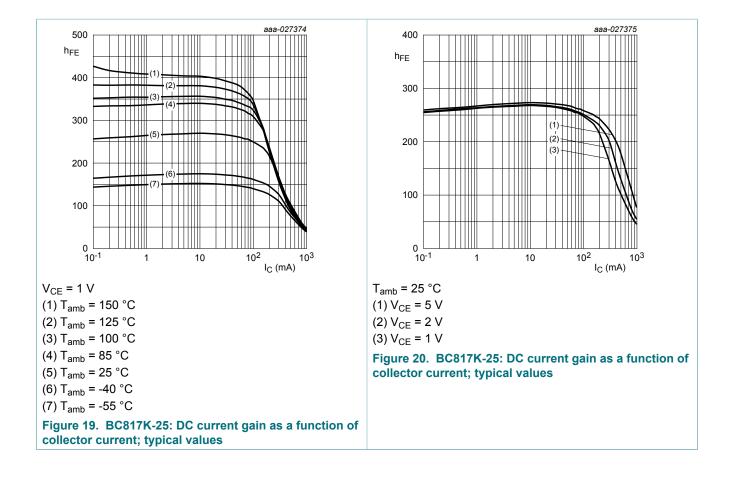
# **BC817K series**



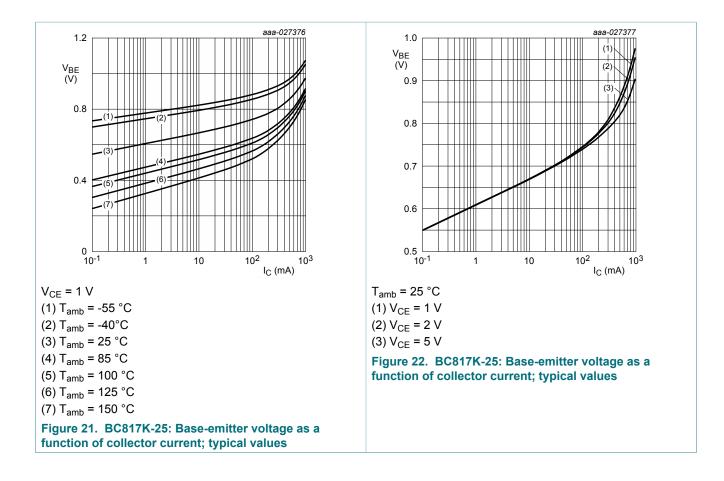
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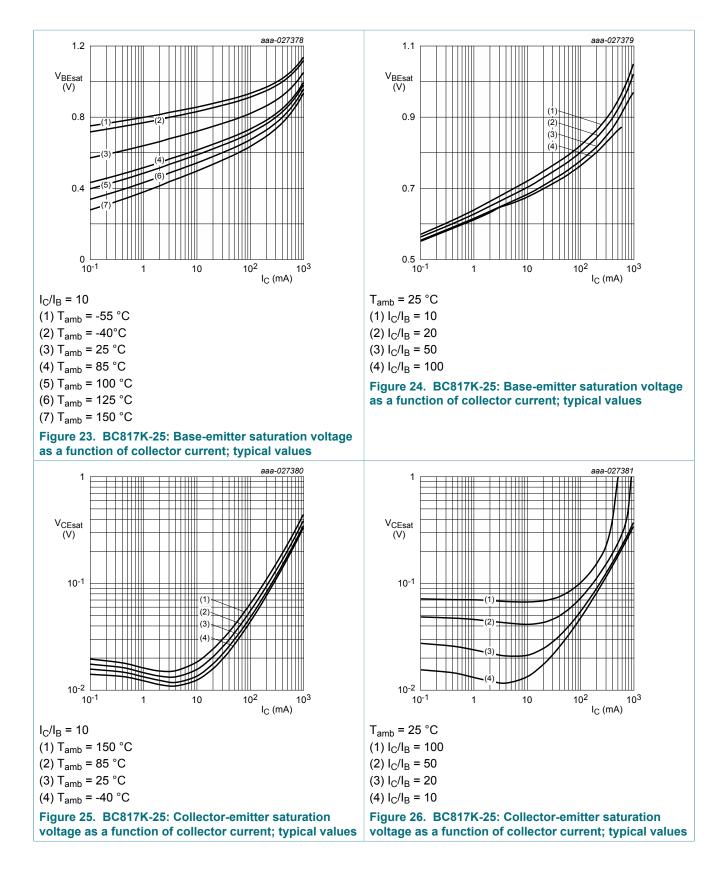
# **BC817K series**



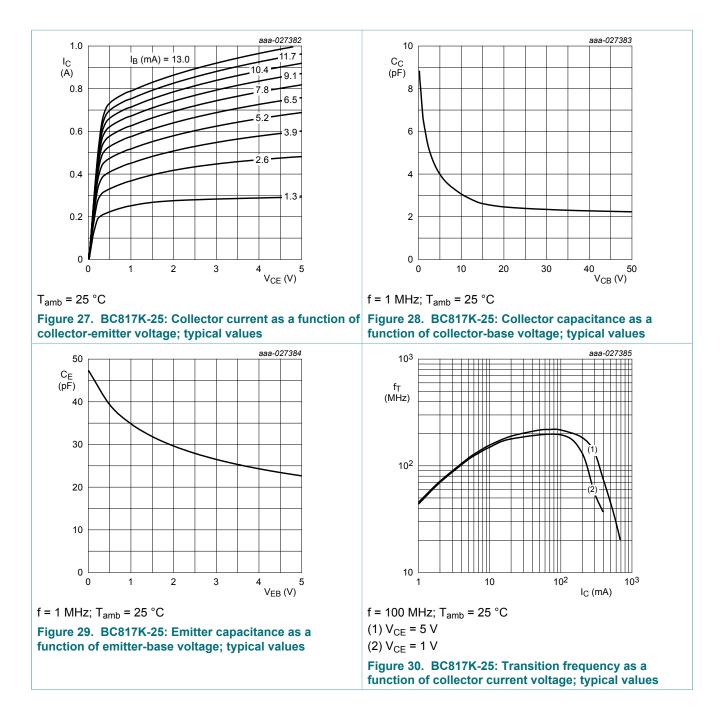
# **BC817K series**



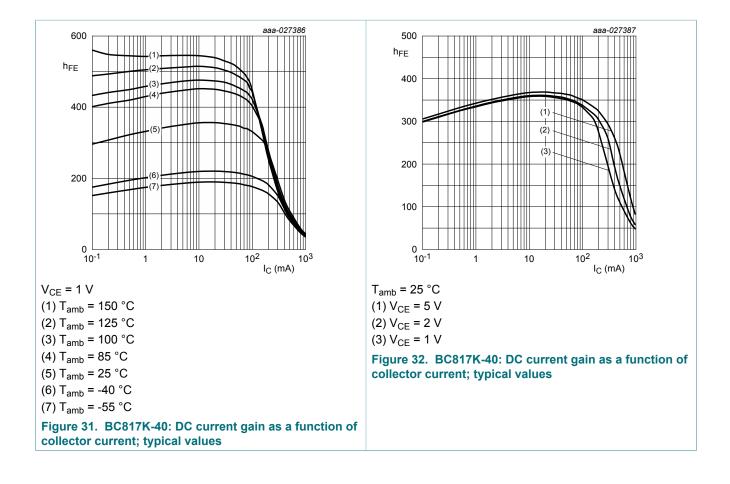
# **BC817K series**



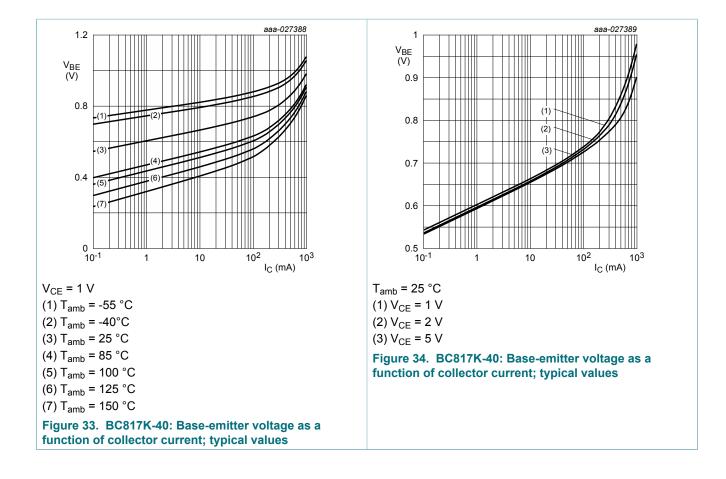
# **BC817K series**



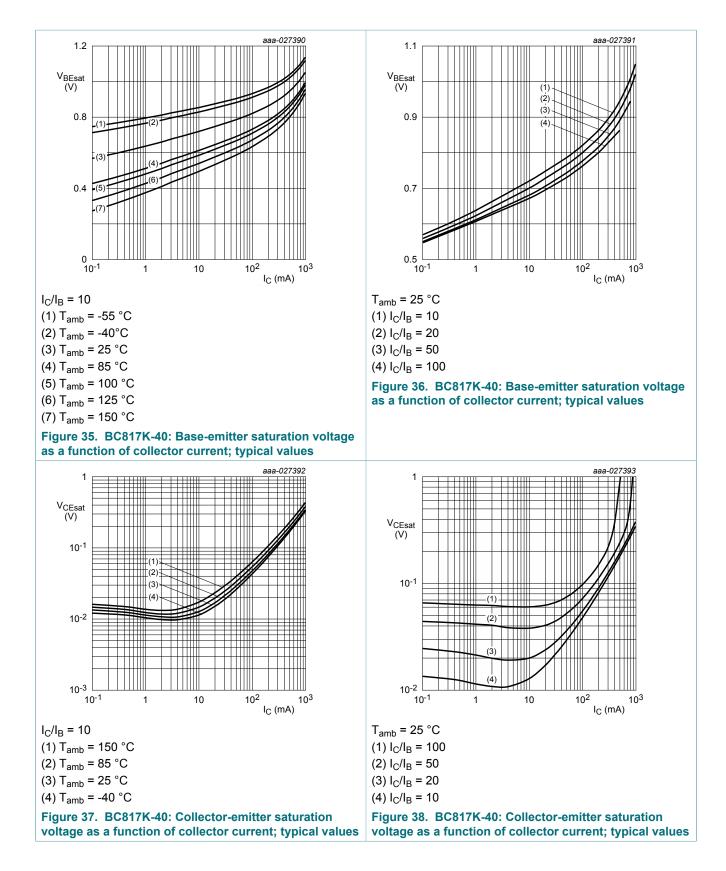
# **BC817K series**



# **BC817K series**

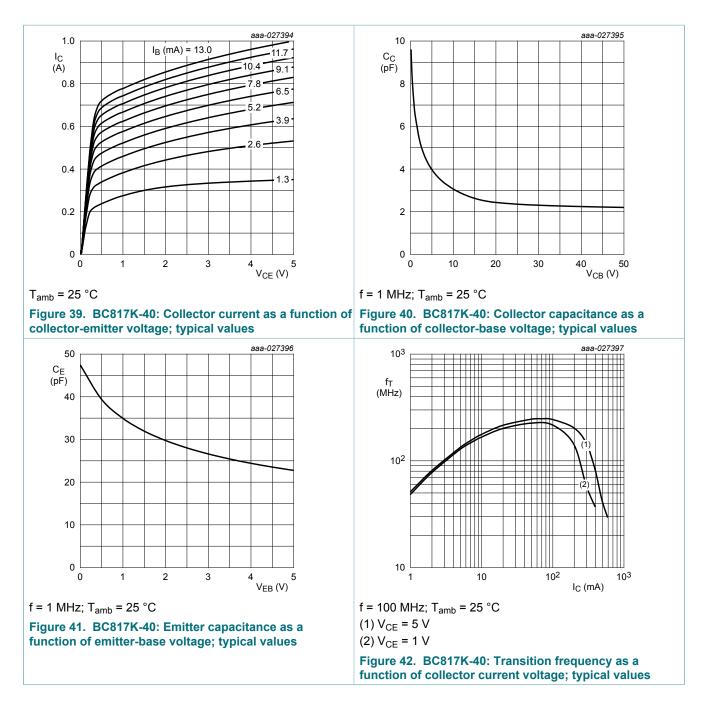


# **BC817K series**



# **BC817K series**

#### 45 V, 500 mA NPN general-purpose transistors



# 8 Test information

## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

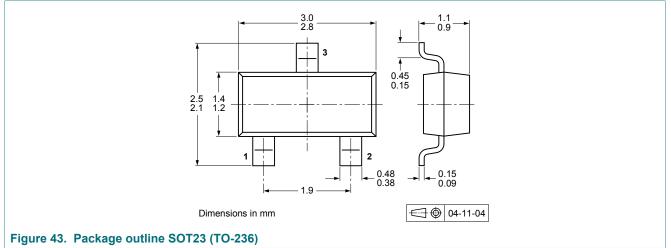
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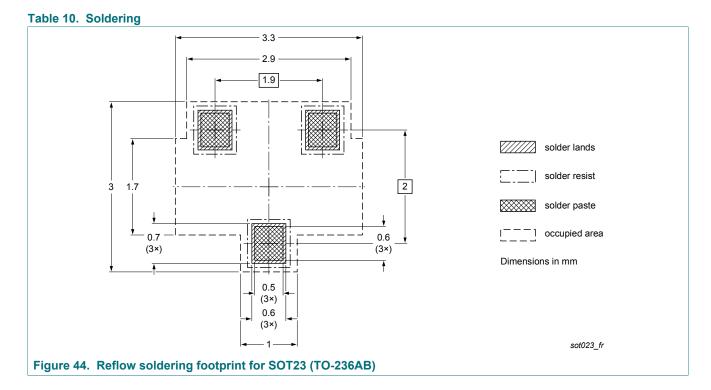
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# 9 Package outline

#### Table 9. Package outline

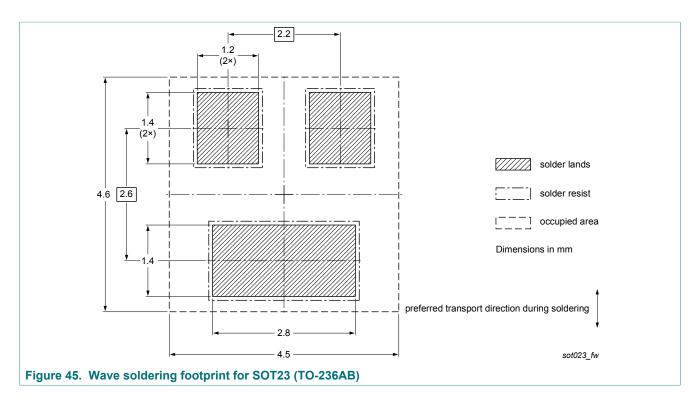


# **10 Soldering**



# **BC817K series**

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# **11 Revision history**

## Table 11. Revision history

Document ID	Release date	Data sheet status		Supersedes			
BC817K_SER v.2	20180306	Product data sheet		BC817K_SER v.1			
Modification:	Characteristics: Figures are updated						
BC817K_SER v.1	20171108			-			

#### 45 V, 500 mA NPN general-purpose transistors

# 12 Legal information

## 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

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BC817K SER **Product data sheet** 

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# **BC817K series**

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