

60 V, 100 mA PNP general-purpose transistor 19 August 2015

Product data sheet

1. General description

PNP general-purpose transistor in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

NPN complement: BC846BMB.

2. Features and benefits

- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Power dissipation comparable to SOT23
- AEC-Q101 qualified

3. Applications

- General-purpose switching and amplification
- Mobile applications

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-60	V
I _C	collector current			-	-	-100	mA
h _{FE}	DC current gain	V_{CE} = -5 V; I _C = -2 mA; T _{amb} = 25 °C		220	-	475	

5. Pinning information

Table 2.	Pinning	information			
Pin	Symbol	Description	Simplified outline	Graphic symbol	
1	В	base	1	3	
2	E	emitter	2 Transparent top view DFN1006B-3 (SOT883B)		1-1
3	С	collector		2 sym013	

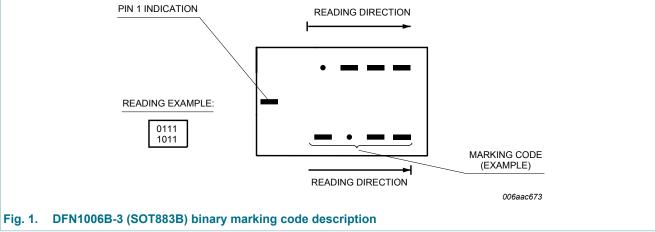


6. Ordering information

Table 3.Ordering in	formation			
Type number	Package			
	Name	Description	Version	
BC856BMB	DFN1006B-3	DFN1006B-3: leadless ultra small plastic package; 3 solder lands; body $1.0 \times 0.6 \times 0.37$ mm	SOT883B	

7. Marking

Table 4. Marking codes	
Type number	Marking code
BC856BMB	0101 1010



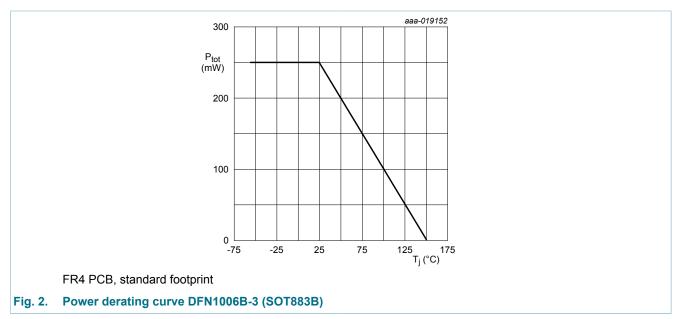
8. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-80	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-200	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

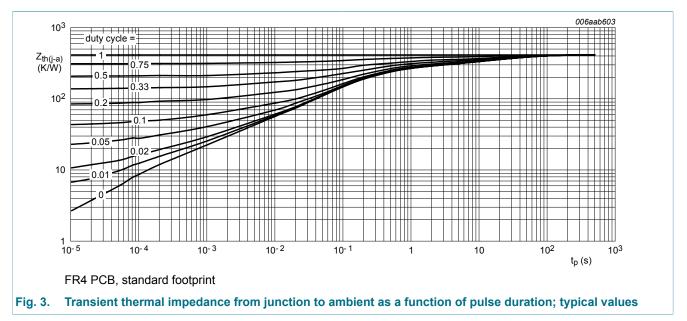
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



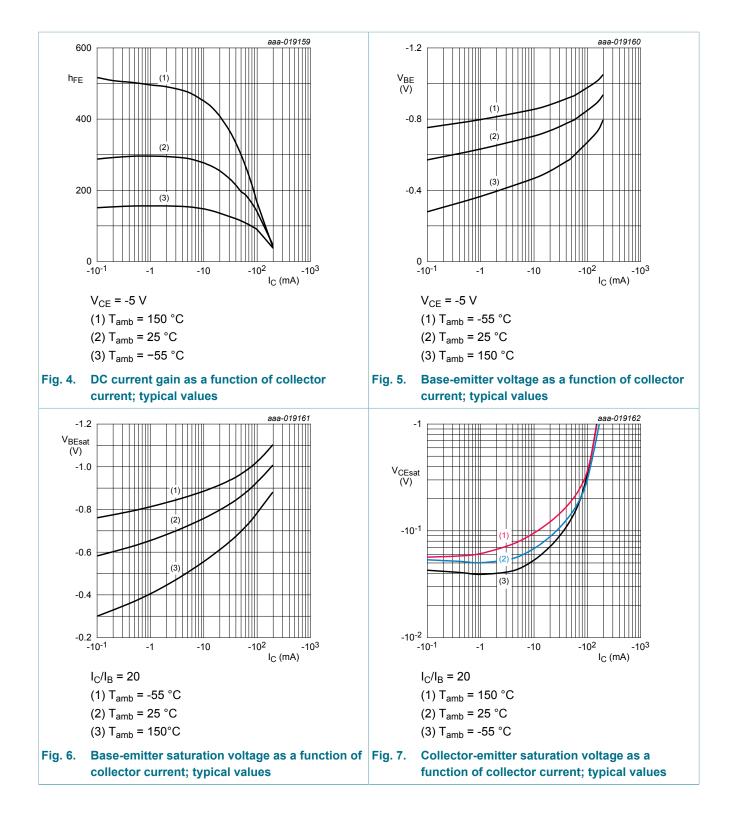
BC856BMB

60 V, 100 mA PNP general-purpose transistor

10. Characteristics

Table 7. (Characteristics					
Symbol	Parameter	Conditions	Mir	า Тур	Max	Unit
I _{CBO}	collector-base cut-off	V_{CB} = -30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-15	nA
	current	V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C	-	-	-5	μA
I _{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V_{CE} = -5 V; I _C = -2 mA; T _{amb} = 25 °C	22	0 -	475	
V _{CEsat}	collector-emitter	I_{C} = -10 mA; I_{B} = -0.5 mA; T_{amb} = 25 °C	-	-	-200	mV
	saturation voltage	I _C = -100 mA; I _B = -5 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-	-400	mV
DEGU	base-emitter saturation	I_{C} = -10 mA; I_{B} = -0.5 mA; T_{amb} = 25 °C	-	-700	-	mV
	voltage	I_{C} = -100 mA; I_{B} = -5 mA; T_{amb} = 25 °C	-	-850	-	mV
V _{BE}	base-emitter voltage	V_{CE} = -5 V; I _C = -2 mA; T _{amb} = 25 °C	-60	- 00	-750	mV
		V_{CE} = -5 V; I _C = -10 mA; T _{amb} = 25 °C	-	-	-820	mV
C _C	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	2.5	pF
C _E	emitter capacitance	V_{EB} = -0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	4.5	-	pF
f⊤	transition frequency	V_{CE} = -5 V; I _C = -10 mA; f = 100 MHz; T _{amb} = 25 °C	10	0 -	-	MHz
NF	noise figure	$\begin{split} V_{CE} &= -5 \; V; \; I_C = -200 \; \mu A; \; R_S = 2 \; k \Omega; \\ f &= 1 \; kHz; \; B = 200 \; Hz; \; T_{amb} = 25 \; ^{\circ} C \end{split}$	-	-	10	dB

60 V, 100 mA PNP general-purpose transistor



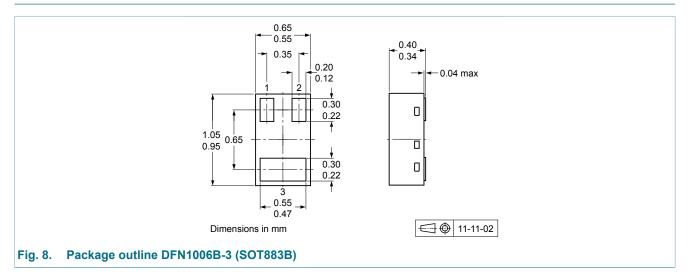
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11. Test information

11.1 Quality information

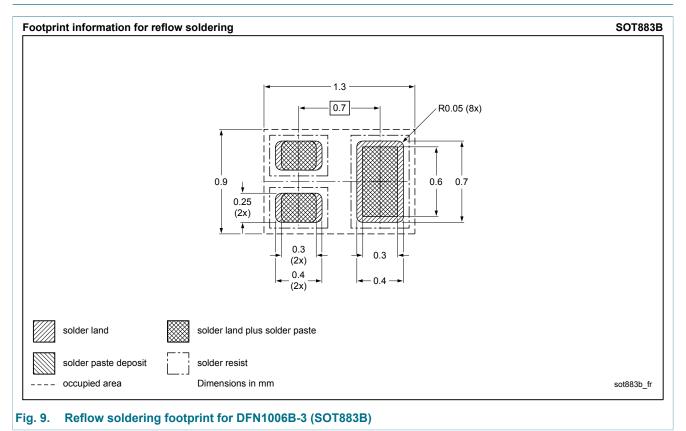
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



60 V, 100 mA PNP general-purpose transistor

13. Soldering



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC856BMB v.1	20150819	Product data sheet	-	-			

60 V, 100 mA PNP general-purpose transistor

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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60 V, 100 mA PNP general-purpose transistor

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60 V, 100 mA PNP general-purpose transistor

16. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	1
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Thermal characteristics	4
10	Characteristics	5
11	Test information	7
11.1	Quality information	7
12	Package outline	7
13	Soldering	8
14	Revision history	9
15	Legal information	10
15.1	Data sheet status	10
15.2	Definitions	10
15.3	Disclaimers	10
15.4	Trademarks	11

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