1. General description

PNP/PNP matched double transistors in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package.

NPN/NPN complement: BCM56DS

2. Features and benefits

- High collector current capability I_C and I_{CM}
- Reduces component count
- Reduces pick and place costs
- Current gain matching 5%
- · Application-optimized pinout
- AEC-Q101 qualified

3. Applications

- Current mirror
- Differential amplifier
- · Linear voltage regulators
- MOSFET drivers
- · High-side switches
- Power management
- Amplifiers

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per transistor	Per transistor							
V _{CEO}	collector-emitter voltage	open base		-	-	-80	V	
I _C	collector current			-	-	-1	Α	
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-2	Α	
h _{FE}	DC current gain	V_{CE} = -2 V; I_{C} = -150 mA; T_{amb} = 25 °C	[1]	63	-	250		
Per device								
h _{FE1} /h _{FE2}	DC current gain matching	$V_{CE} = -5 \text{ V}; I_{C} = -2 \text{ mA}; T_{amb} = 25 \text{ °C}$		0.95	1	1.05		



80 V, 1 A PNP/PNP matched double transistors

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{BE1} -V _{BE2}	base-emitter voltage matching		[2]	-	-	2	mV

- [1] Pulse test: $t_0 \le 300 \,\mu\text{s}$; $\delta \le 0.02$
- [2] The smaller of the two values is subtracted from the larger value.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol	
1	B1	base TR1	<u> </u>	C1 E1 E2	
2	B2	base TR2		TR1	
3	C2	collector TR2	<u>0</u>	TR2	
4	E2	emitter TR2	TSOP6 (SOT457)	TSOP6 (SOT457) B1 B2 C2	_
5	E1	emitter TR1		aaa-024630	
6	C1	collector TR1			

6. Ordering information

Table 3. Ordering information

Type number	Package	Package				
	Name	Description	Version			
BCM53DS	TSOP6	plastic, surface-mounted package (SC-74)	SOT457			

7. Marking

Table 4. Marking codes

Type number	Marking code
BCM53DS	3C

80 V, 1 A PNP/PNP matched double transistors

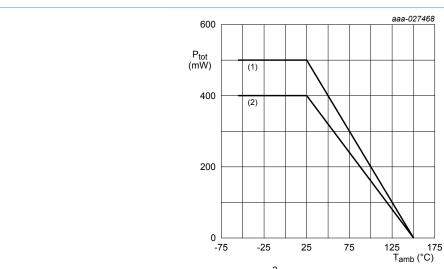
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or		'			
V _{CBO}	collector-base voltage	open emitter		-	-100	V
V _{CEO}	collector-emitter voltage	open base		-	-80	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-2	Α
I _{Blim}	limiting base current			-	-0.2	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-0.3	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	270	mW
			[2]	-	320	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	400	mW
			[2]	-	500	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 1 cm².



- (1) = FR4 PCB, single sided copper, 1 cm^2
- (2) = FR4 PCB, single sided copper, standard footprint

Per device: Power derating curves Fig. 1.

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor		,				
R _{th(j-a)}	thermal resistance	in free air	[1]	-	-	463	K/W
	from junction to ambient		[2]	-	-	391	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	150	K/W
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	313	K/W
			[2]	-	-	250	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm².

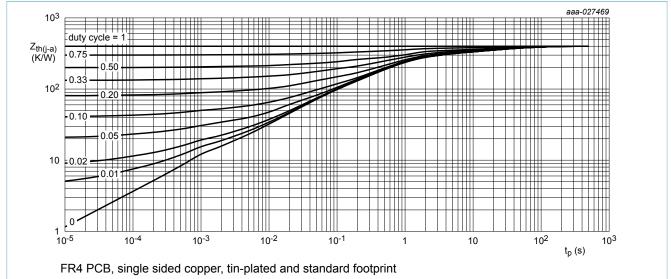
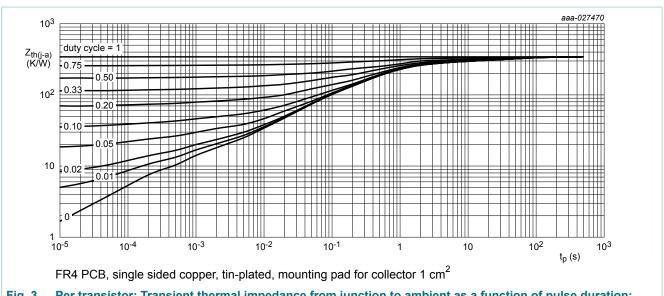


Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transisto	or						
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-100	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = -2 \text{ mA}; I_B = 0 \text{ A}$		-80	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _C = 0 A; I _E = -100 μA		-5	-	-	V
I _{CBO}	collector-base cut-off	V_{CB} = -30 V; I_{E} = 0 A; T_{amb} = 25 °C		-	-	-100	nA
	current	V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C		-	-	-10	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	-100	nA
h _{FE}	DC current gain	V_{CE} = -5 V; I_{C} = -2 mA; T_{amb} = 25 °C		63	-	-	
		V_{CE} = -2 V; I_{C} = -150 mA; T_{amb} = 25 °C	[1]	63	-	250	
		V_{CE} = -2 V; I_{C} = -500 mA; T_{amb} = 25 °C	[1]	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -500 mA; I_B = -50 mA; T_{amb} = 25 °C	[1]	-	-	-500	mV
V_{BE}	base-emitter voltage	V_{CE} = -2 V; I_{C} = -500 mA; T_{amb} = 25 °C	[1]	-	-	-1	V
C _c	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	7	-	pF
f _T	transition frequency	V_{CE} = -5 V; I_{C} = -50 mA; f = 100 MHz; T_{amb} = 25 °C		100	140	-	MHz
Per device							
h _{FE1} /h _{FE2}	DC current gain matching	$V_{CE} = -5 \text{ V}; I_{C} = -2 \text{ mA}; T_{amb} = 25 \text{ °C}$		0.95	1	1.05	
V _{BE1} -V _{BE2}	base-emitter voltage matching		[2]	-	-	2	mV

Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02$ The smaller of the two values is subtracted from the larger value.

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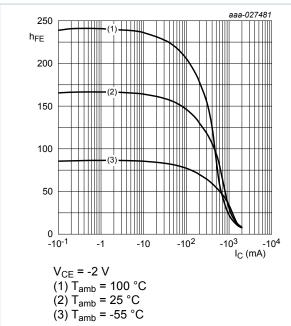


Fig. 4. DC current gain as a function of collector current; typical values

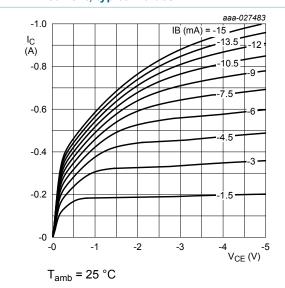


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

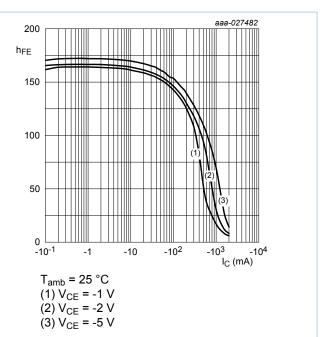


Fig. 5. DC current gain as a function of collector current; typical values

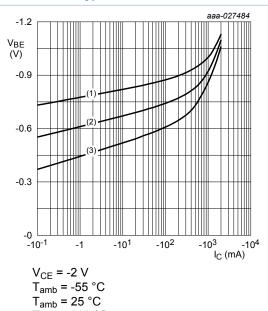
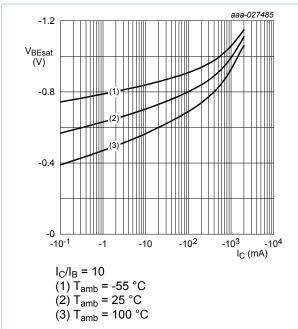


Fig. 7. Base-emitter voltage as a function of collector current; typical values

T_{amb} = 100 °C

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Base-emitter saturation voltage as a function of Fig. 9. Fig. 8. collector current; typical values

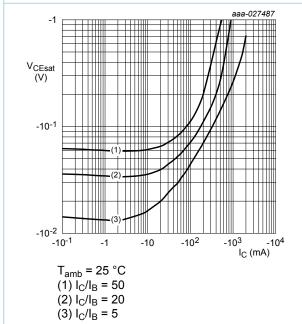
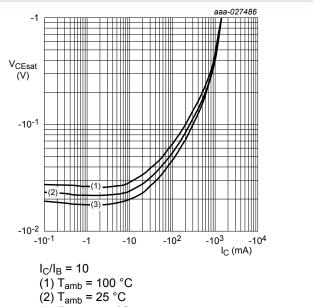


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



(3) $T_{amb} = -55 \, ^{\circ}C$

Collector-emitter saturation voltage as a function of collector current; typical values

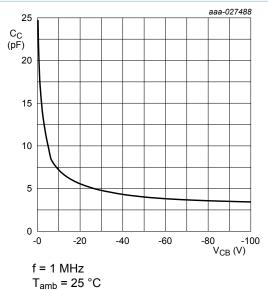


Fig. 11. Collector capacitance as a function of collector-base voltage; typical values

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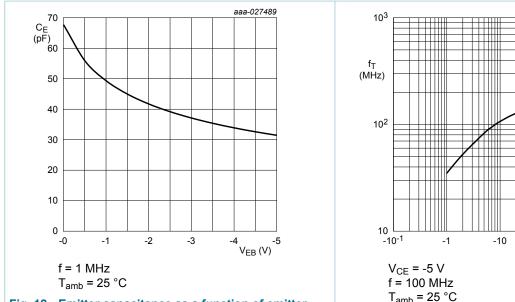


Fig. 12. Emitter capacitance as a function of emitterbase voltage; typical values

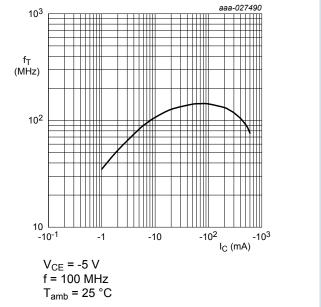


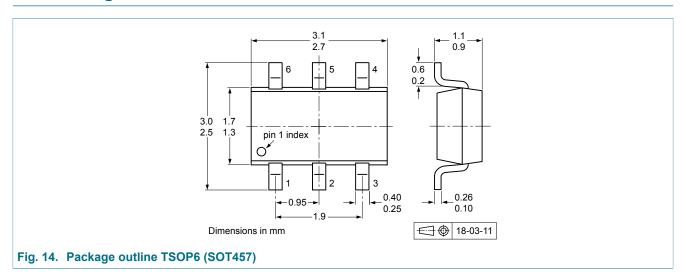
Fig. 13. Transition frequency as a function of collector current; typical values

11. Test information

Quality information

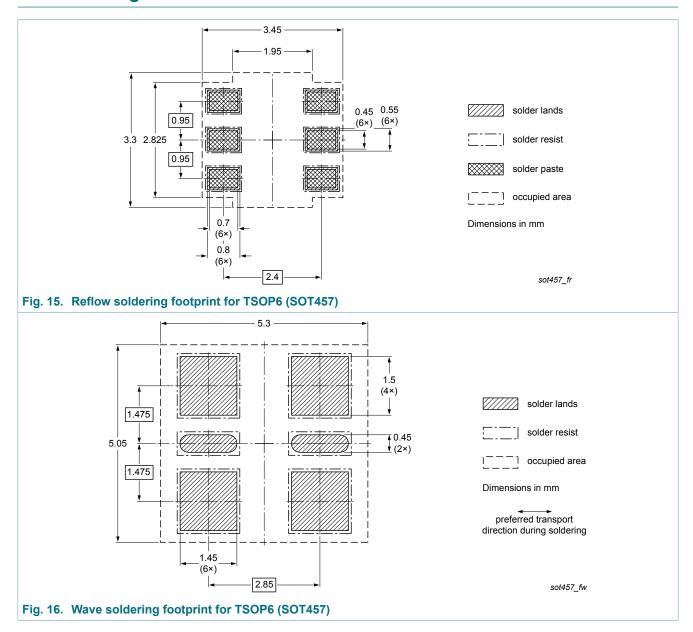
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



80 V, 1 A PNP/PNP matched double transistors

13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCM53DS v.1	20180410	Product data sheet	-	-

80 V, 1 A PNP/PNP matched double transistors

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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