

80 V, 1 A NPN medium power transistors Rev. 2 — 29 April 2019

**Product data sheet** 

# 1. Product profile

### 1.1. General description

NPN medium power transistors in a medium power SOT223 (SC73) Surface-Mounted Device (SMD) plastic package.

#### Table 1. Product overview

Type number	Package	NPN comlement	
	Nexperia	JEDEC	
BCP56T	SOT223	SC-73	BCP53T
BCP56-10T			BCP53-10T
BCP56-16T			BCP53-16T

### 1.2. Features and benefits

- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- Three current gain selections
- High power dissipation capability
- AEC-Q101 qualified

### 1.3. Applications

- Linear voltage regulators
- MOSFET drivers
- High-side switches
- Power management
- Amplifiers

### 1.4. Quick reference data

#### Table 2. Quick reference data

#### $T_{amb}$ = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	80	V
I <sub>C</sub>	collector current		-	-	1	A
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	2	A

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h <sub>FE</sub>	DC current gain						
	BCP56T	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	250	
	BCP56-10T		[1]	63	-	160	
	BCP56-16T		[1]	100	-	250	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

### 2. Pinning information

Table 3. Pinning				
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	E	emitter		B - M
4	С	collector	<b>1 2 3</b>	Ĕ
				sym123

# 3. Ordering information

### Table 4. Ordering information

Type number	Package	Package				
	Name	Description	Version			
BCP56T	SC-73	plastic, surface-mounted package with increased heatsink;	SOT223			
BCP56-10T		4 leads				
BCP56-16T						

### 4. Marking

Table 5. Marking					
Type number	Marking code				
BCP56T	BCP56T				
BCP56-10T	P5610T				
BCP56-16T	P5616T				

### 5. Limiting values

#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T<sub>amb</sub> = 25 °C unless otherwise specified.

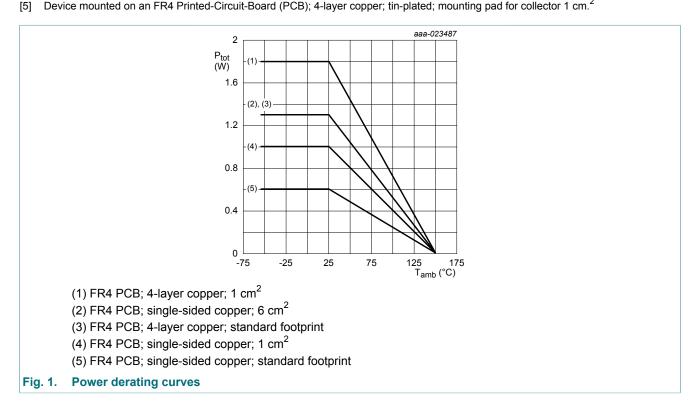
Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	open emitter		100	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	1	A
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	2	A
I <sub>B</sub>	base current				0.2	A
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	0.3	А
P <sub>tot</sub> tot	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.6	W
			[2]	-	1	W
			[3]	-	1.3	W
			[4]	-	1.3	W
			[5]	-	1.8	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

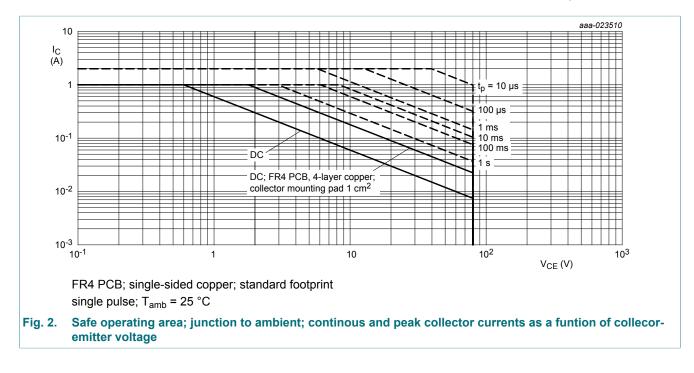
Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. [2]

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>. [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint. [4]

Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm.<sup>2</sup>



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# 6. Thermal characteristics

#### **Table 7. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	thermal resistance from junction to ambient	in free air	[1]	-	-	209	K/W
		[2]			125	K/W	
		[3]			97	K/W	
			[4]	-	-	97	K/W
			[5]	-	-	70	K/W
R <sub>(j-sp)</sub>	thermal resistance from junction to solder point			-	-	18	K/W

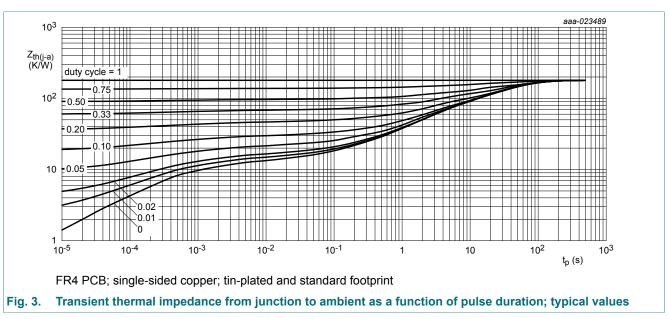
Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint. [1]

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>. [2]

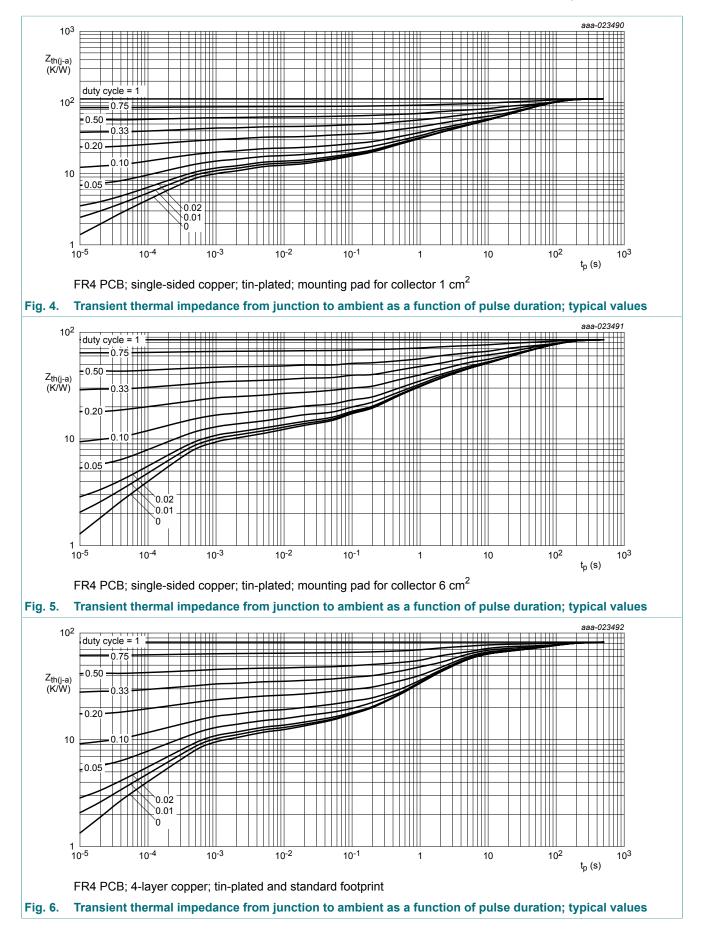
[3]

Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint. [4]

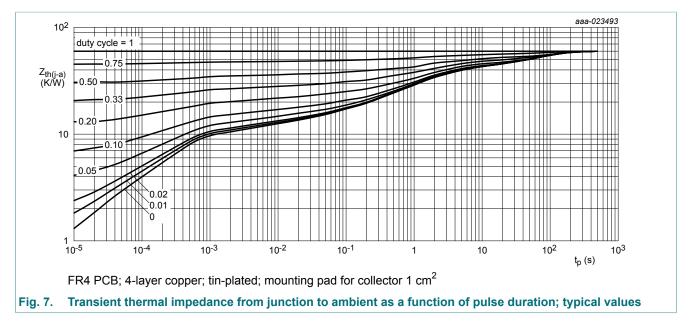
[5] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



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# 7. Characteristics

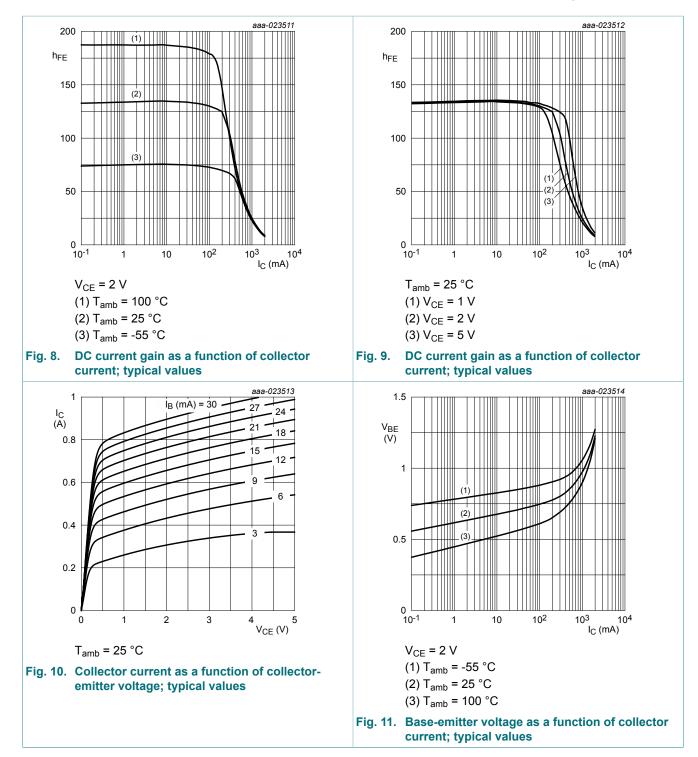
#### **Table 8. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

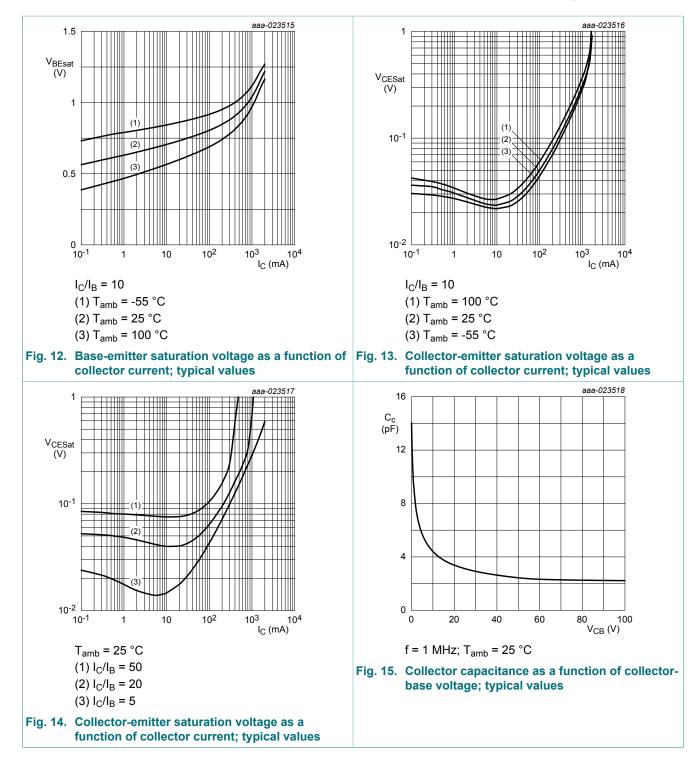
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		100	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 2 mA; I <sub>E</sub> = 0 A		80	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A		5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	10	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$		-	-	100	nA
h <sub>FE</sub> DC current gain							
BCP56T, -10T, -16T	BCP56T, -10T, -16T	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA		63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
	BCP56T	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	250	
	BCP56-10T	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	160	
	BCP56-16T	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	100	-	250	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	500	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	-	-	1	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz		100	155	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	4.5	-	pF

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

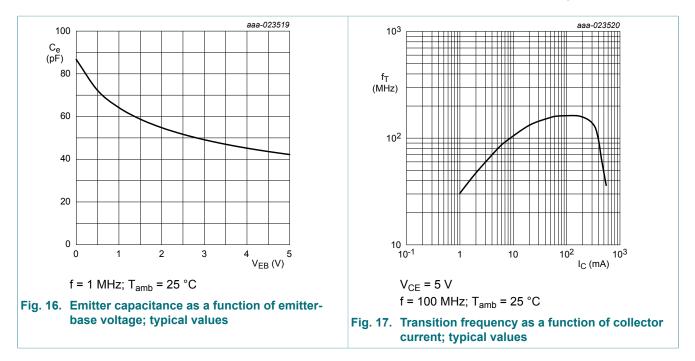
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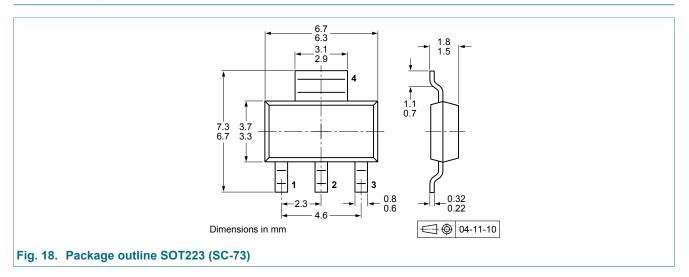


### 8. Test information

### 8.1. Quality information

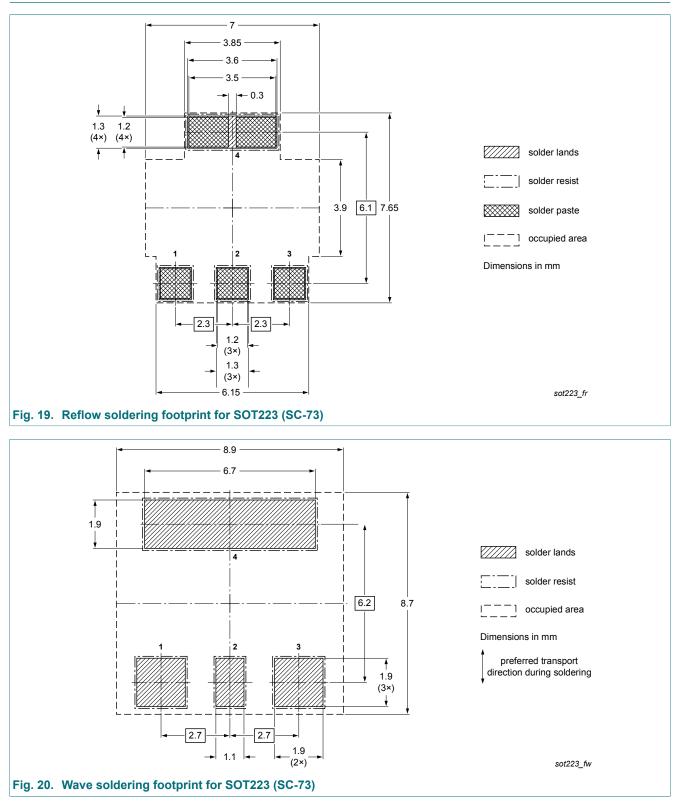
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 9. Package outline



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# 10. Soldering



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# **11. Revision history**

Table 9. Revision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes			
BCP56T_SER v.2	20190429	Product data sheet	-	BCP56T_SER v.1			
Modifications:	Characteristics:	Characteristics: breakdown voltages added					
BCP56T_SER v.1	20160705	Product data sheet	-	-			

BCP56T\_SER

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# 12. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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