

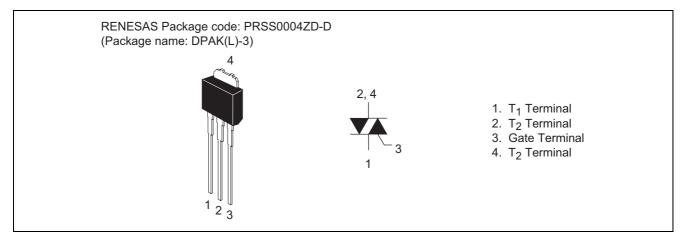
Triac Low Power Use

> REJ03G0290-0200 Rev.2.00 Nov 30, 2007

Features

- $I_{T(RMS)}: 3 A$
- V_{DRM} : 600 V
- I_{FGTI}^{om} , I_{RGTI} , $I_{RGT III}$: 15 mA (10 mA)^{Note5}
- Non-Insulated Type
 Planar Passivation T
- Planar Passivation Type
- Lead Mounted Type

Outline



Applications

Hybrid IC, solid state relay, switching mode power supply, light dimmer, electric fan, electric blanket, washing machine, and other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Faranieter	Symbol	12		
Repetitive peak off-state voltage ^{Note1}	V _{DRM}	600	V	
Non-repetitive peak off-state voltage ^{Note1}	V _{DSM}	720	V	

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Parameter	Symbol	RATINGS	Unit	Conditions
RMS on-state current	I _{T (RMS)}	3	A	Commercial frequency, sine full wave 360° conduction, Tc = $108^{\circ}C^{Note3}$
Surge on-state current	I _{TSM}	30	A	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	3.7	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	3	W	
Average gate power dissipation	P _{G (AV)}	0.3	W	
Peak gate voltage	V _{GM}	6	V	
Peak gate current	I _{GM}	0.3	А	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
eMassim		0.26	g	Typical value

Notes: 1. Gate open.

Electrical Characteristics

Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state current		I _{DRM}	_	—	2.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V _{TM}	_	—	1.7	V	Tc = 25° C, I _{TM} = 4.5 A, Instantaneous measurement
Gate trigger voltage ^{Note2}	I	Veee			1.5	V	Tj = 25°C, V_D = 6 V, R_L = 6 Ω ,
Gale lingger voltage		V _{FGTI}			-	-	
	II	V _{RGTI}	_	—	1.5	V	$R_G = 330 \Omega$
	III	V _{RGTIII}	—	—	1.5	V	
Gate trigger current ^{Note2}	Ι	I _{FGTI}	_	—	15 ^{Note5}	mA	$Tj = 125^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$
	II	I _{RGTI}	_	—	15 ^{Note5}	mA	$R_G = 330 \Omega$
	III	I _{RGTIII}	_	—	15 ^{Note5}	mA	
Gate non-trigger voltage		V_{GD}	0.2	—	—	V	$Tj = 25^{\circ}C, V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-c)}	_	_	3.8	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-state commutating voltage ^{Note4}		(dv/dt)c	5	—	—	V/µs	Tj = 125°C

Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

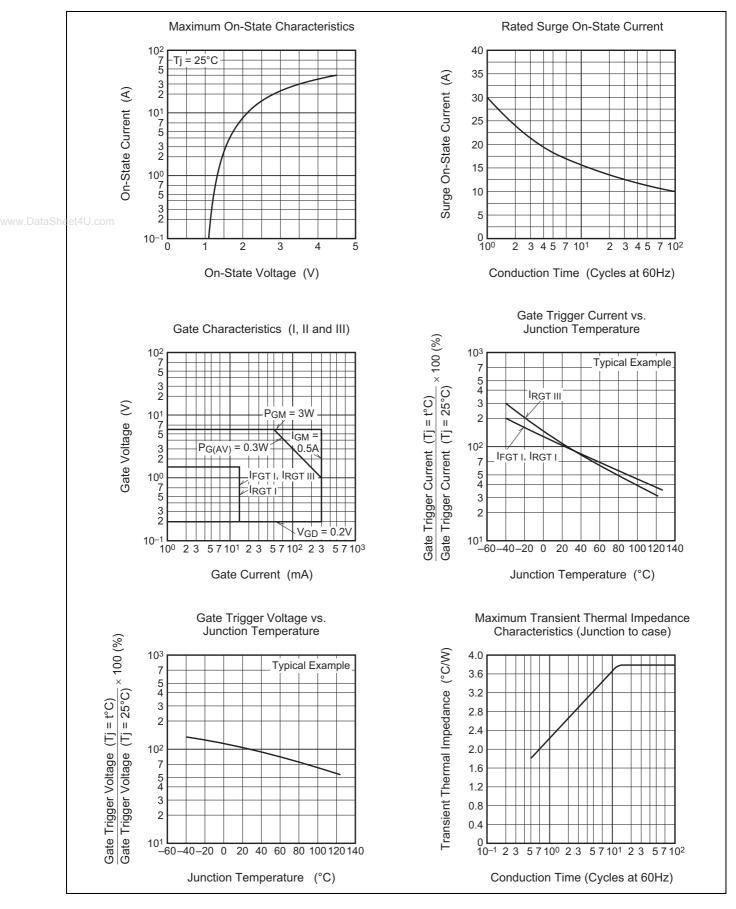
3. Case temperature is measured on the T_2 tab.

4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

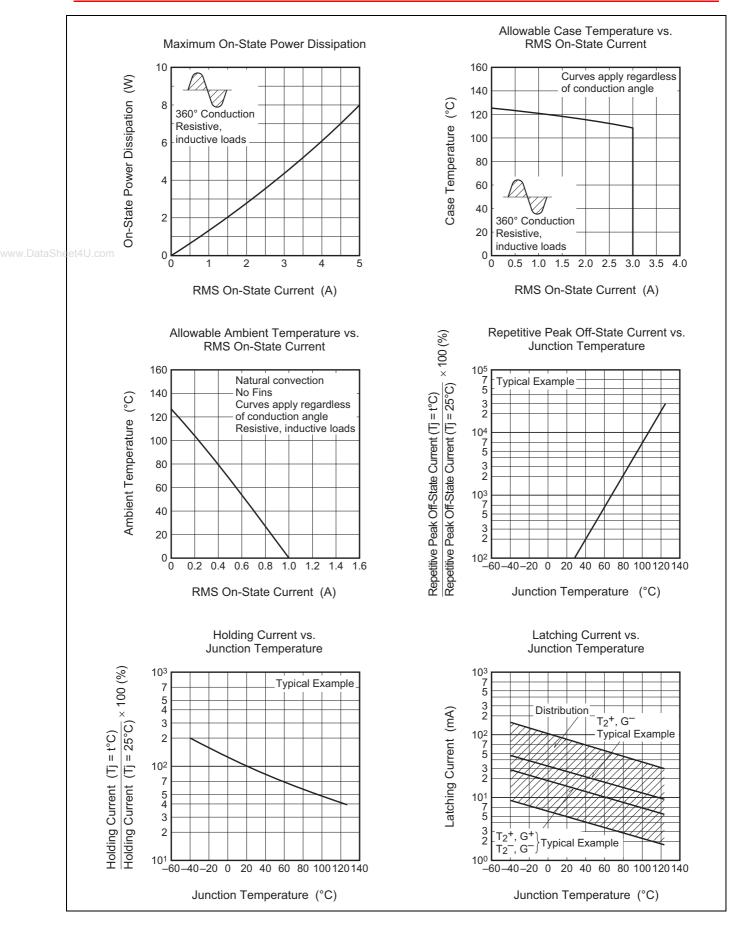
5. High sensitivity ($I_{GT} \le 10$ mA) is also available. (I_{GT} item: 1)

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage → Time
2. Rate of decay of on-state commutating current (di/dt)c = -1.5 A/ms	Main Current → Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage Time (dv/df)c V _D

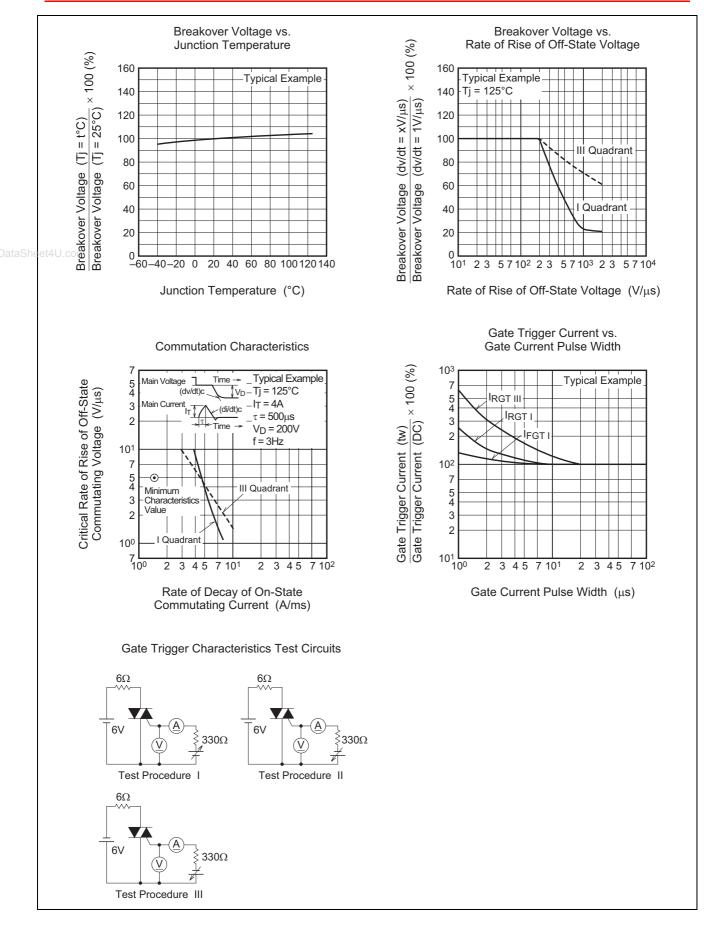
Performance Curves



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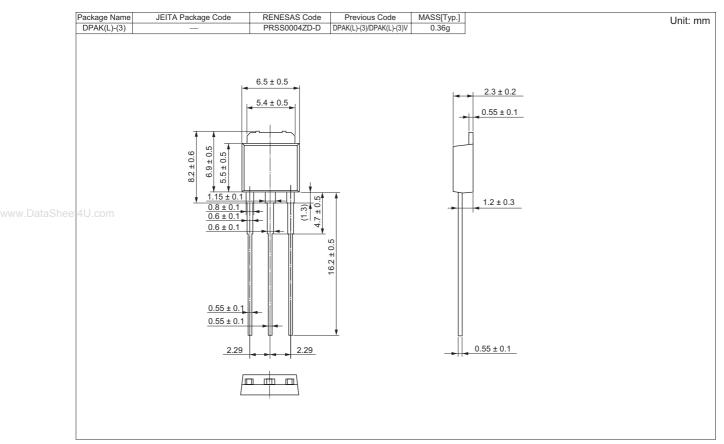


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Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	100	Type name – A1	BCR3AS-12A-A1

Note : Please confirm the specification about the shipping in detail.

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