





SOT-23 Formed SMD Package

BCW61A BCW61B BCW61C BCW61D

SILICON PLANAR EPITAXIAL TRANSISTORS

P-N-P silicon transistors

Marking

BCW61A = BA

BCW61B = BB

BCW61C = BC

BCW61D = BD

PACKAGE OUTLINE DETAILS
ALL DIMENSIONS IN mm

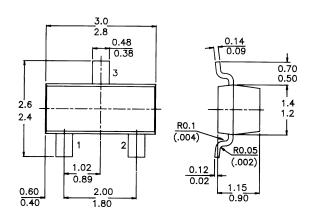
Pin configuration

1 = BASE

2 = EMITTER

3 = COLLECTOR





ABSOLUTE MAXIMUM RATINGS

Collector-emitter voltage ($V_{BE} = 0$)	$-V_{CES}$	max.	32 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	32 V
Collector current (d.c.)	$-I_C$	max.	200 mA
Total power dissipation	P_{tot}	max.	<i>250</i> mW
Junction temperature	T_{j}	max.	150 °C
Transition frequency at f: 100 MHz	J		
$-V_{CE} = 5 \ V_{C} - I_{C} = 10 \ mA$	f_T	typ.	180 MHz
Noise figure at $f = 1 \text{ kHz}$			
$-V_{CE} = 5 \ V; \ -I_{C} = 200 \ mA$	F	typ.	2 dB
Total power dissipation Junction temperature Transition frequency at f : 100 MHz $-V_{CE} = 5 \ V$; $-I_{C} = 10 \ \text{mA}$ Noise figure at $f = 1 \ \text{kHz}$	P_{tot} T_j f_T	max. max. typ.	250 mW 150 °C 180 MHz

BCW61A BCW61B BCW61C BCW61D

RATINGS (at $T_A = 25^{\circ}C$ unless otherwi	ise spec	ified)					
Limiting values				17		20	17
Collector-emitter voltage ($V_{BE} = 0$) Collector-emitter voltage (open base)				$V_{CES} \ V_{CE0}$	max. max.	32 32	
Emitter-base voltage (open collector)				V_{EB0}	max.		V
Collector current (d.c.)				v ebu I _C	max.		v mA
Base current				l_R	max.		mA
Total power dissipation up to $T_{amb} = 23$	5 °C			tot	max.		mW
Storage temperatu re				stg	-55 to		
Junction temperature			7	0	max.	150	
THERMAL RESISTANCE							
From junction to ambient			R	th j-a	=	500	KW
CHARACTERISTICS							
T_{amb} = 25 °C unless otherwise specified	!						
Collector-emitter cut-off current				_			
$V_{EB} = 0; -V_{CE} = 32 V$	0.0			I_{CES}	<		nA
$V_{EB} = 0$; $-V_{CE} = 32$ V; $T_{amb} = 150$ Emitter-base cut-off current	${}^{\circ}C$		-	I_{CES}	<	20	mA
$I_C = 0$; $-V_{EB} = 4 V$			_	I_{EB0}	<	20	nΑ
Saturation voltages							
$-I_C = 10 \text{ mA}; -I_B = 0.25 \text{ mA}$			-	V _{CEsat}	0,06 to	0,25	V
			-	V _{BEsat}	0,6 to	0,85	V
$-I_C = 50mA; -I_B = 1,25mA$				V _{CEsat}	0,12 to	0,55	V
			_	V_{BEsat}	0,68 to	1,05	V
Transition frequency at $f = 100 \text{ MHz}$							
$-V_{CE} = 5V; -I_{C} = 10 \text{ mA}$			f	T	typ.	180	MHz
Collector capacitance at $f = 1$ MHz							
$-V_{CB} = 10 \ V; \ I_{E:} \ I_{e} = 0$			C	\overline{c}_c	typ.	4,5	рF
Emitter capacitance at $f = 1$ MHz							-
$-V_{EB} = 0.5 \ V; I_C = I_C = 0$			C	Z_e	typ.	11	рF
Noise figure at $R_S = 2 \text{ kW}$					typ.	2	dB
$-V_{CE} = 5 \ V; \ -I_{C} = 200 \ \text{mA}; \ B = 200$	Hz		F	,	<	6	dВ
			BCW61A	61B	61C	61D	
D.C. current gain							
$-V_{CE} = 5 \ V; \ -I_{C} = 10 \ \text{mA}$	h_{FE}	>	20	30	40	100	
$-V_{CE} = 5 V; -I_{C} = 2 mA$	h_{FE}	>	120	180	250	380	
		<	220	310	460	630	
$-V_{CE} = 1 \ V; \ -I_{C} = 50 mA$	h_{FE}	>	60	80	100	110	
Input impedance							
$-V_{CE} = 5 \ V; -I_{C} = 2 \ mA; f = 1 \ kHz$	h _{ie}	typ	. 2,7	3,6	4,5	7,5	₩

			\boldsymbol{A}	В	C	D	
Reverse voltage transfer ratio $-V_{CE} = 5 \ V$; $-I_{C}$: 2 mA; $f = 1 \ kHz$ Small-signal current gain	h _{re}	typ.	1,5	2	2	3	10-4
$-V_{CE} = 5 V$; $-I_{C}$: 2 mA; $f = 1 kHz$	hfe	min. max.	125 250	175 350	250 500	350 700	
Output admittance							
$-V_{CE} = 5 \ V; -I_{C} = 2 \ mA; \ f = 1 \ kHz$	h_{oe}	typ.	18	24	30	<i>50</i>	m S
Base-emitter voltage							
$-V_{CE} = 5 \text{ V; } -I_{C} = 2 \text{ mA}$	V_{BE}		0,6 to 0,75 0. 0,65		75		V
		typ.			0,65		V
$-V_{CE} = 5 \ V; \ -I_{C} = 10 \ mA$	V_{BE}	typ.	0,55				V
$-V_{CE} = 1 \ V; \ -I_{C:} \ 50 \ mA$	V_{BE}	typ.		l	0,72		V

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