



# BCX18

## PNP general purpose transistors

4 December 2024

Product data sheet

## 1. General description

PNP general-purpose transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.  
NPN complement: BCX19

## 2. Features and benefits

- High current (max. 500 mA)
- Low voltage (max. 25 V)
- AEC-Q101 qualified

## 3. Applications

- Saturated switching and driver applications e.g. for industrial service
- Thick and thin-film circuits

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-25	V
$I_C$	collector current		-	-	-500	mA
$h_{FE}$	DC current gain	$V_{CE} = -1 \text{ V}$ ; $I_C = -300 \text{ mA}$ ; $T_j = 25 \text{ }^\circ\text{C}$	70	-	-	

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base		
2	E	emitter		
3	C	collector		

## 6. Ordering information

**Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
BCX18	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<a href="#">SOT23</a>

## 7. Marking

**Table 4. Marking codes**

Type number	Marking code <sup>[1]</sup>
BCX18	T2%

[1] % = placeholder for manufacturing site code

## 8. Limiting values

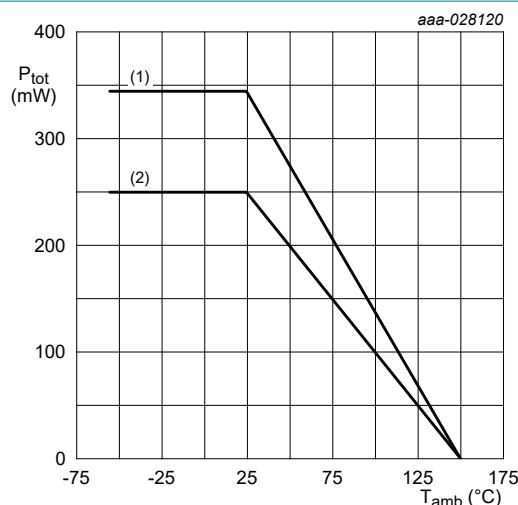
**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-30	V
$V_{CEO}$	collector-emitter voltage	open base		-	-25	V
$V_{EBO}$	emitter-base voltage	open collector		-	-5	V
$I_C$	collector current			-	-500	mA
$I_{CM}$	peak collector current			-	-1	A
$I_{BM}$	peak base current			-	-200	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$	[1]	-	250	mW
			[2]	-	345	mW
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-65	150	°C
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



(1) FR4 PCB, single-sided copper;  $1 \text{ cm}^2$   
 (2) FR4 PCB, single-sided copper; standard footprint

Fig. 1. Power derating curves for SOT23

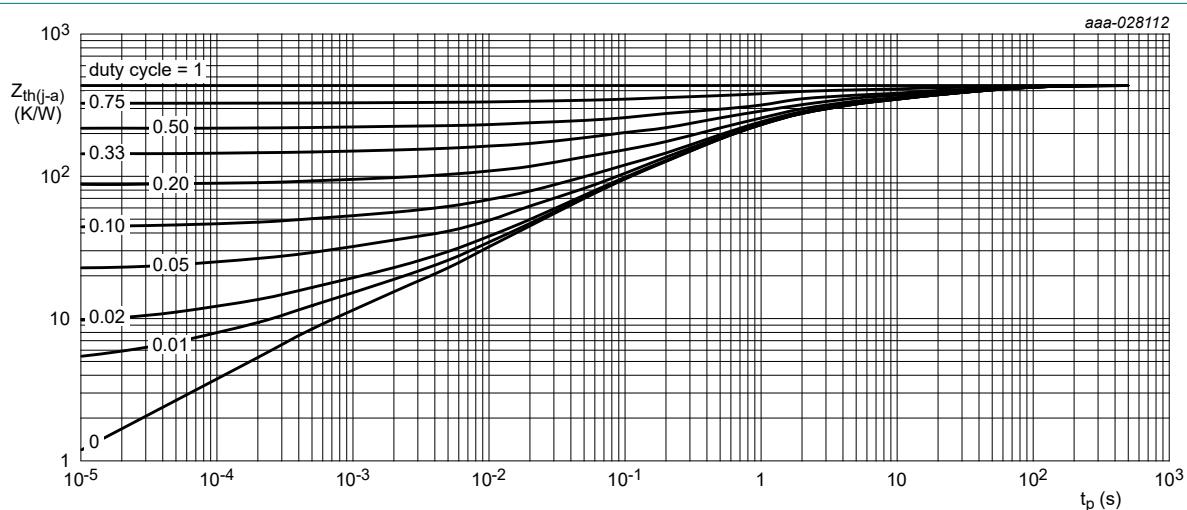
## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
			[2]	-	-	362	K/W

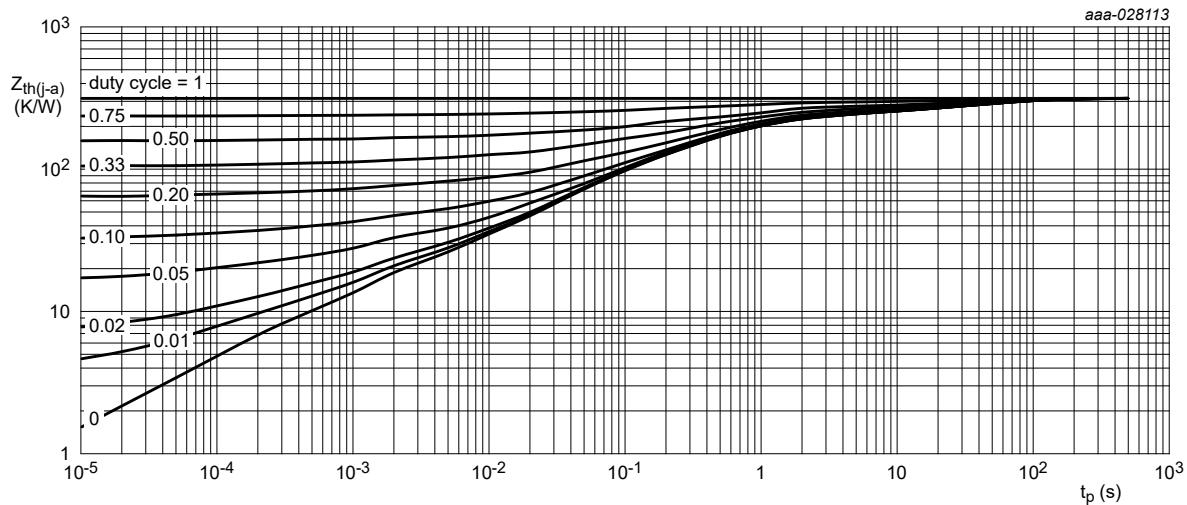
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector  $1 \text{ cm}^2$ .



FR4 PCB, single-sided copper, tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, single-sided copper, tin-plated and mounting pad for cathode 1 cm<sup>2</sup>

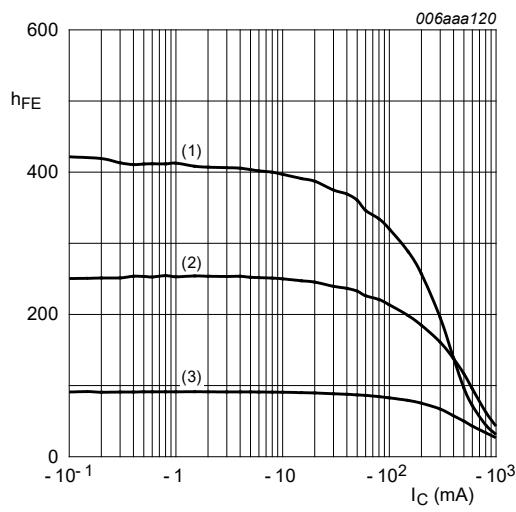
Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 25 °C	-	-	-100	nA	
		V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-5	μA	
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>j</sub> = 25 °C	-	-	-100	nA	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA; T <sub>j</sub> = 25 °C	100	-	600		
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -300 mA; T <sub>j</sub> = 25 °C	70	-	-		
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA; T <sub>j</sub> = 25 °C	40	-	-		
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA; T <sub>j</sub> = 25 °C	-	-	-620	mV	
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA; T <sub>j</sub> = 25 °C	[1]	-	-	-1.2	V
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>j</sub> = 25 °C	-	9	-	pF	
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz; T <sub>j</sub> = 25 °C	80	-	-	MHz	

[1] V<sub>BE</sub> decreases by approximately -2 mV/°C with increasing temperature.



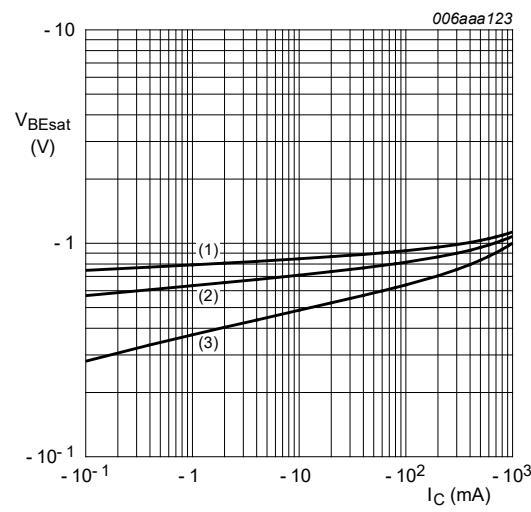
$V_{CE} = -1 \text{ V}$

(1)  $T_{amb} = 100 \text{ }^{\circ}\text{C}$

(2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

(3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 4. DC current gain as a function of collector current; typical values**



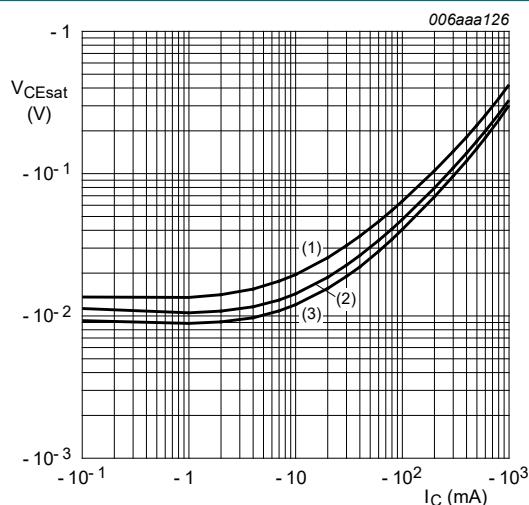
$I_C/I_B = 10$

(1)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

(2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

(3)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$

**Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values**



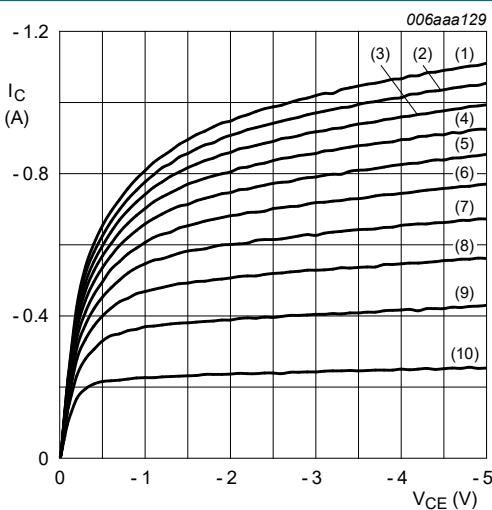
$I_C/I_B = 10$

(1)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$

(2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

(3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values**



$T_{amb} = 25 \text{ }^{\circ}\text{C}$

(1)  $I_B = -13.0 \text{ mA}$

(2)  $I_B = -11.7 \text{ mA}$

(3)  $I_B = -10.4 \text{ mA}$

(4)  $I_B = -9.1 \text{ mA}$

(5)  $I_B = -7.8 \text{ mA}$

(6)  $I_B = -6.5 \text{ mA}$

(7)  $I_B = -5.2 \text{ mA}$

(8)  $I_B = -3.9 \text{ mA}$

(9)  $I_B = -2.6 \text{ mA}$

(10)  $I_B = -1.3 \text{ mA}$

**Fig. 7. Collector current as a function of collector-emitter voltage; typical values**

## 11. Test information

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

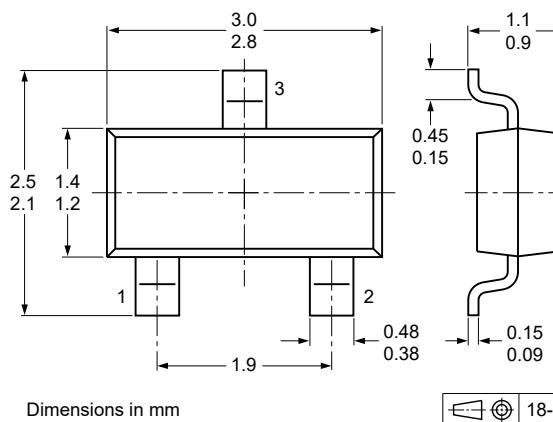


Fig. 8. Package outline SOT23

## 13. Soldering

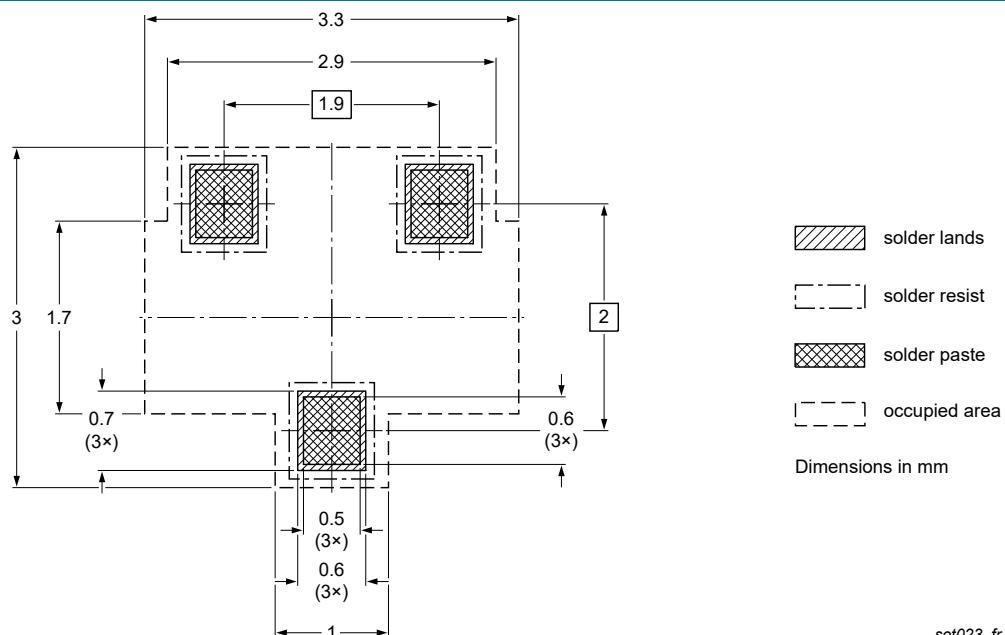


Fig. 9. Reflow soldering footprint for SOT23

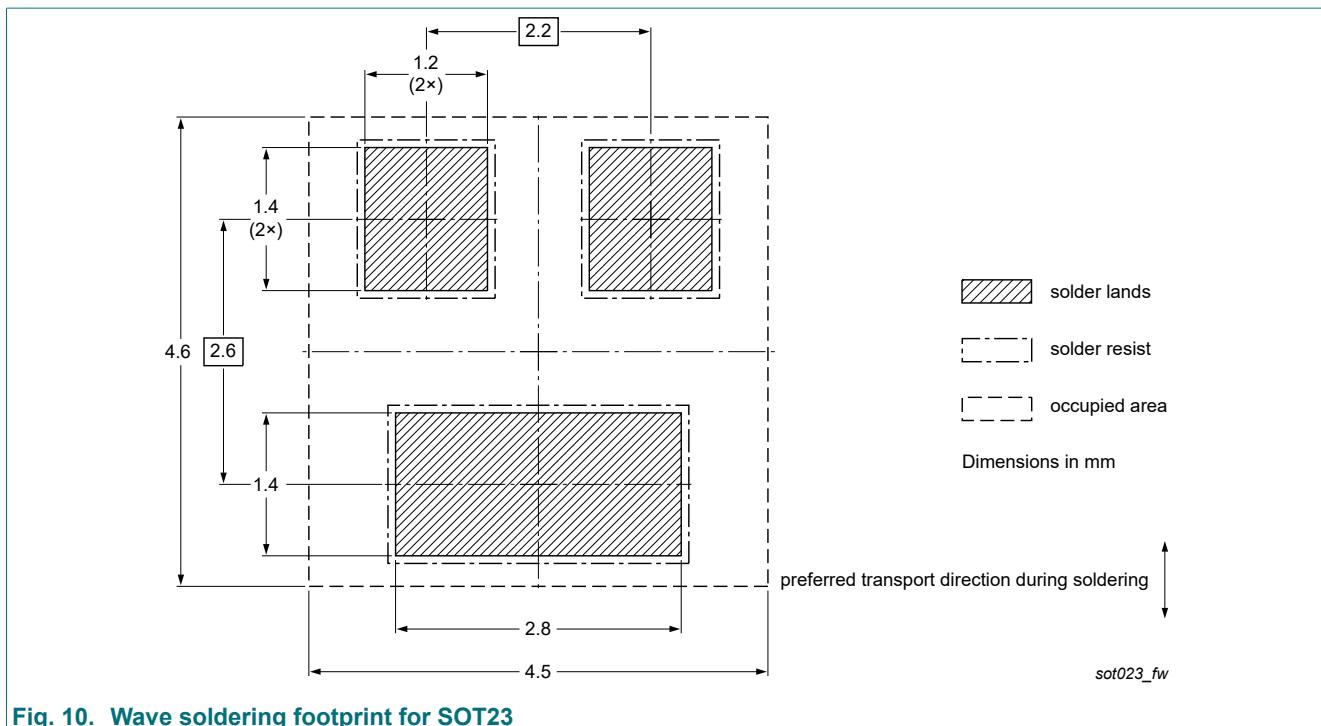


Fig. 10. Wave soldering footprint for SOT23

## 14. Revision history

**Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCX18 v.3	20241204	Product data sheet	-	BCX17_BCX18 v.2
Modifications:		<ul style="list-style-type: none"><li>• Family data sheet splitted to single type data sheets.</li><li>• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• Figures 1 - 7 added</li></ul>		
BCX17_BCX18 v.2	20040116	Product data sheet	-	BCX17_BCX18 v.1
BCX17_BCX18 v.1	19990531	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 4 December 2024

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