



BCX18

PNP general purpose transistors

4 December 2024

Product data sheet

1. General description

PNP general-purpose transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

NPN complement: BCX19

2. Features and benefits

- High current (max. 500 mA)
- Low voltage (max. 25 V)
- AEC-Q101 qualified

3. Applications

- Saturated switching and driver applications e.g. for industrial service
- Thick and thin-film circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-25	V
I_C	collector current		-	-	-500	mA
h_{FE}	DC current gain	$V_{CE} = -1\text{ V}$; $I_C = -300\text{ mA}$; $T_J = 25\text{ °C}$	70	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym132
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCX18	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BCX18	T2%

[1] % = placeholder for manufacturing site code

8. Limiting values

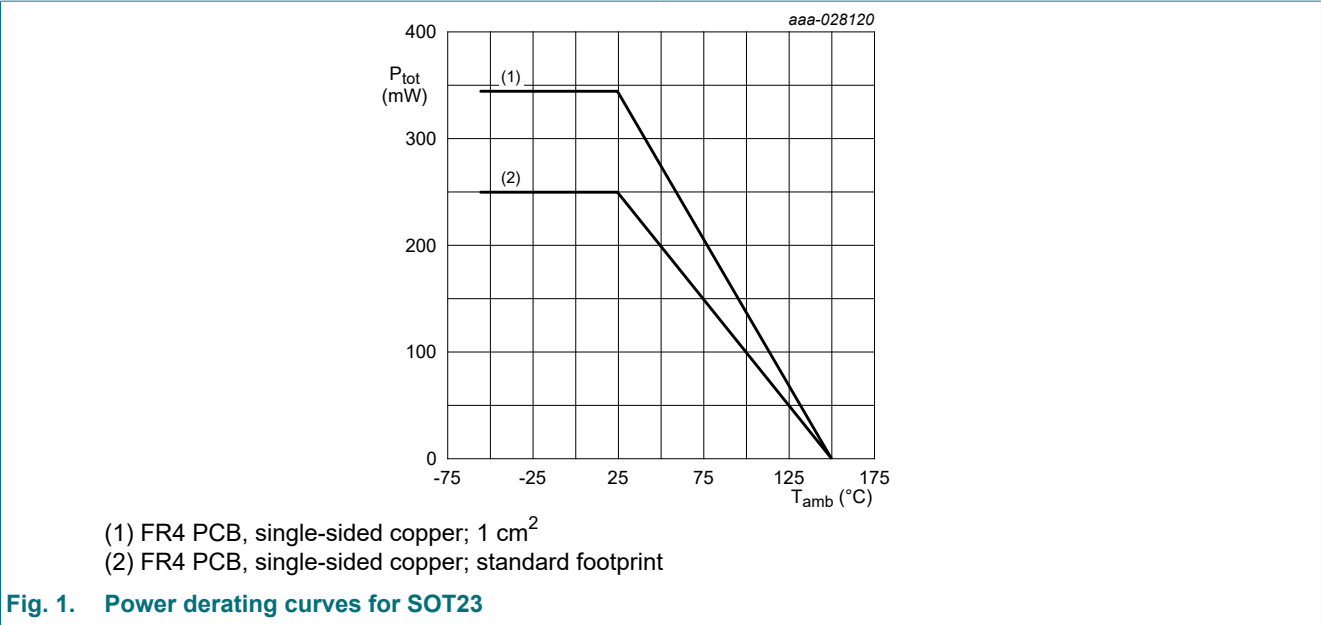
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-30	V
V _{CEO}	collector-emitter voltage	open base		-	-25	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-500	mA
I _{CM}	peak collector current			-	-1	A
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
			[2]	-	345	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper,tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm².

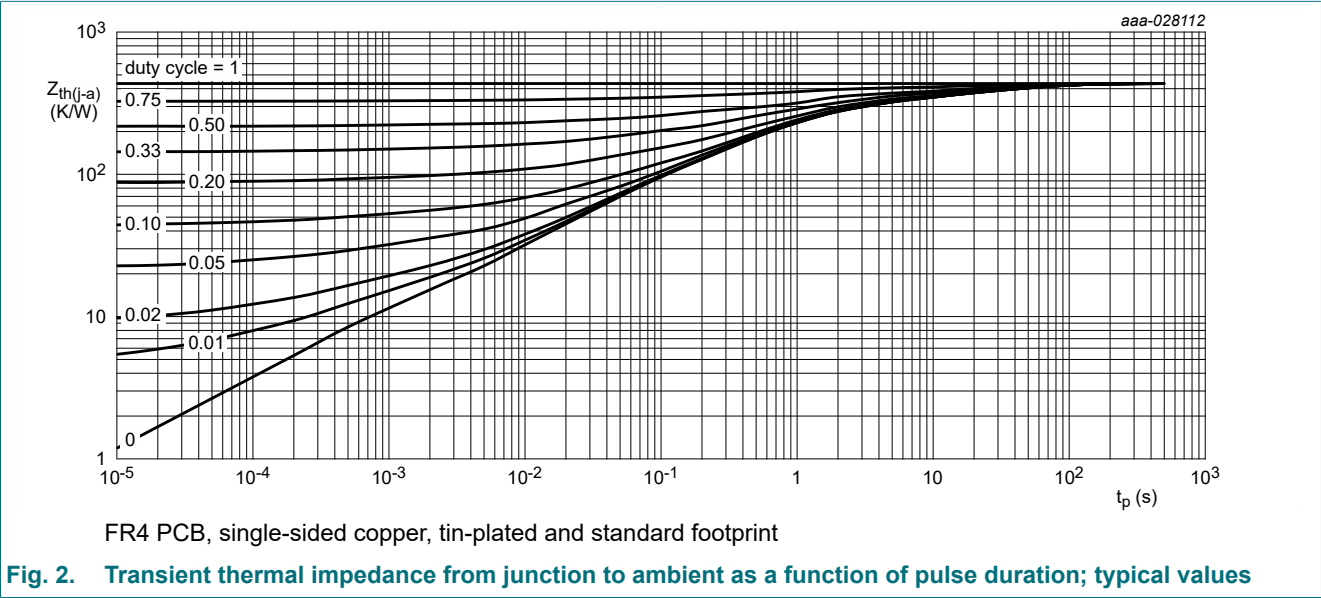


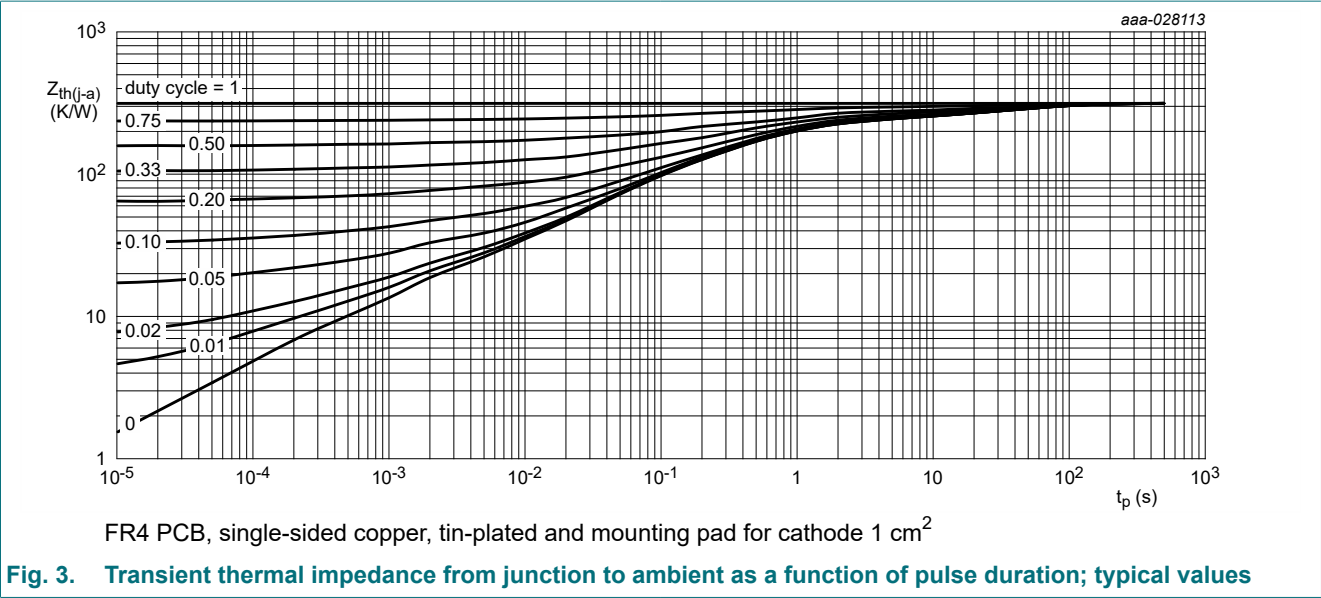
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
			[2]	-	-	362	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm².





10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -20 V; I _E = 0 A; T _j = 25 °C		-	-	-100	nA
		V _{CB} = -20 V; I _E = 0 A; T _j = 150 °C		-	-	-5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _j = 25 °C		-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -100 mA; T _j = 25 °C		100	-	600	
		V _{CE} = -1 V; I _C = -300 mA; T _j = 25 °C		70	-	-	
		V _{CE} = -1 V; I _C = -500 mA; T _j = 25 °C		40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -500 mA; I _B = -50 mA; T _j = 25 °C		-	-	-620	mV
V _{BE}	base-emitter voltage	V _{CE} = -1 V; I _C = -500 mA; T _j = 25 °C	[1]	-	-	-1.2	V
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _j = 25 °C		-	9	-	pF
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz; T _j = 25 °C		80	-	-	MHz

[1] V_{BE} decreases by approximately -2 mV/°C with increasing temperature.

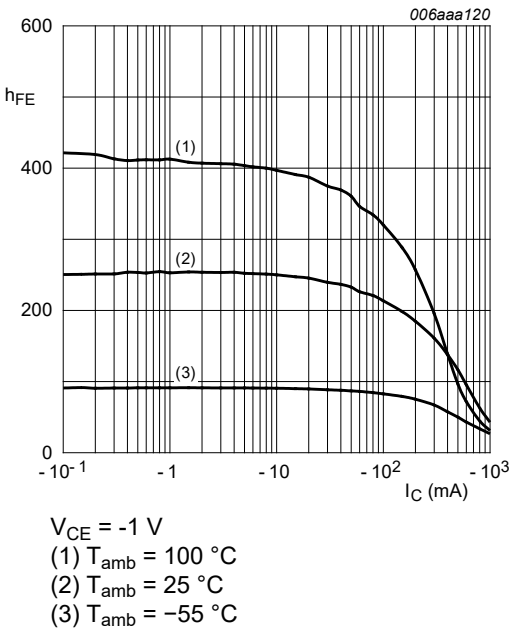


Fig. 4. DC current gain as a function of collector current; typical values

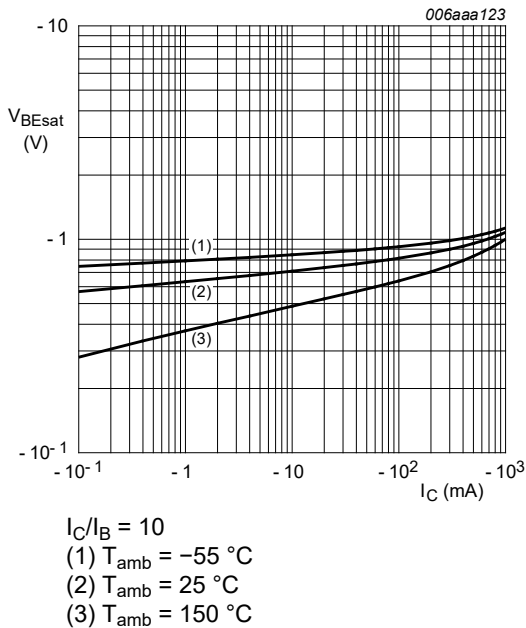


Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values

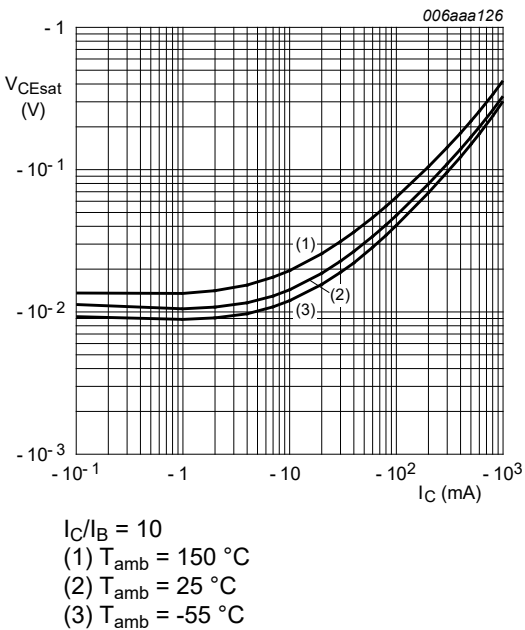


Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values

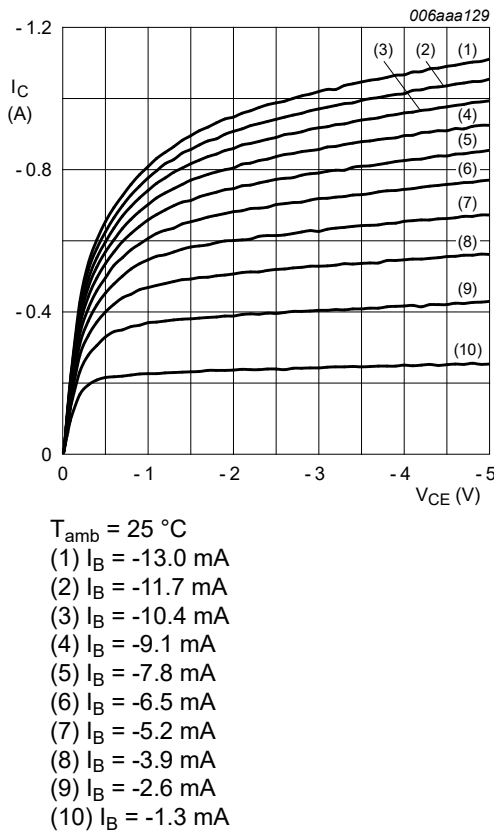


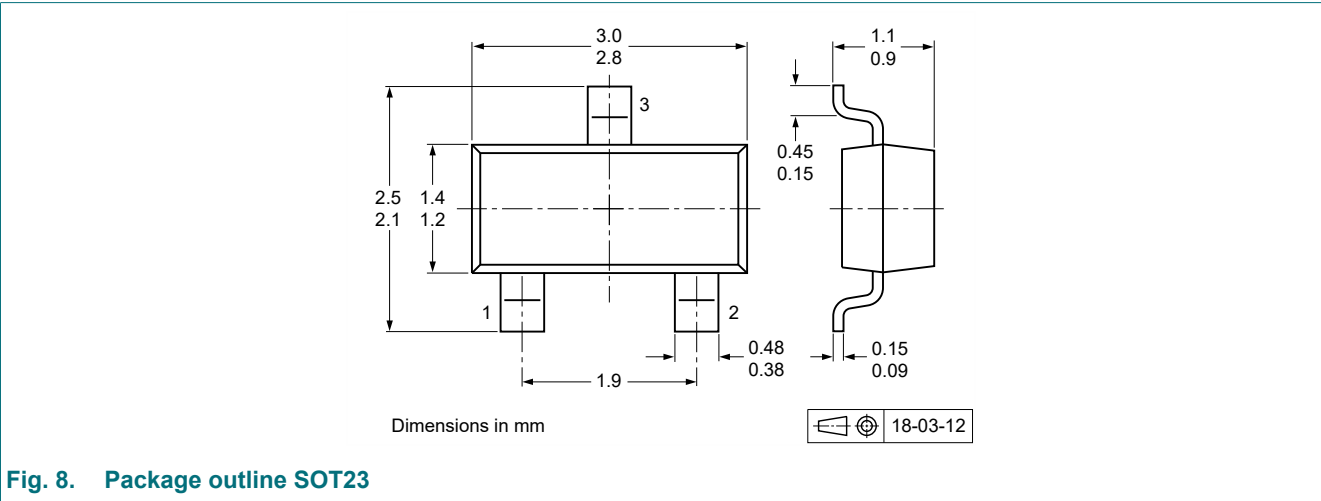
Fig. 7. Collector current as a function of collector-emitter voltage; typical values

11. Test information

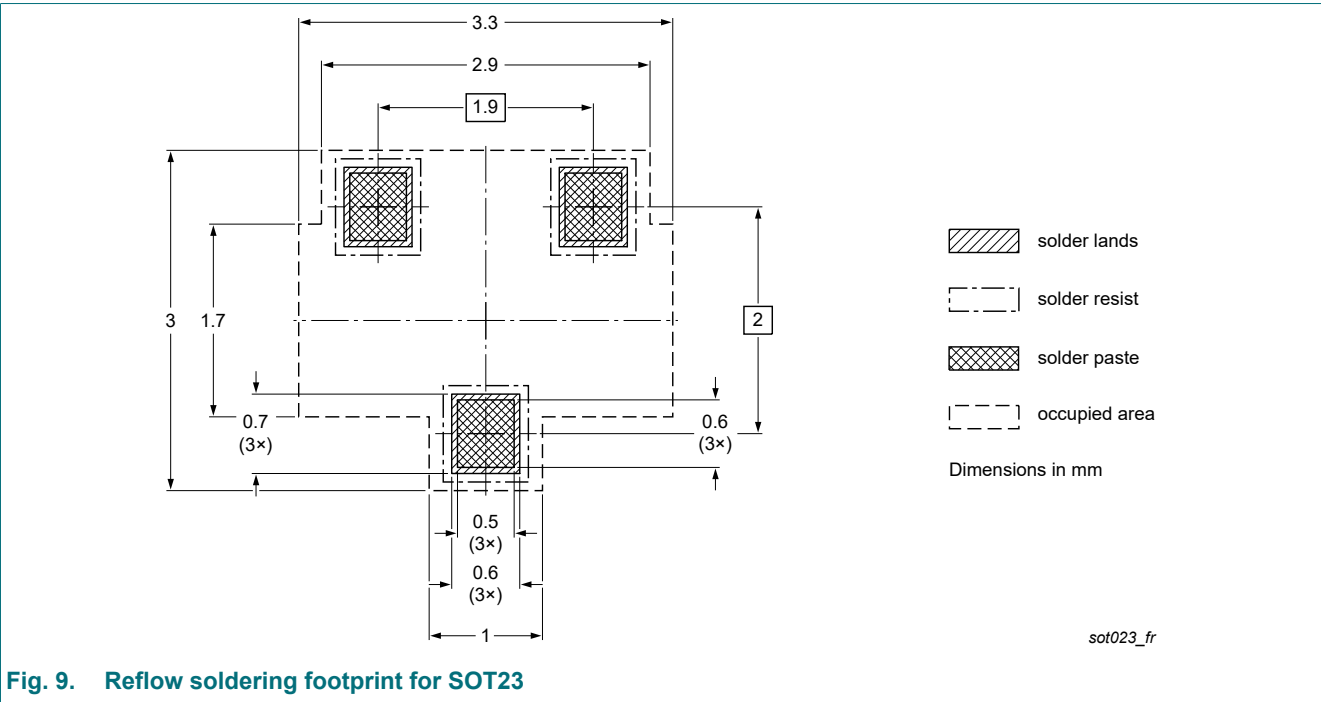
Quality information

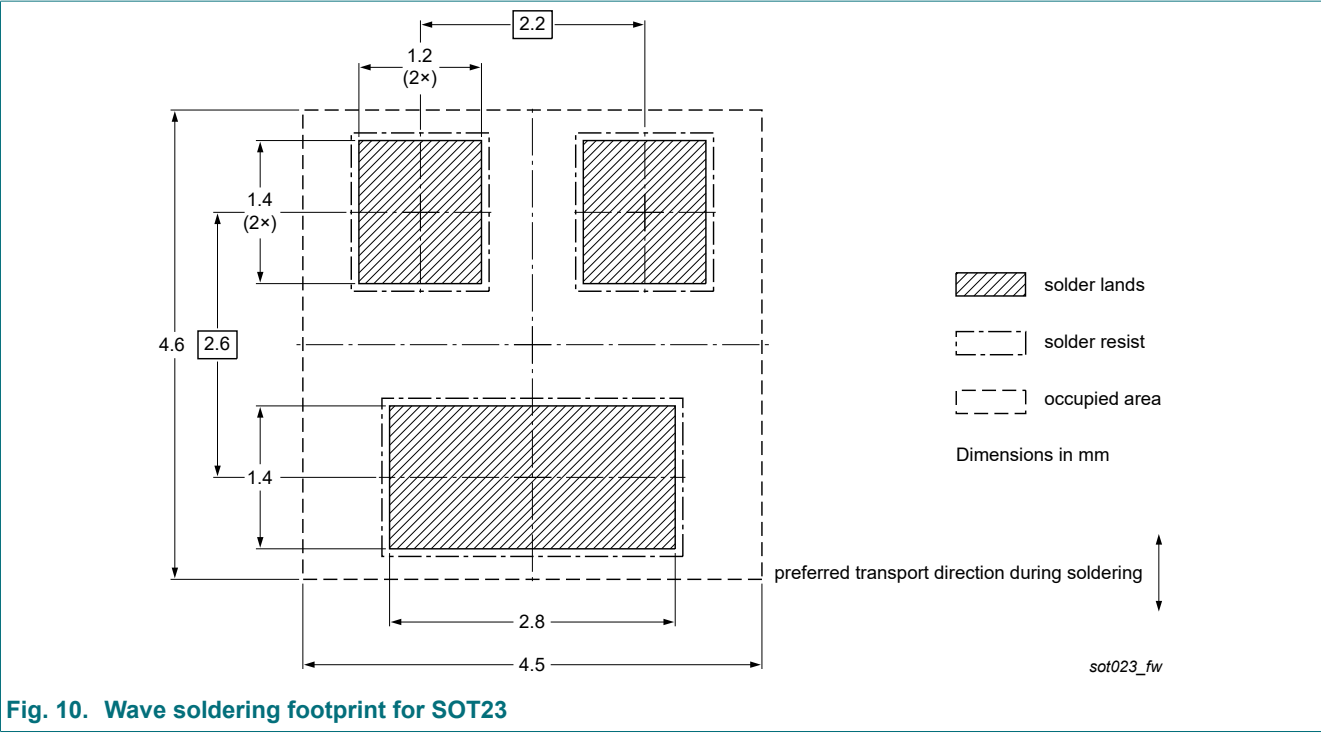
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCX18 v.3	20241204	Product data sheet	-	BCX17_BCX18 v.2
Modifications:	<ul style="list-style-type: none">Family data sheet splitted to single type data sheets.The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.Figures 1 - 7 added			
BCX17_BCX18 v.2	20040116	Product data sheet	-	BCX17_BCX18 v.1
BCX17_BCX18 v.1	19990531	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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