

**MAXIMUM RATINGS**

Rating	Symbol	BCX46	BCX48	BCX50	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	45	60	80	V <sub>dc</sub>
Collector-Base Voltage	V <sub>CBO</sub>	45	60	80	V <sub>dc</sub>
Emitter-Base Voltage	V <sub>EBO</sub>	5.0			V <sub>dc</sub>
Collector Current - Continuous	I <sub>C</sub>	1.0			A <sub>dc</sub>
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	625	5.0		mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	1.5	12		Watt mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150			°C

**THERMAL CHARACTERISTICS**

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	83.3	°C/W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	200	°C/W

**BCX46  
BCX48  
BCX50**

**CASE 29-02, STYLE 17  
TO-92 (TO-226AA)  
HIGH CURRENT TRANSISTORS  
PNP SILICON**

Refer to MPSA55 for graphs.

**ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C unless otherwise noted)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
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**OFF CHARACTERISTICS**

Collector-Emitter Breakdown Voltage* (I <sub>C</sub> = 10 mA <sub>dc</sub> , I <sub>B</sub> = 0) BCX46 BCX48 BCX50	V <sub>(BR)CEO</sub>	45 60 80			V <sub>dc</sub>
Collector-Base Breakdown Voltage (I <sub>C</sub> = 100 μA <sub>dc</sub> , I <sub>E</sub> = 0) BCX46 BCX48 BCX50	V <sub>(BR)CBO</sub>	45 60 80			V <sub>dc</sub>
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 10 μA <sub>dc</sub> , I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	5.0			V <sub>dc</sub>
Collector Cutoff Current (V <sub>CB</sub> = 30 V <sub>dc</sub> - I <sub>E</sub> = 0) BCX46 (V <sub>CB</sub> = 40 V <sub>dc</sub> - I <sub>E</sub> = 0) BCX48 (V <sub>CB</sub> = 60 V <sub>dc</sub> - I <sub>E</sub> = 0) BCX50	I <sub>CBO</sub>			100 100 100	nA <sub>dc</sub>

**ON CHARACTERISTICS\***

DC Current Gain (I <sub>C</sub> = 10 mA <sub>dc</sub> , V <sub>CE</sub> = 2.0 V <sub>dc</sub> ) (I <sub>C</sub> = 100 mA <sub>dc</sub> , V <sub>CE</sub> = 2.0 V <sub>dc</sub> ) (I <sub>C</sub> = 500 mA <sub>dc</sub> , V <sub>CE</sub> = 2.0 V <sub>dc</sub> ) (I <sub>C</sub> = 1 A <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> )	h <sub>FE</sub>	40 50 30 15	130 140 60		
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 500 mA <sub>dc</sub> , I <sub>B</sub> = 50 mA <sub>dc</sub> ) (I <sub>C</sub> = 1 A <sub>dc</sub> , I <sub>B</sub> = 100 mA <sub>dc</sub> )	V <sub>CE(sat)</sub>		0.25 0.3	0.5	V <sub>dc</sub>
Base-Emitter Saturation Voltage (I <sub>C</sub> = 500 mA <sub>dc</sub> , I <sub>B</sub> = 50 mA <sub>dc</sub> )	V <sub>BE(sat)</sub>		0.9		V <sub>dc</sub>
Base-Emitter On Voltage (I <sub>C</sub> = 500 mA <sub>dc</sub> , V <sub>CE</sub> = 2.0 V <sub>dc</sub> )	V <sub>BE(on)</sub>		0.85	1.2	V <sub>dc</sub>

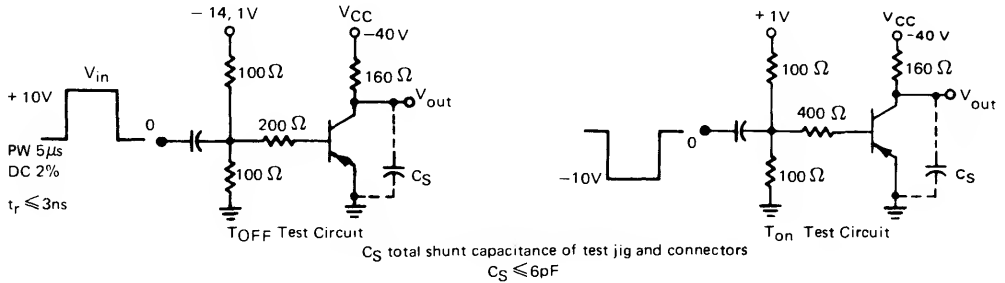
\* Pulse test - Pulse width ≤ 300 μs - Duty cycle 2%

**BCX46, BCX48, BCX50**

**ELECTRICAL CHARACTERISTICS** (continued) ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
<b>SMALL SIGNAL CHARACTERISTICS</b>					
Current Gain-Bandwidth Product ( $I_C = 50\text{ mAdc}$ , $V_{CE} = 2.0\text{ Vdc}$ , $f = 50\text{ MHz}$ )	$f_T$	60	130		MHz
Output Capacitance - Common Base ( $V_{CB} = 10\text{ Vdc}$ , $f = 1.0\text{ MHz}$ )	$C_{ob}$		9.0	15	pF
Input Capacitance - Common Base ( $V_{CB} = 0.5\text{ Vdc}$ , $f = 1.0\text{ MHz}$ )	$C_{ib}$		110		pF
Input Impedance ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 2.0\text{ Vdc}$ , $f = 1.0\text{ KHz}$ )	$h_{ie}$		700		ohms
Voltage Feedback Ratio ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 2.0\text{ Vdc}$ , $f = 1.0\text{ KHz}$ )	$h_{re}$		$1.7 \cdot 10^{-4}$		
Small-Signal Current Gain ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 2.0\text{ Vdc}$ , $f = 1.0\text{ KHz}$ )	$h_{fe}$		160		
Output Admittance ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 2.0\text{ Vdc}$ , $f = 1.0\text{ KHz}$ )	$h_{oe}$		110		$\mu\text{mho}$
Turn - On delay Time ( $V_{BE(\text{off})} = 0.5\text{ V}$ , $I_{B1} = 3\text{ mA}$ ) ( $V_{CC} = 40\text{ Vdc}$ , $I_C = 250\text{ mAdc}$ (see Figure 1))	$t_d$		12		ns
Rise Time ( $V_{BE(\text{off})} = 0.5\text{ V}$ , $I_{B1} = 3\text{ mA}$ ) ( $V_{CC} = 40\text{ Vdc}$ , $I_C = 250\text{ mAdc}$ (see Figure 1))	$t_r$		28		ns
Storage Time ( $I_{B1} = I_{B2} = 3\text{ mA}$ ) ( $V_{CC} = 40\text{ Vdc}$ , $I_C = 250\text{ mAdc}$ (see Figure 1))	$t_s$		330		ns
Fall Time ( $I_{B1} = I_{B2} = 3\text{ mA}$ ) ( $V_{CC} = 40\text{ Vdc}$ , $I_C = 250\text{ mAdc}$ (see Figure 1))	$t_f$		50		ns

**FIGURE 1 – SWITCHING TIME TEST CIRCUITS**



**FIGURE 2 – DC CURRENT GAIN**

