

45 V, 100 mA PNP general purpose transistor

10 July 2025

Product data sheet

1. General description

PNP general-purpose transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package. NPN complement: BCX70H-Q

2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 45 V)
- · Low noise
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low level, low noise, low frequency applications in hybrid circuits
- · General purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-100	mA
h _{FE}	DC current gain	$V_{CE} = -5 \text{ V; } I_{C} = -2 \text{ mA; } T_{amb} = 25 \text{ °C}$	180	-	310	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	_
2	Е	emitter		C
3	С	collector		В
			1 2	 E sym132
			SOT23	



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6. Marking

Table 3. Marking codes

Type number	Marking code[1]
BCX71H-Q	BH%

[1] % = placeholder for manufacturing site code

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-45	V
V _{CEO}	collector-emitter voltage	open base		-	-45	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-200	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -45 V; I _E = 0 A; T _{amb} = 25 °C		-	-	-20	nA
	current	V _{CB} = -45 V; I _E = 0 A; T _{amb} = 150 °C		-	-	-20	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -4 \text{ V; } I_{C} = 0 \mu\text{A; } T_{amb} = 25 ^{\circ}\text{C}$		-	-	-20	nA
h _{FE}	DC current gain	V_{CE} = -5 V; I_{C} = -10 μ A; T_{amb} = 25 °C		30	-	-	
		V_{CE} = -5 V; I_{C} = -2 mA; T_{amb} = 25 °C		180	-	310	
		V_{CE} = -1 V; I_{C} = -50 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C		80	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -10 mA; I_B = -0.25 mA; T_{amb} = 25 °C		-60	-	-250	mV
		I_C = -50 mA; I_B = -1.25 mA; pulsed; $t_p \le$ 300 μs; $\delta \le 0.02$; T_{amb} = 25 °C		-120	-	-550	mV
V _{BEsat}	base-emitter saturation voltage	I_C = -10 mA; I_B = -0.25 mA; T_{amb} = 25 °C		-600	-	-850	mV
		I_C = -50 mA; I_B = -1.25 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C		-680	-	-1050	mV
V _{BE}	base-emitter voltage	V_{CE} = -5 V; I_{C} = -2 mA; T_{amb} = 25 °C		-600	-650	-750	mV
		V_{CE} = -5 V; I_{C} = -10 μ A; T_{amb} = 25 °C	[1]	-	-550	-	mV
		V_{CE} = -1 V; I_{C} = -50 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02		-	-720	-	mV
C _c	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	4.5	-	pF
C _e	emitter capacitance	V_{EB} = -0.5 V; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	11	-	pF
f _T	transition frequency	V_{CE} = -5 V; I_{C} = -10 mA; f = 100 MHz; T_{amb} = 25 °C		100	-	-	MHz
NF	noise figure	V_{CE} = -5 V; I_{C} = -200 μ A; R_{S} = 2 k Ω ; f = 1 kHz; B = 200 Hz; T_{amb} = 25 °C		-	2	6	dB

^[1] V_{BE} decreases by about 2 mV/K with increasing temperature.

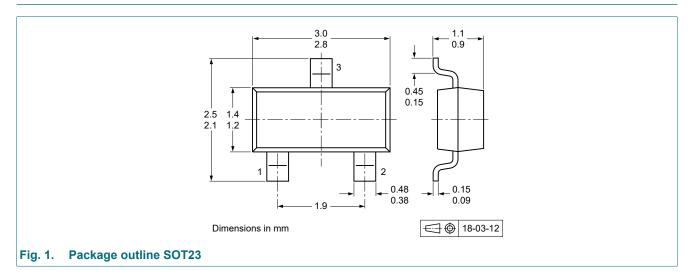
10. Test information

Quality information

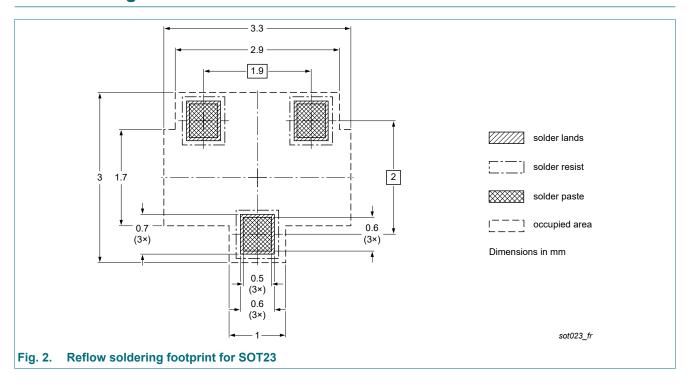
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 -Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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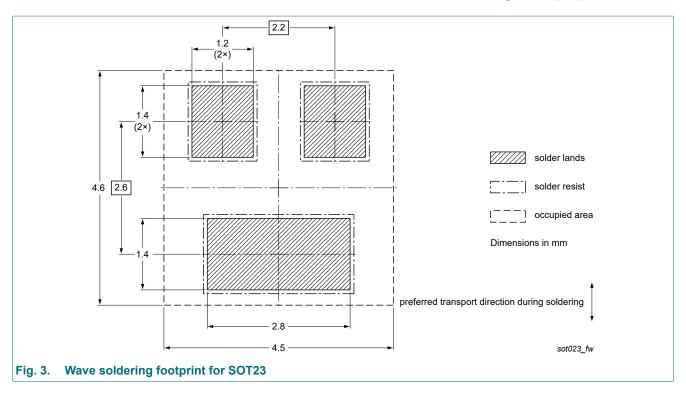
11. Package outline



12. Soldering



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13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCX71H-Q v.1	20250710	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BCX71H-C

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