BCX71K

45 V, 100 mA PNP general purpose transistor

10 July 2025 Product data sheet

1. General description

PNP general-purpose transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

NPN complement: BCX70K

2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 45 V)
- Low noise
- AEC-Q101 qualified

3. Applications

- Low level, low noise, low frequency applications in hybrid circuits
- General purpose switching and amplification

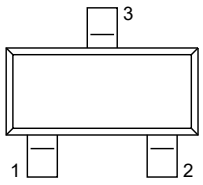
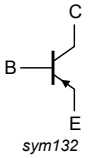
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-45	V
I_C	collector current			-	-	-100	mA
h_{FE}	DC current gain	V_{CE} = -5 V; I_C = -2 mA; T_{amb} = 25 °C		380	-	630	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym132
2	E	emitter		
3	C	collector		

6. Marking

Table 3. Marking codes

Type number	Marking code[1]
BCX71K	BK%

[1] % = placeholder for manufacturing site code

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-45	V
V_{CEO}	collector-emitter voltage	open base		-	-45	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-100	mA
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	-200	mA
I_{BM}	peak base current			-	-200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	250	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -45 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-20	nA
		V_{CB} = -45 V; I_E = 0 A; T_{amb} = 150 °C		-	-	-20	µA
I_{EBO}	emitter-base cut-off current	V_{EB} = -4 V; I_C = 0 µA; T_{amb} = 25 °C		-	-	-20	nA
h_{FE}	DC current gain	V_{CE} = -5 V; I_C = -10 µA; T_{amb} = 25 °C		100	-	-	
		V_{CE} = -5 V; I_C = -2 mA; T_{amb} = 25 °C		380	-	630	
		V_{CE} = -1 V; I_C = -50 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		110	-	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -10 mA; I_B = -0.25 mA; T_{amb} = 25 °C		-60	-	-250	mV
		I_C = -50 mA; I_B = -1.25 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-120	-	-550	mV
V_{BEsat}	base-emitter saturation voltage	I_C = -10 mA; I_B = -0.25 mA; T_{amb} = 25 °C		-600	-	-850	mV
		I_C = -50 mA; I_B = -1.25 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-680	-	-1050	mV
V_{BE}	base-emitter voltage	V_{CE} = -5 V; I_C = -2 mA; T_{amb} = 25 °C		-600	-650	-750	mV
		V_{CE} = -5 V; I_C = -10 µA; T_{amb} = 25 °C	[1]	-	-550	-	mV
		V_{CE} = -1 V; I_C = -50 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02		-	-720	-	mV
C_c	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	4.5	-	pF
C_e	emitter capacitance	V_{EB} = -0.5 V; I_C = 0 A; i_c = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	11	-	pF
f_T	transition frequency	V_{CE} = -5 V; I_C = -10 mA; f = 100 MHz; T_{amb} = 25 °C		100	-	-	MHz
NF	noise figure	V_{CE} = -5 V; I_C = -200 µA; R_S = 2 kΩ; f = 1 kHz; B = 200 Hz; T_{amb} = 25 °C		-	2	6	dB

[1] V_{BE} decreases by about 2 mV/K with increasing temperature.

10. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard*Q101 -Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

11. Package outline

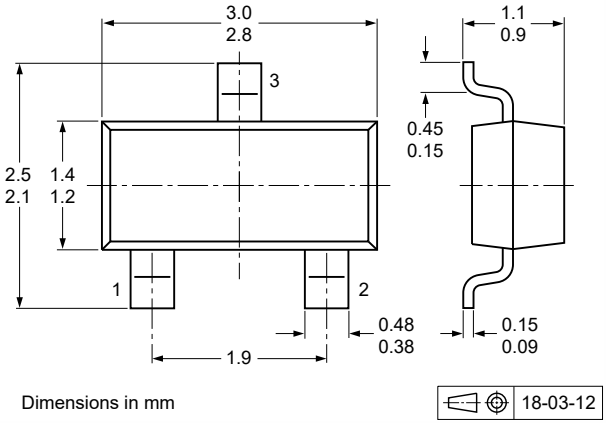


Fig. 1. Package outline SOT23

12. Soldering

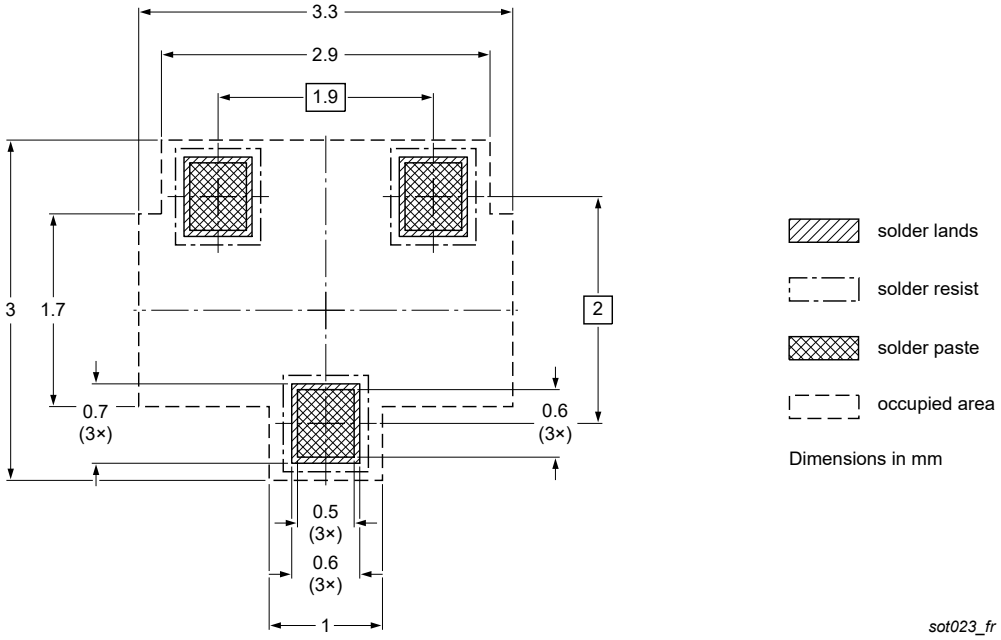


Fig. 2. Reflow soldering footprint for SOT23

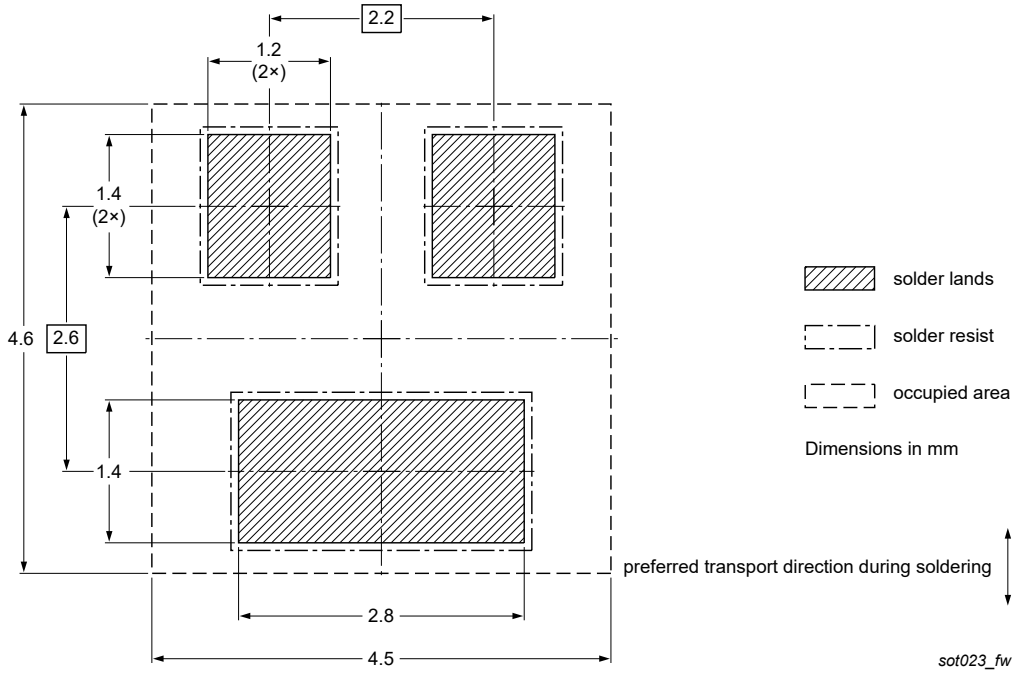


Fig. 3. Wave soldering footprint for SOT23

13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCX71K v.3	20250710	Product data sheet	-	BCX71 v.2
Modifications:	• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate. • Family data sheet split to single type data sheets.			
BCX71 v.2	20040216	Product data sheet	-	BCX71 v.1
BCX71 v.1	19990420	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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