

# Sound Processors for BOOM BOX / Mini-component Stereo

# Sound Processors with Built-in 2-band Equalizers





BD3870FS,BD3871FS,BD3872FS,BD3873FS

No.10086EAT02

# Description

The Sound Processor with built-in 2 Band Equalizer, incorporates functions required for BOOM BOX and mi ni-component systems, such as input se lector, input gain amp, vol ume, surround, tone (b ass, treble), and loud speaker equalizer (BD3872FS only). These functions can be controlled through the two-wire serial control.

#### Features

- 1) Using external components, center frequencies and Q factors of the bass characteristics are flexible.
- 2) Providing a mute switch onto one of the input pins allows cross-talk suppression.
- 3) A surround function can be constructed without external components.
- 4) Energy-saving design resulting in low current consumption, by utilizing the BiCMOS process

# Applications

BOOM BOX, mini-audio systems, and micro-audio systems.

# Line up matrix

Parameter	BD3870FS BD3	3 71FS BD3	3 73FS	BD3872FS
Input Gain	0, 6, 12, 18dB	24, 26, 28dB	18, 21, 24, 27dB	0, 5, 10, 19, 21, 23, 26, 28dB
Loud Speaker Equalizer (SEQ)	No	No	No	Yes
Input Selector	Three inputs	Three inputs	Three inputs	Five inputs
Volume	0 to -87dB/ 1dB step, -∞dB	0 to -87dB/ 1dB step, -∞dB	0 to –87dB/ 1dB step, -∞dB	0 to -87dB/ 1dB step, -∞dB
Equalizer	2 band (Bass, Treble)	2 band (Bass, Treble)	2 band (Bass, Treble)	2 band (Bass, Treble)
Surround	Yes	Yes	Yes	Yes
Serial Control	Two-wire serial	Two-wire serial	Two-wire serial	Two-wire serial
Package SSOP-A24		SSOP-A24 SSOI	P-A24	SSOP-A32

# ● Absolute maximum ratings (Ta=25°C)

Prameter S	ymbol		Ratings						
Frameter 3	ymboi	BD3870FS	BD3870FS BD3871FS BD38		BD3872FS	Unit			
Power Supply Voltage	Vcc	10	10	10	10	V			
Power Dissipation	Pd	800 * <sup>1</sup>	800 * <sup>1</sup>	800 *1	950 * <sup>2</sup>	mW			
Operating Temperature Range	Topr	-25 to +75	-25 to +75	-25 to +75	-25 to +75	°C			
Storage Temperature Range	Tstg	-55 to +125	-55 to +125	-55 to +125	-55 to +125	°C			

<sup>\*1</sup> Reduced by 8.0 mW/°C at 25°C or higher, when installed on the standard board (Size: 70 × 70 × 1.6mm).

# Operating voltage range

Prameter S	vmbol	Ratings						
Frameter 3	ymboi	BD3870FS	BD3871FS	BD3873FS	BD3872FS	Unit		
Operating Voltage Range	Vcc	4.5 to 9.5	4.5 to 9.5	4.5 to 9.5	4.5 to 9.5	V		

<sup>\*</sup>It must function normally at Ta=25°C.

<sup>\*2</sup> Reduced by 9.5 mW/°C at 25°C or higher, when installed on the standard board (Size: 70 × 70 × 1.6mm).

# Electrical characteristics

 $(Ta=25\ ^{\circ}\text{C}\ , \ VCC=\ 8V, \ f=\ 1kHz, \ V\ i=200mVrms, \ RL=\ 10k\Omega, \ Rg=\ 600\Omega, \ Input\ Gain=\ 0dB\ (BD387\ 0FS, \ BD3872F\ S\ onl\ y), \ Input\ Gain=\ 24dB\ (BD38\ 71FS\ onl\ y), \ Input\ Gain=\ 18dB\ (BD3873F\ S\ onl\ y), \ Volume=0dB, \ Bass=0dB, \ T\ reble=0dB, \ Surround=0FF, \ Loud\ Speaker\ Equalizer=0FF\ (BD3872FS\ only), \ unless\ otherwise\ noted)$ 

Jui	round=OFF, Loud Speaker Equali:	zei=OFF	(603012	ZF3 Ulliy	/), uriles:	Somerwis	se noteu)
Block	Parameter S	ymbol	,	Limits	Г	Unit C	on dition
В́	i didiliotoi o	yiiiooi	Min.	Тур.	Max.	Offic O	on altion
	Circuit Current	IQ	-	8	21	mA	At no signal
			-2 0		2		BD3870FS, BD3872FS Gv=20log(VOUT/VIN)
	Total Output Voltage Gain	Gv	22 24	26		dB	BD3871FS Gv=20log(VOUT/VIN)
			16 18	20	)		BD3873FS Gv=20log(VOUT/VIN)
	Total Harmonic Distortion ratio	THD	-	0.01	0.1	%	BW=400 to 30kHz
	Maximum Output Voltage	Vom	1.6	2.1	-	Vrms	THD=1% BW=400 to 30kHz
Total	Total Output Noise Voltage	Vno	- 4.5		15	μVrms	BD3870FS, BD3872FS Rg=0 $\Omega$ , BW=IHF-A
=	Total Output Holdo Voltage	VIIO	- 40		80	μνιιιο	BD3871FS, BD3873FS Rg=0Ω, BW=IHF-A
	Total Residual Noise Voltage	Vmno	-	4.5	15	μVrms	Rg=0Ω, BW=IHF-A Volume=-∞dB
	Cross-talk between Channels	СТС	-	-80	-70	dB	Rg=0Ω, BW=IHF-A VOUT=1Vrms
	Cross-talk between Selectors	CTS	-	-80	-70	dB	Rg=0Ω, BW=IHF-A
	Input Impedance	Rin	35 50 65			kΩ	BD3870FS, BD3871FS, BD3873FS
	mpat impodanto	1 (11)	70 10	0	130	1132	BD3872FS
	Output Impedance	Rout	-	-	50	Ω	
d)	Volume Control Range	VRI	-90	-87 -8	4	dB	VR=20log(VOUT/VIN)
Volume	Maximum Volume Attenuation	Vmin	-	-	-90	dB	BW=IHF-A
	Volume Input Impedance	Rvin	39	56	73	kΩ	
	Bass Boost Gain	GBB	12	14	16	dB	GB=20log(VOUT/VIN)
Bass	Bass Cut Gain	GBC	-16	-14	-12 dl	В	GB=20log(VOUT/VIN)
	Bass Step Resolution	BR	-	2	-	dB	
	Treble Boost Gain	GTB	10 12	14		dB	BD3870FS, BD3871FS, BD3873FS
m		3.0	12 14	16		45	BD3872FS
Treble	Treble Cut Gain	GTC	-14 -1	2 -10		dB	BD3870FS, BD3871FS, BD3873FS
	Tiobic out oaiii	510	-16 -1	4 -12		QD.	BD3872FS
	Treble Step Resolution	TR	-	2	-	dB	
ρι	Surround In-phase Gain	Vsur1	-2	0	2	dB	2ch in-phase inputs
Surround	Surround Single-phase Gain	Vsur2	4.3	6.3	8.3	dB	1ch input, 1ch grounded
Ō	Surround Opposite-phase Gain	Vsur3	8	10	12	dB	2ch opposite-phase inputs
SEQ	Loud Speaker Equalizer Gain (BD3872FS only)	Seq	357			dB	f=10kHz

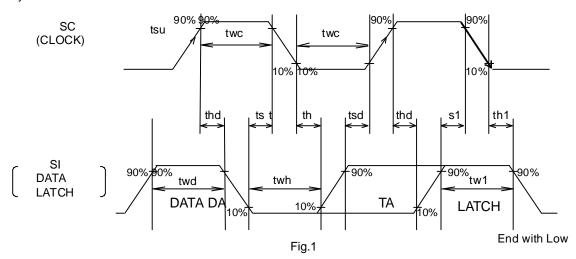
<sup>\*</sup> For measurement, VP-9690A (Average value wave detection, Effective value display) IHF-A filter by Matsushita Communication Industrial is used.

<sup>\*</sup> Phase relation between Input/Output signal terminals is the same.

<sup>\*</sup> Note: This IC is not designed to be radiation-resistant.

# ●Timing chart

- 1) Signal Timing Conditions
  - · Data is read on the rising edge of the clock.
  - Latch is read out on the falling edge of the clock.
  - · Latch signal must end with the LOW state.
  - To avoid malfunctions, clock and data signals must terminate with the LOW state.
     1byte=10bit



Parameter S	ymbol		Limits				
r arameter 3	ymboi	Min. T	yp.	Max.	Unit		
Minimum Clock Width	Twc	2.0	-	-	μs		
Minimum Data Width	twd	2.0	-	-	μs		
Minimum Latch Width	Tw1	2.0	-	-	μs		
Data Set-up Time (DATA→CLK)	Tsd	1.0 -		-	μs		
Data Hold Time (CLK→DATA)	Thd	1.0 -		-	μs		
Latch Set-up Time (CLK→LATCH)	Ts1	1.0	-	-	μs		
Latch Hold Time (DATA→LATCH)	Th1	1.0	-	-	μs		
Latch Low Set-up Time	Ts	1.0	-	-	μs		
Latch Low Hold Width	Twh	2.0	-	-	μs		

2) Voltage Conditions for Control Signals

		Limits				
Parameter	Min. T yp. Max. (≦Vcc)			Unit C	on dition	
"H" Input Voltage	2.2	-	5.5	V	Vcc=4.5 to 9.5V	
"L" Input Voltage	0	-	1.0	V	Vcc=4.5 to 9.5V	

# 3) Control Data Format List

Basic Configuration of Control Data Format

← Data input direction

MSB						LSB
D9	D8	D7 D6	D5	D4 D3	D2 D1	D0
			Data			Select Address

# Control Data Format (BD3870FS, BD3871FS, BD3873FS)

← Data input direction

· Data i	nput un ect	1011								
	D9	D8 D7		D6 D5	D4		D3	D2 D1	D0	
(1)	Input S	Input Selector Input Gain			Surround	*	*	*	0	0
(2) V		olume A				Volur	ne B	*	0	1
(3)		Bass Gain				Treble	Gain		1	0
(4)	*	* *		* * *			*	*	1	1

# Control Data Format (BD3872FS)

← Data input direction

	D9	D8	D7	D6	D5 D4	D3		D2 D1		D0
(1)		Input Gair	1	In	put Select	or	*	*	0	0
(2) V	olume A					Volume B *			0	1
(3)		Bass Gain				Treble	Gain		1	0
(4) *		*	*	*	*	*	Surround	SEQ	1	1

<sup>• &</sup>quot;\*" indicates 0 or 1.

←Input direction

MSB	LS	В	MSB	LS	B	MSB	LS	В	MSB	L	SB	
Data(1)	L		Data	(2)	L	Data	a(3) L		Data	a(4)	L	

<sup>&</sup>quot;L" indicates a latch.

• After power-on, for the second and subsequent times, only the necessary data can be selected for setting.

Example: When changing the volume,

←Input direction

MSB LSB

Data(2) L

"L" indicates a latch.

<sup>•</sup> By changing the setting of Select Address, three or four different control formats (BD3871FS, BD3872FS) are selectable.

In every power-on sequence, all of the address data must be initialized.
 Example:

●Block diagram, application circuit, pin assignment (BD3870FS, BD3871FS, BD3873FS)

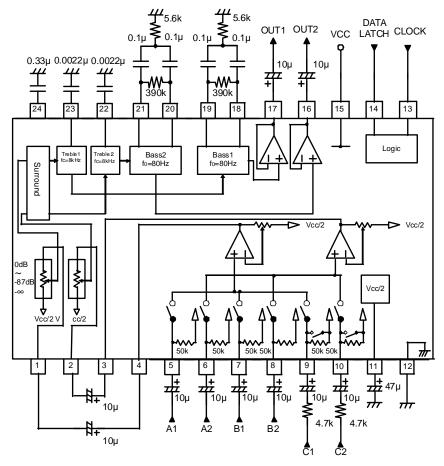


Fig.2

Unit: Resistance = $\Omega$ , Capacitor = F

# ●Pin description (BD3870FS, BD3871FS, BD3873FS)

Pin No.	Pin Name	Description	Pin No.	Pin Name	Description
1	VIN1	1ch volume input pin	13	SC	Serial clock input pin
2	VIN2	2ch volume input pin	14	SI	Serial data input pin
3	SEL2	2ch input selector output pin	15	VCC	Power supply pin
4	SEL1	1ch input selector output pin	16	OUT2	2ch output pin
5	A1	1ch input pin A	17	OUT1	1ch output pin
6	A2	2ch input pin A	18	BOUT1	1ch bass filter setting pin
7	B1	1ch input pin B	19	BNF1	1ch bass filter setting pin
8	B2	2ch input pin B	20	BOUT2	2ch bass filter setting pin
9	C1	1ch input pin C	21	BNF2	2ch bass filter setting pin
10	C2	2ch input pin C	22	TNF2	2ch treble filter setting pin
11	FILTER	1/2 VCC pin	23	TNF1	1ch treble filter setting pin
12 GI	ND	Ground pin	24	CAP	Time constant setting pin for absorbing surround switching shock sound

● Block diagram, application circuit, pin assignment (BD3872FS)

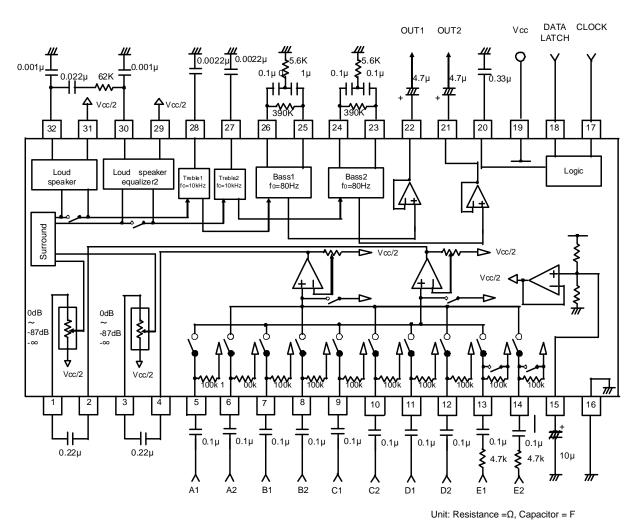
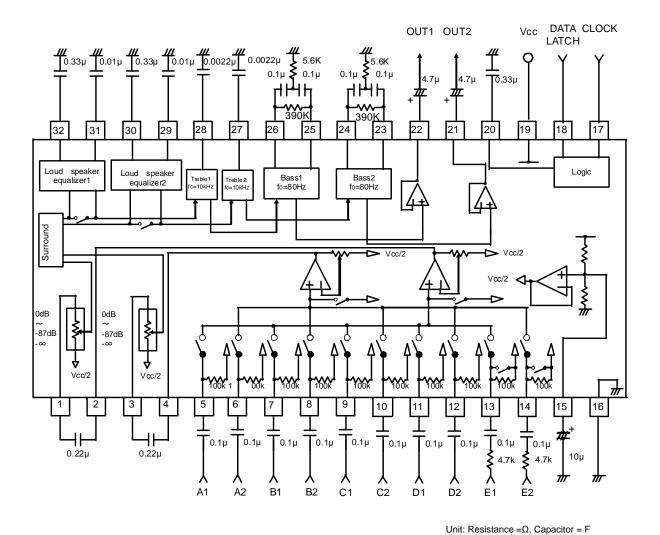


Fig.3 Application to Cinema Surround



Office Production = 12, Supuditor =

Fig.4 Application to Loud Speaker Equalizer

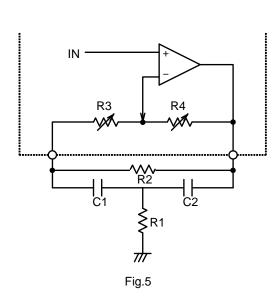
# ●Pin description (BD3872FS)

Pin No.	Pin Name	Description	Pin No.	Pin Name	Description
1	VIN1	1ch volume input pin	17	SC	Serial clock input pin
2	SEL1	1ch input selector output pin	18	SI	Serial data input pin
3	VIN2	2ch volume input pin	19	VCC	Power supply pin
4	SEL2	2ch input selector output pin	20	CAP	Time constant setting pin for absorbing switching shock sound
5	A1	1ch input pin A	21	OUT2	2ch output pin
6	A2	2ch input pin A	22	OUT1	1ch output pin
7	B1	1ch input pin B	23	BOUT2	2ch bass filter setting pin
8	B2	2ch input pin B	24	BNF2	2ch bass filter setting pin
9	C1	1ch input pin C	25	BOUT1	1ch bass filter setting pin
10	C2	2ch input pin C	26	BNF1	1ch bass filter setting pin
11	D1	1ch input pin D	27	TNF2	2ch treble filter setting pin
12	D2	2ch input pin D	28	TNF1	1ch treble filter setting pin
13	E1	1ch input pin E	29	SOUT2	2ch cinema surround or SEQ setting pin
14	E2	2ch input pin E	30	SQI2	2ch cinema surround or SEQ setting pin
15	FILTER	1/2 VCC pin	31	SOUT1	1ch cinema surround or SEQ setting pin
16	GND	Ground pin	32	SQI1	1ch cinema surround or SEQ setting pin

# Description of operations

# 1) Bassfilter

Using external components, center frequencies and Q factors of the bass characteristics are flexible



$$f_0 = \frac{1}{2 \pi \sqrt{\frac{R1R2(R3 + R4)C1C2}{R2 + R3 + R4}}}$$
 (Hz)

$$Q = \frac{\sqrt{\frac{R1R2R4C1C2}{R2 + R3 + R4}}}{R1(C1 + C2)}$$

$$G(boost) = 20log \frac{\frac{R2(R3 + R4)}{R1(R2 + R3 + R4)} + \frac{C2}{C1} + 1}{\frac{R2R3}{R1(R2 + R3 + R4)} + \frac{C2}{C1} + 1} (dB)$$

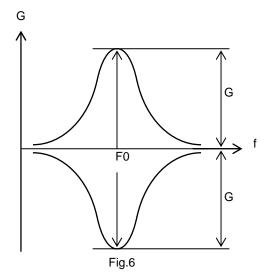
$$Gcut = 20log \frac{\frac{R2R3}{R1(R2 + R3 + R4)} + \frac{C2}{C1} + 1}{\frac{R2(R3 + R4)}{R1(R2 + R3 + R4)} + \frac{C2}{C1} + 1} (dB)$$

R1(R2+R3+R4)

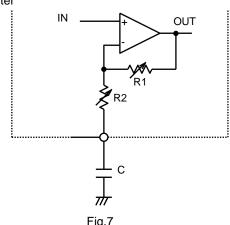
Standard values of R3, R4 (reference)  $(R1=5.6k\Omega, R2=390k\Omega, C1=C2=0.1\mu F)$ 

Bass Boost Cut Amount	Resistance (kΩ) * Typ.	
	R3	R4
0dB	77.64	0
2dB	58.90	18.73
4dB	44.02	33.60
6dB	32.20	45.42
8dB	22.82	54.80
10dB	15.36	62.26
12dB	9.44	68.18
14dB	4.78	72.84

<sup>\*</sup> The actual boost cut amount may deviate from the standard values in some degree.



# 2) Treble filter



$$fc = \frac{1}{2\pi R^2 C}$$
 (Hz)

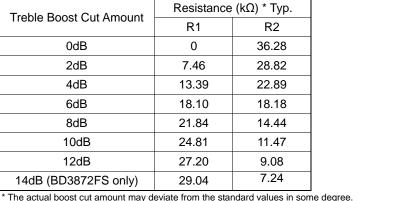
G (boost) = 
$$20\log \frac{R1+R2+Zc}{R2+Zc}$$
(dB)

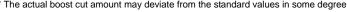
G (cut) = 
$$20\log \frac{R2+Zc}{R1+R2+Zc}$$
 dB)

$$Zc = \frac{1}{i\omega c}(\Omega)$$

Standard values of R1, R2 (reference)

Treble Boost Cut Amount	Resistance (kΩ) * Typ.	
	R1	R2
0dB	0	36.28
2dB	7.46	28.82
4dB	13.39	22.89
6dB	18.10	18.18
8dB	21.84	14.44
10dB	24.81	11.47
12dB	27.20	9.08
14dB (BD3872FS only)	29.04	7.24





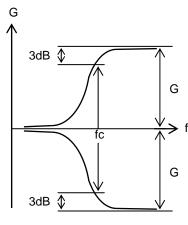
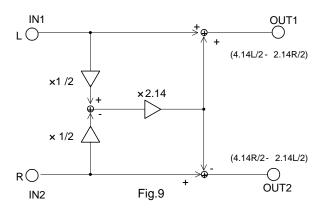


Fig.8

# 3) Matrix surround



Matrix surround is constructed as shown above. Gains are defined as follows:

In-phase Gain: Gains obt ained on OUT 1 and OUT 2 again st IN1 when inputting a signal of the equivalent level and in-phase (L=R) to IN1 and IN2.

$$\frac{\text{OUT1}}{\text{IN1}} = \frac{\text{OUT2}}{\text{IN2}} = \left(\frac{4.14}{2} \text{ L} - \frac{2.14}{2} \text{ L}\right) / \text{L} = 1$$
 20log1= 0 (dB)

Single-phase Gain: Gains obtained on OUT1 and OUT2 against IN1 when inputting a signal to IN1 and AC-grounding IN2

Opposite-phase Gain: Gains obtained on OUT1 and OUT2 against IN1 when inputting signals of the equivalent level and opposite-phase (L-R) to IN1 and IN2.

$$\frac{\text{OUT1}}{\text{IN1}} = \frac{\text{OUT2}}{\text{IN2}} = \left(\frac{4.14}{2} L + \frac{2.14}{2} L\right) / L = 3.14$$
 20log3.14= 10 (dB)

# 1) Sound generation using Loud Speaker Equalizer (BD3872FS)

Using external components, one of the foll owing two functions can be e stablished; one having an effect to localize the vocal forward and enhance it (Loud Speaker Equalizer) and the other having an effect to clarify the vocal quality and improve the Articulation Index (Cinema Surround). Those characteristic values can be adjusted by choosing the external components with desirable constants. Details of above two functions are described below:

#### Ocinema Surround

Constructing t he e xternal c omponents as sho wn b elow all ows the v ocals to be m oved for ward and enhanced. Surround effect with enhanced vocal is achieved by turning on the built-in surround simultaneously.

Merging this ef fect, while pla ying movi es on a DVD pla yer, will make vocal liste ning clearer and s urround ef fect more impactful.

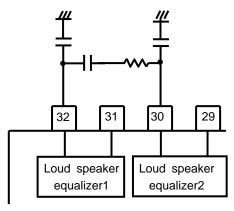


Fig.10

# OLoud Speaker Equalizer (SEQ)

BOOM BOX and Micro-sound systems are often restricted to using small-diameter speakers because of location space. These speakers, where the v ocal range are attenuated, may give an impression of having a muffled vocal sound with respect to its audibility. Using the Loud Speaker Equalizer, the vocal quality which degrades in small-diameter speakers, can be improved and the Articulation Index is also increased.

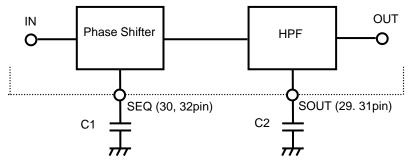


Fig.11

• C1 can be selected to determine the frequency where the phase should be inverted.

C1	Frequency where the phase is inverted
Larger value	Set to the lower frequency side
Smaller value	Set to the higher frequency side

· C2 can be selected to determine the HPF cut-off frequency fc. The SEQ gain remains constant.

$$fc = \frac{1}{2 \quad \pi RC} \quad (Hz)$$

(Where R is a built-in resistance of  $10k\Omega$ )

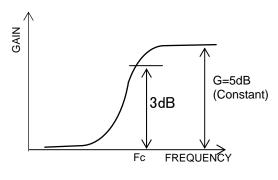


Fig.12

# ●Reference data

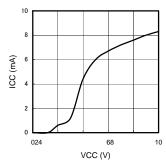


Fig.13 Circuit Current - Power Supply Voltage

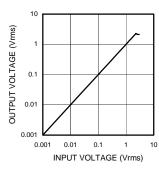


Fig.14 Output Voltage - Input Voltage (BD3870FS)

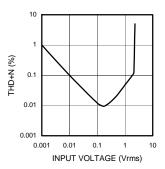


Fig.15 Total Harmonic Distortion ratio - Input Voltage (BD3870FS)

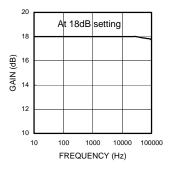


Fig.16 Current Gain - Frequency (BD3870FS, BD3873FS)

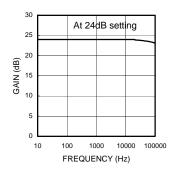


Fig.17 Voltage Gain - Frequency (BD3871FS)

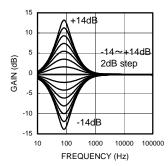


Fig.18 Bass Gain - Frequency

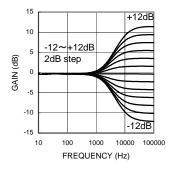


Fig.19 Treble Gain - Frequency (BD3870FS,BD3871FS,BD3873FS)

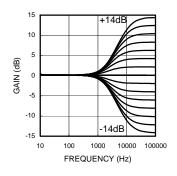


Fig.20 Treble Gain - Frequency (BD3872FS)

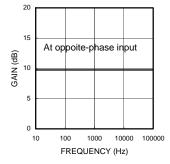


Fig.21 Surround Gain - Frequency

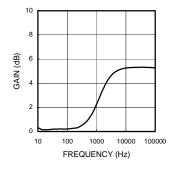


Fig. 22 Treble Gain - Frequency (BD3872FS)

#### Notes for use

- 1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- 2) Although ROHM is confident that the example application circuit reflects the best possible recommendations, be sure to verify circuit characteristics for your particular application. Modification of constants for other externally connected circuits may cause variations in both static and transient characteristics for external components as well as this Rohm IC. Allow for sufficient margins when determining circuit constants.

#### 3) Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings, such as the applied voltage or operating temperature range (Topr), may result in IC damage. Assumptions should not be made regarding the state of the IC (short mode or open mode) when such damage is suffered. A physical safety measure, such as a fuse, should be implemented when using the IC at times where the absolute maximum ratings may be exceeded.

#### 4) GND potential

Ensure a minimum GND pin potential in all operating conditions. Make sure that no pins are at a voltage below the GND at any time, regardless of whether it is a transient signal or not.

#### T hermal design

Perform thermal design, in which there are adequate margins, by taking into account the permissible dissipation (Pd) in actual states of use.

#### 6) Short circuit between terminals and erroneous mounting

Pay attention to the assembly direction of the ICs. Wrong mounting direction or shorts between terminals, GND, or other components on the circuits, can damage the IC.

# 7) Operation in strong electromagnetic field

Using the ICs in a strong electromagnetic field can cause operation malfunction.

#### 8) Po wer-ON RESET

A built-in circuit for performing initialization inside the IC at power-ON is provided. In unstable systems it is recommended that the data shall be sent to all the addresses during power-ON, until this operation cycle is completed. Mute should be applied during this cycle.

Function Initial	Condition
Input Selector	Input A
Input Gain	0dB
Loud Speaker Equalizer (BD3872FS only)	OFF
Volume	-∞dB
Surround	OFF
Treble Gain	0dB
Bass Gain	0dB

# 9) 2-wire serial control

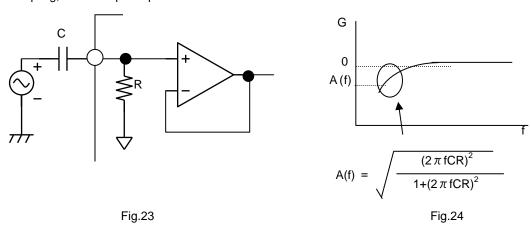
For the C L and DA terminals, the p atterned and other wirings should be routed as not to cause interference with the analog-signal-related lines.

# 10) Switching between functions

Shock sound is absorbed when switching between the volume, bass, and treble functions.

#### 11) Input coupling capacitor

As described in the figure below, low frequency characteristics are determined depending on the external capacitor value for input coupling, and the input impedance value inside the IC.



# 12) S witching noise

For Surround and Loud Speaker Equalizer (BD3872FS only), external capacitor C is attached to the CAP pin as a measures to control the switching noise. In the application circuit, a constant value as an example, is indicated by the CAP pin. The time constant, which is used to charge/discharge the external capacitor C (varying between Vbe to 5Vbe (2.65V)) on the CAP pin, controls the soft switching operation. The switching time constant T is stated as  $T=2.55\times10^5\times C$ . Vbe has a temperature characteristic and may affect the time constant T.

# 13) Input Selector and Input Gain

When setting/changing Input Selector or Input Gain, the soft switching operation does not function and therefore noise is not controlled. For this reason, it is recommended to provide a mute circuit constructed of the external components for the set design.

# MUTE setting example

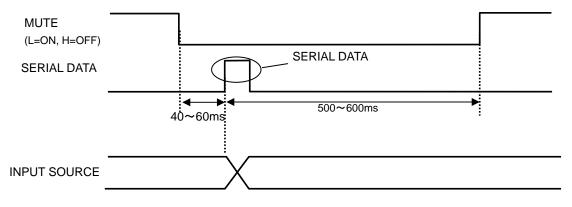
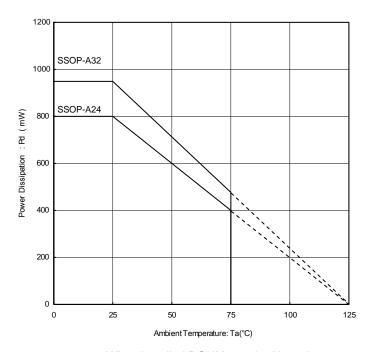


Fig.25

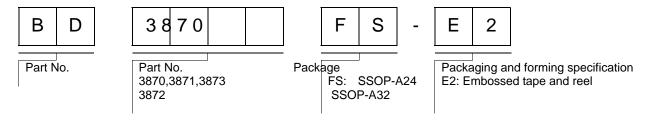
#### Thermal Derating Curve



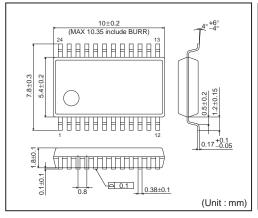
When installed ROHM standard board  $(70 \times 70 \times 1.6 \text{mm})$  Glass epoxy board)

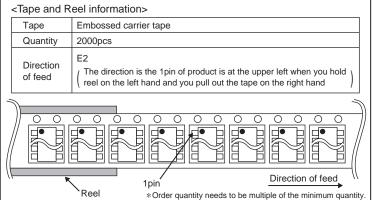
Fig.26

# Ordering part number

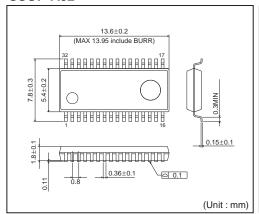


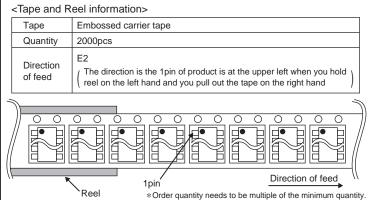
# SSOP-A24





# SSOP-A32





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CLASSIV CLASS	Ⅲ	

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  - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient lo ad (a large amount of load applied in a short period of time, such as pulse, is a pplied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in a ny way responsible or liable for failure induced under deviant condition from what is defined in this document.

# Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

# **Precautions Regarding Application Examples and External Circuits**

- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Prod ucts and e xternal components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is stron gly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is in dicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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#### **Precaution for Disposition**

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