

LED Drivers for LCD Backlights

White Backlight LED Drivers for Small to Medium LCD Panels (Switching Regulator Type)



BD6074GUT

No.11040EAT39

●Description

The BD6074GUT is a white LED driver IC with synchronous rectification that can drive up to 4LEDs. With synchronous rectification (no external schottky diode required) and small package, they can save mount space. And the brightness of LED can be adjusted by using PWM pulse on EN pin.

●Features

- 1) Synchronous rectification Boost DC/DC converter
- 2) No external schottky diode required
- 3) Driving 4 series white LEDs
- 4) Over voltage protection
- 5) Protect open and short output
- 6) Thermal shut down
- 7) Brightness adjustment by external PWM pulse
- 8) Small and Thin CSP package in 8pins

●Applications

White LED Backlight
Torch light and easy flash for camera of mobile phone

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit	Condition
Maximum applied voltage 1	VMAX1	7 *1	V	Vin, EN, VFB, TEST
Maximum applied voltage 2	VMAX2	20 *1	V	SW, Vout
Power dissipation	Pd	800 *2	mW	
Operating temperature range	Topr	-30~+85	°C	
Storage temperature range	Tstg	-55~+150	°C	

*1 These value are based on GND and GNDA pins.

*2 50mm×58mm×1.75mm At glass epoxy board mounting. When it's used by more than Ta=25 °C, it's reduced by 6.4mW/°C.

●Operating range (Ta=-30°C~+85°C)

Parameter	Symbol	Ratings			Unit	Condition
		Min.	Typ.	Max.		
Power supply voltage	Vin	2.7	3.6	5.5	V	

●Electrical characteristics

Unless otherwise specified Ta =-30°C ~+85°C, Vin=3.1~5.5V

Parameter	Symbol	Limits			Unit	Condition
		Min.	Typ.	Max.		
[EN terminal]						
EN threshold voltage (Low)	VthL	-	-	0.4	V	
EN threshold voltage (High)	VthH	1.4	-	-	V	
EN terminal input current	lin	-	18.3	30.0	μA	EN=5.5V
EN terminal output current	Iout	-2.0	-0.1	-	μA	EN=0
[Switching regulator]						
Quiescent Current	Iq	-	0.1	2.0	μA	EN=0V
Current Consumption	Idd	-	1.1	1.5	mA	EN=2.6V,VFB=1.0V,VIN=3.6V
Feedback voltage	Vfb	0.47	0.50	0.53	V	
Inductor current limit	Icoil	310	400	490	mA	Vin=3.6V *1
SW saturation voltage	Vsat	-	0.10	0.28	V	Isw=200mA, Vout=13V
SW on resistance P	Ronp	-	2.1	3.2	Ω	Ipch=200mA,Vout=13V
Switching frequency	fSW	0.8	1.0	1.2	MHz	
Duty cycle limit	Duty	82.7	92.5	-	%	VFB=0V
Output voltage range	Vo	-	-	18.0	V	
Over voltage limit	Ovl	18.0	18.5	19.0	V	VFB=0V
Start up time	Ts	-	0.5	1.0	ms	

*1 This parameter is tested with DC measurement.

● Test circuit

*Test circuit A (for Inductor current limit, Feedback voltage, Start up time.)

Procedure

~Inducton current limit~

1. Start to increase Iout from 0mA gradually.
2. You will find that Vout will start to go down and the duty will be decreased.
3. Then, you can measure the coil current as "inductor current limit"

~VFB voltage~

1. Supply 0mA to Iout
2. Then, you can measure the VFB voltage as "Feedback voltage".

~Start up time~

1. Supply the voltage to VIN
2. Then, you can measure the VFB waveform as "Start up time".

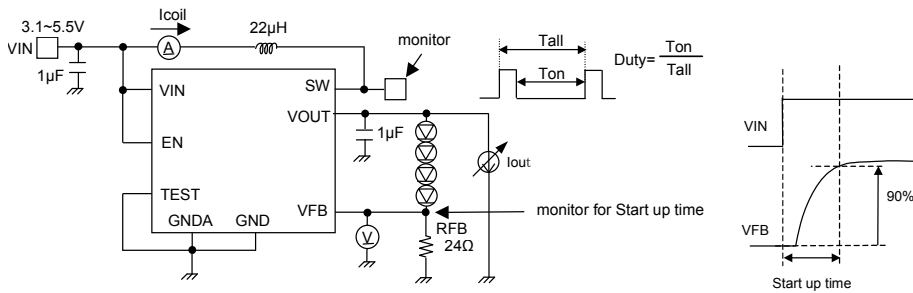


Fig.1 Test Circuit A

*Test circuit B (for Over voltage limit, Duty cycle limit, Switching frequency)

Procedure

~Over voltage limit~

1. Start to increase VOUT from 9V to 20V
2. You will find frequency change from around 1MHz to 0Hz
3. Then, it is "Over Voltage limit"

~Duty cycle limit, Switching frequency ~

1. Supply 9V to VOUT terminal
2. Then, you can measure the duty as "Duty cycle limit" and the frequency and "Switching frequency".

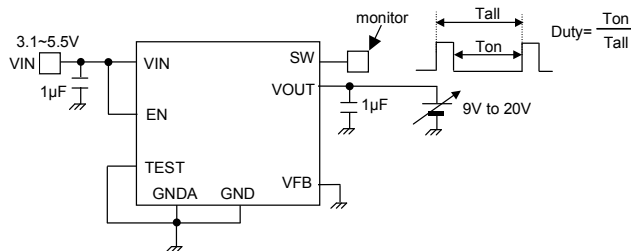


Fig.2 Test Circuit B

*TEST circuit C

(for Quiescent current, current consumption, EN Terminal input/output current, EN threshold voltage(Low/High))

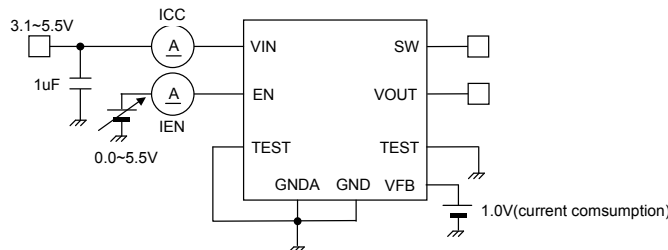


Fig.3 Test Circuit C

●Electrical characteristic curves (Reference data)

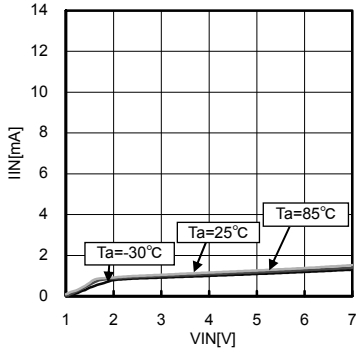


Fig.4 Current consumption vs. Power supply voltage

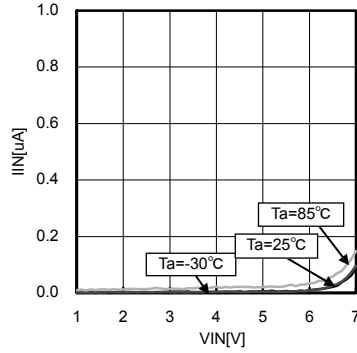


Fig.5 Quiescent current vs. Power supply voltage

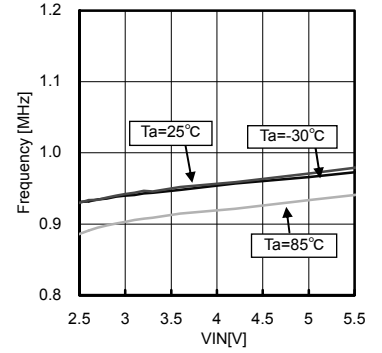


Fig.6 Oscillation frequency vs. Power supply voltage

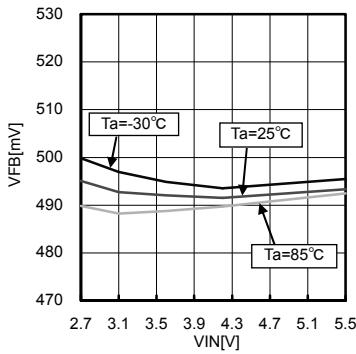


Fig.7 Feedback voltage vs. Power supply voltage

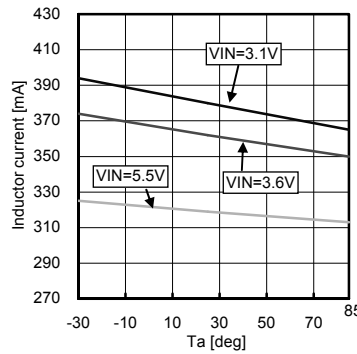


Fig.8 Inductor current limit vs. Temperature

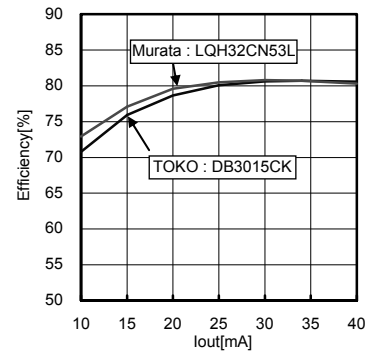


Fig.9 Efficiency vs. LED current (4LED=VOUT14V)

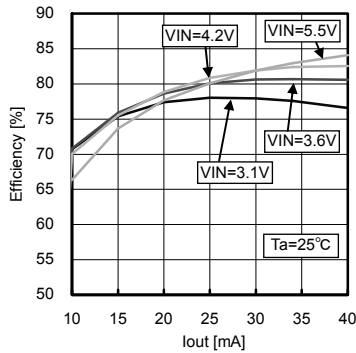


Fig.10 Efficiency vs. LED current (4LED=VOUT14V) coil : TOKO DB3015CK

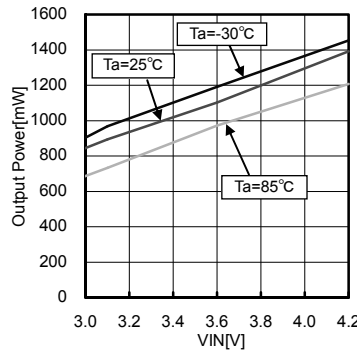


Fig.11 Output power vs. Power supply voltage coil : TOKO DB3015CK

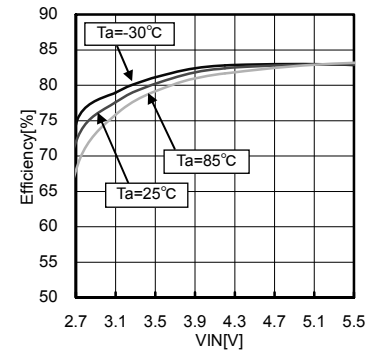


Fig.12 Efficiency vs. Power supply voltage (Load=34mA) coil : TOKO DB3015CK

●Electrical characteristic curves (Reference data) – Continued

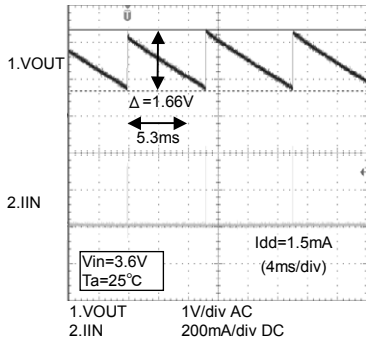


Fig.13 LED Open output voltage

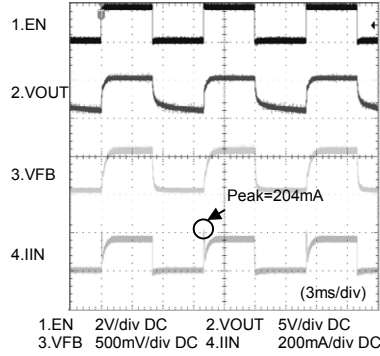


Fig.14 LED brightness adjustment

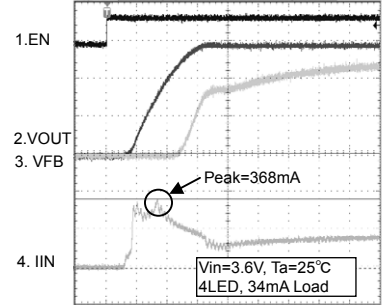


Fig.15 Soft Start

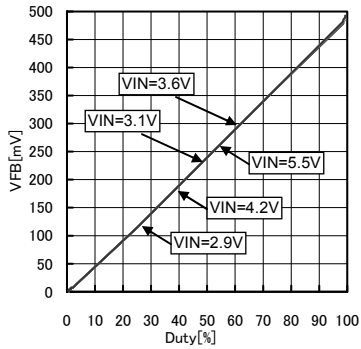


Fig.16 LED brightness adjustment for PWM control

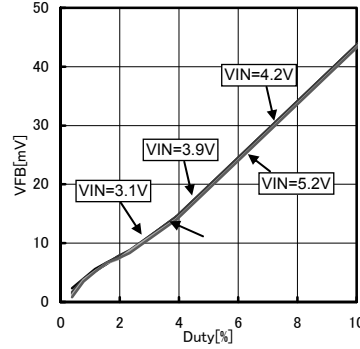


Fig.17 LED brightness adjustment for PWM control (Expansion)

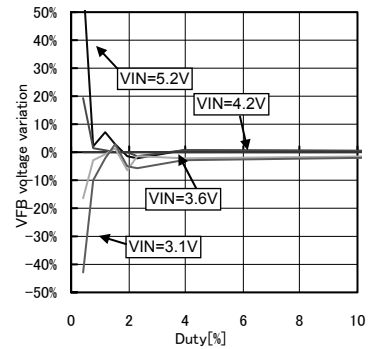


Fig.18 LED brightness variation for PWM control (comparison of VFB at VIN=3.9V)

●Block diagram and pin configuration

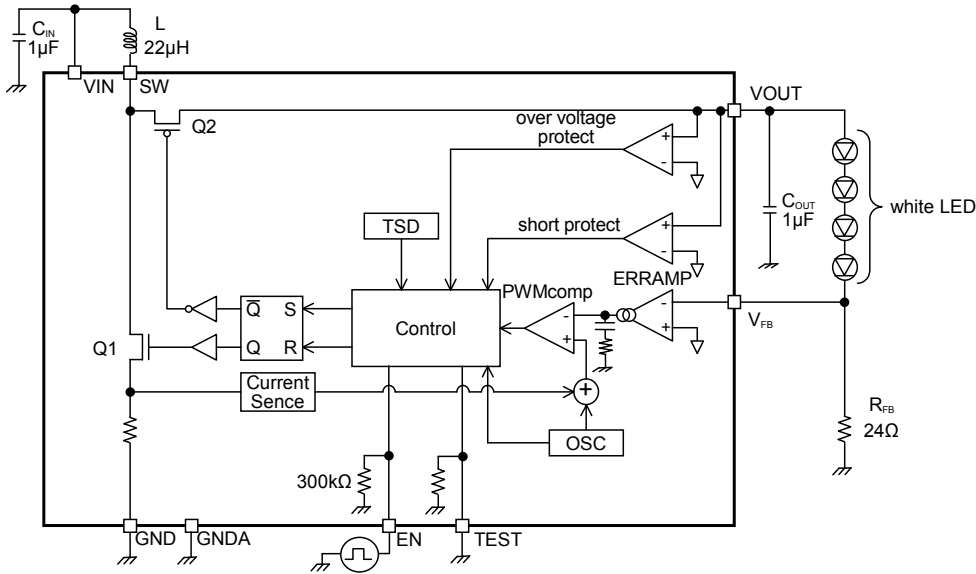


Fig.19 Block diagram and recommended circuit diagram

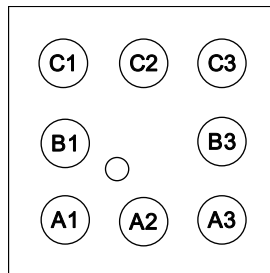


Fig.20 Pin location diagram VCSP60N1(8 pin)

●Pin assignment table

PIN Name	In/Out	Ball number	Function
GNDA	-	A1	Analog GND
EN	In	A2	Enable control (pull down by inner resistor)
TEST	In	A3	TEST input (pull down by inner resistor)
VIN	In	B1	Power supply input
VFB	In	B3	Feedback voltage input
VOUT	Out	C1	Boost output
SW	In	C2	Switching terminal
GND	-	C3	Power GND

●Operation

BD6074GUT is PWM current mode DC/DC converter with fixed frequency. It adopts synchronous rectification architecture. The feature of the PWM current mode is that input is the combination of error components from the error amplifier, and a current sense signal that controls the inductor current into Slope waveform for sub harmonic oscillation prevention. This output controls Q1 and Q2 via the RS latch. Timing of Q1 and Q2 is precisely adjusted so that they will not turn ON at the same time, thus putting them into non-overlapped relation. In the period when Q1 is ON, energy is accumulated in the external inductor, and in the period when Q1 is OFF, energy is transferred to the capacitor of VOUT via Q2. Further more, BD6074GUT has many safety functions, and their detection signals stop switching operation at once.

●Description of Functions

1)Soft start and off status

BD6074GUT has soft start function and off status function.

The soft start function and the off status function prevent large current from flowing to the IC via coil.

The soft start function prevents rush current when turning on and the off status function prevents invalid current when turning off.

• Soft start

When V_{OUT} is smaller than V_{short} , to decrease charge current PMOS is set to off by PMOS Startup Control (in Term "I"). V_{short} means "VOUT short detect voltage".

After V_{OUT} is bigger than V_{short} , PMOS is turned on and start switching. In term "II" ($V_{short} < V_{OUT} < V_{IN}$), status of Current Limiter is "soft mode". So "A" voltage is restricted and "D" duty is kept low. Therefore V_{OUT} voltage goes up slowly and coil current is restricted.

In term III ($V_{OUT} > V_{IN}$), status of Current Limiter is "normal mode". So "A" voltage goes up suitable voltage, and "D" duty goes up slowly. And then V_{OUT} voltage goes up to required voltage.

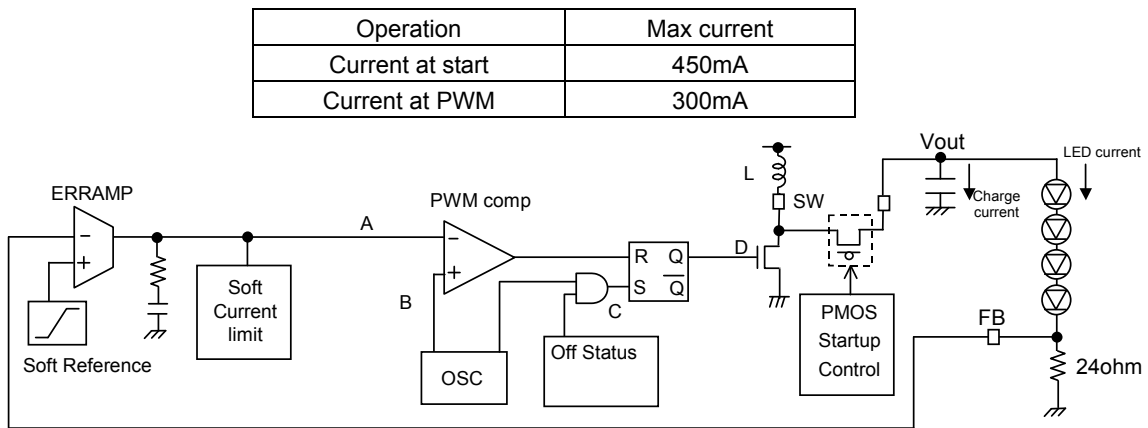


Fig. 21 lock diagram of soft start and off status

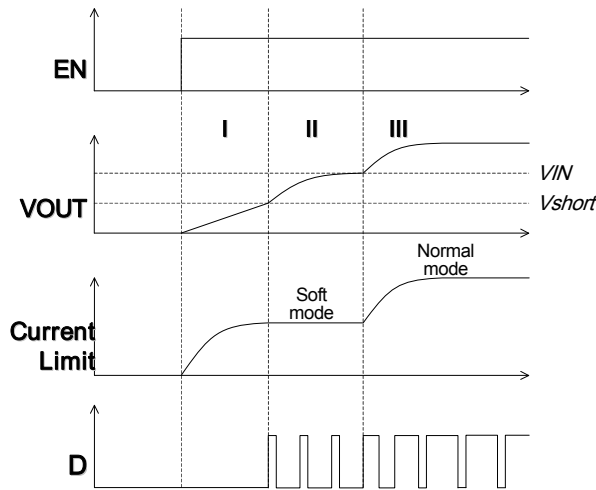


Fig. 22 timing chart

• Off status

The gate voltage of the switching Tr either "H" or "L" at power off depends on the operation conditions at that time. When it is fixed to "H", the switching Tr remains to be ON, and invalid current from the battery is consumed. In order to prevent this, at power off, D is always fixed to L level. So that, it is possible to prevent invalid current at power off.

2) Isolation control

BD6074GUT has isolation control to prevent LED wrong lighting at power off. The cause of the LED wrong lighting is leak current from VIN to the white LED. Therefore, when BD6074GUT powered off (EN = L), the isolation control cuts the DC path between SW and Vout, so that, it prevents from leak current from VIN to LED.

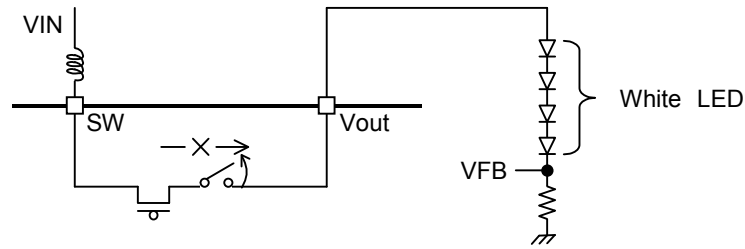


Fig.23 Isolation control

3) Short-circuit protection and over voltage protection

BD6074GUT has short-circuit protection and over voltage protection. These detect the voltage of VOUT, and at error, they stop the output Tr. Details are as shown below.

• Short-circuit protection

In the case of short-circuit of the DC/DC output (VOUT) to GND, the coil or the IC may be destroyed.

Therefore, in a case of error as VOUT becomes 0.7V or lower, the Under Detector shown in the figure works, and turns off the output Tr, and prevent the coil and the IC from being destroyed.

And the IC turns into non operation condition from operation condition, and current does not flow to the coil (0mA).

• Over voltage protection

In a case of error as the IC and the LED being cut off, over voltage causes the SW terminal and the VOUT terminal exceed the absolute maximum ratings, and may destroy the IC. Therefore, when VOUT becomes 18.5V or higher, the over voltage limits works, and turns off the output Tr, and prevents the SW terminal and the VOUT terminal from exceeding the absolute maximum ratings.

At this moment, turns into non operation condition from operation condition, and the output voltage goes down slowly. And, when the output voltage becomes the hysteresis of the over voltage limit or below, the output voltage goes on up to 18.5V once again.

This protection action is shown in Fig.24.

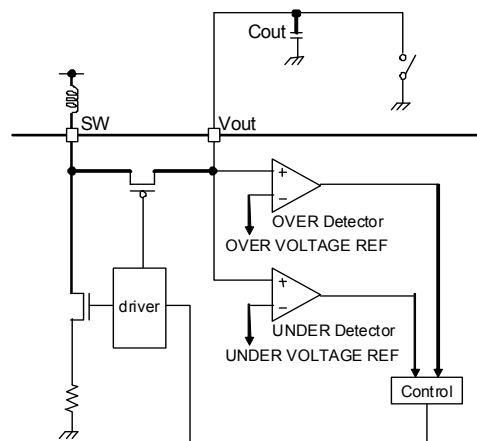


Fig.24 Block diagram of short-circuit protection and over voltage

4) Thermal shut down

BD6074GUT has thermal shut down function.

The thermal shut down works at 175°C or higher, and while holding the setting of EN control from the outside, turns into non operation condition from operation condition. And at 175°C or below, the IC gets back to its normal operation.

●Start control and brightness control

BD6074GUT can control the start conditions by its EN terminal, and power off at 0.4V or below, and power on at 1.4V or higher. And by changing the duty of power on and off by PWM control, the LED brightness can be adjusted.

Two techniques are available for the brightness adjustment. One is PWM adjustment, and the other is analog adjustment.

(1) PWM brightness adjustment is done by giving PWM signal to EN as shown in Fig.25.

The BD6074GUT is powered on/off by the PWM signal. By this method, LED current is controlled from 0 to the maximum current. The average LED current increases with proportion to the duty cycle of PWM signal. While in PWM off-cycle mode, the IC and LED both consume no currents, thus providing a high-efficiency operation. The recommended PWM frequency is 100Hz ~ 300Hz.

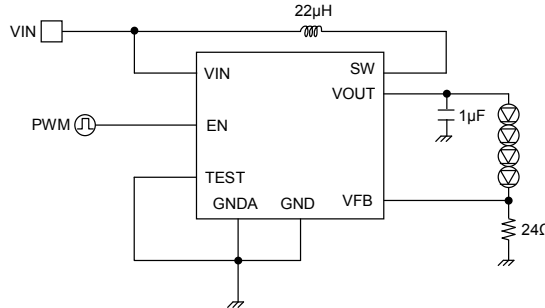


Fig.25 The brightness adjustment example of EN terminal by PWM (fPWM = 100 ~ 300Hz)

(2-1) Analog brightness adjustment is made by giving DC control voltage to VFB pin of BD6074GUT via a series resistor as shown in Fig.26. LED brightness (current) changes by giving DC voltage to VFB directly. DC voltage is given from filtered one of DAC signal, or PWM signal shown in Fig.28. The advantage of this approach is that the PWM signal to be used to control the LED brightness can be set to a high frequency (1kHz or higher).

LED current (ILED) is approximated by the following equation.

$$I_{LED} = \frac{[(V_{FB} - DAC) / R1] * R2 + V_{FB}}{R_{FB}}$$

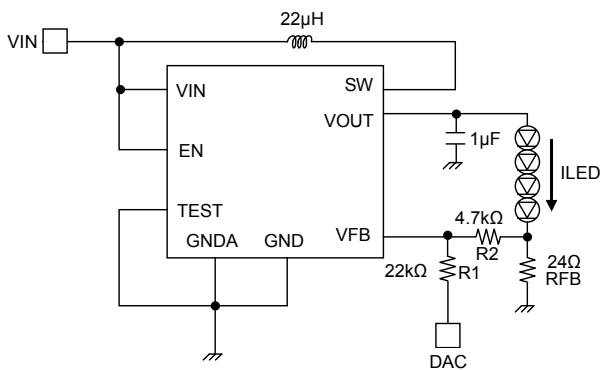


Fig.26 The brightness adjustment example by DAC

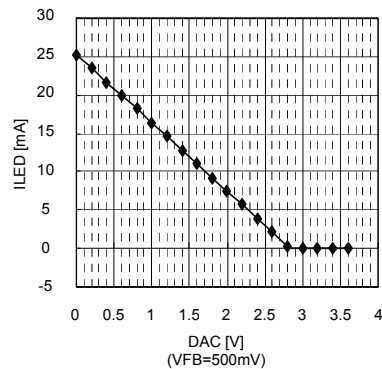


Fig.27 DAC adjustment

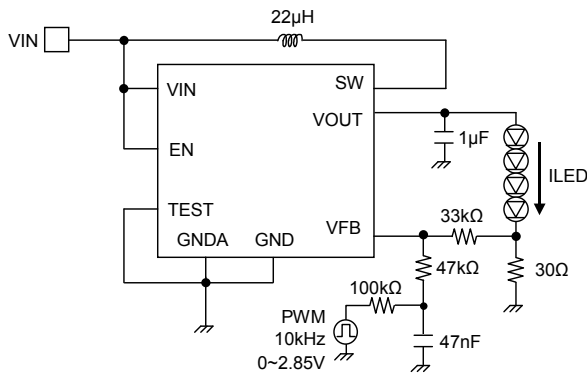


Fig.28 The brightness adjustment example of VFB terminal by PWM (f_{PWM} = 10kHz)

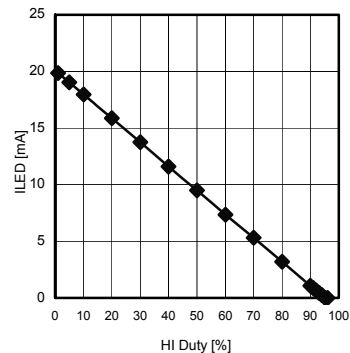


Fig.29 VFB PWM Control

2-2) The below the brightness adjustment is done in adjusting of R2 ON time at Duty cycle of R1 and PWM.
 The minimum value of the LED current is decided by V_{FB} / R_1 at the PWM 0%, the maximum value of the LED current is decided by V_{FB} / R_2 at the PWM 100%.
 I_{LED} is given as shown below.

$$I_{LED} = V_{FB} / R_1 + V_{FB} / R_2 \times \text{HI Duty}$$

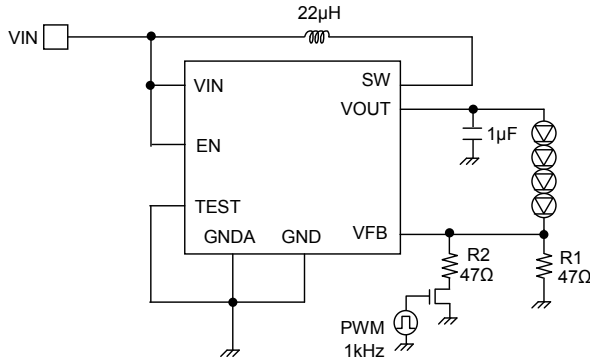


Fig.30 the brightness adjustment example of VFB terminal by PWM
 ($f_{PWM} = 100 \sim 1\text{kHz}$)

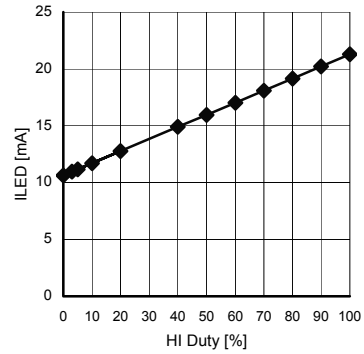


Fig.31 FB PWM Control

● Setting range of LED current

LED current is determined by the voltage of VFB and the resistor connected to VFB terminal.

I_{LED} is given as shown below.

$$I_{LED} = V_{FB} / R_{FB}$$

The current in the standard application is as shown below.

$$V_{FB} = 0.5\text{V}, R_{FB} = 24\Omega$$

$$I_{LED} = 20.8\text{mA}$$

Reference

The maximum value of R_{FB} is 30Kohm and minimum current of I_{LED} is 16uA.

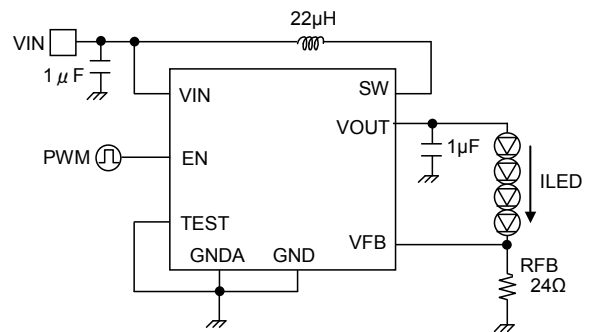


Fig.32 standard application

The shaded portion in the figure below is the setting range of LED current to become the standard. Depending on coils and white LEDs to be used, however, some ICs may not be used at desired currents. Consequently, for the proper setting of LED current, thoroughly check it for the suitability under use conditions including applicable power supply voltage and temperature.

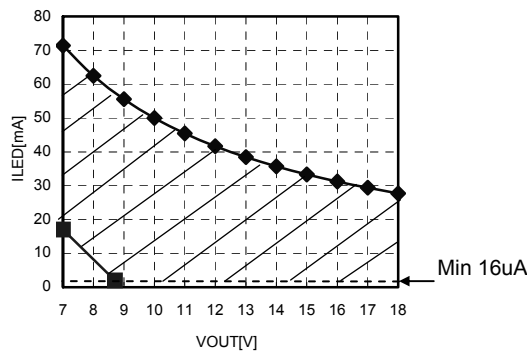


Fig.33 Setting range of LED current

● Selection of external parts

Recommended external parts are listed as below.

When to use other parts than these, select the following equivalent components.

• Coil

Value	Tolerance	Manufacturer	Product number	Size			DCR (Ω)
				Vertical size	Horizontal size	Height	
22 μ H	$\pm 10\%$	MURATA	LQH32CN220K53	2.5	3.2	1.55	0.71
22 μ H	$\pm 20\%$	TDK	VLF3012AT220MR33	2.6	2.8	1.2	0.66
22 μ H	$\pm 20\%$	Coil Craft	DO1608	4.45	6.6	2.92	0.37
22 μ H	$\pm 20\%$	TDK	VLF3010AT220MR33	2.6	2.8	1.0	1.30

Please refer to the reference data of p.4 for the change in the efficiency when the coil is changed.

• Capacitor

Value	Manufacturer	Product number	Size			Temperature range
			Vertical size	Horizontal size	Height	
【 CIN 】						
1 μ F	MURATA	GRM188B11A105K	1.6	0.8	0.8	-25 $^{\circ}$ C~+85 $^{\circ}$ C
【 COUT 】						
1 μ F	MURATA	GRM188B11E105K	1.6	0.8	0.8	-25 $^{\circ}$ C~+85 $^{\circ}$ C

• Resistor

Value	Tolerance	Manufacturer	Product number	Size		
				Vertical size	Horizontal size	Height
【 R _{FB} 】						
24 Ω	$\pm 1\%$	ROHM	MCR006YZPF24R0	0.6	0.3	0.23

The coil is the component that is most influential to efficiency. Select the coil which direct current resistor (DCR) and current - inductance characteristic are excellent. The BD6074GUT are designed for the inductance value of 22 μ H. Please do not use other inductance value. Select a capacitor of ceramic type with excellent frequency and temperature characteristics. Further, select Capacitor to be used for CIN/COUT with small direct current resistance, and pay much attention to the PCB layout shown in the next page.

●PCB layout

In order to make the most of the performance of this IC, PCB layout is very important.

Please note that characteristics such as efficiency and ripple will likely to change greatly depending on PCB layout.

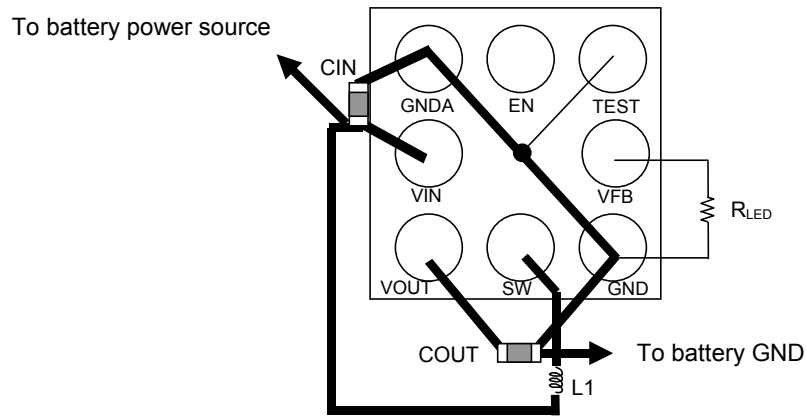
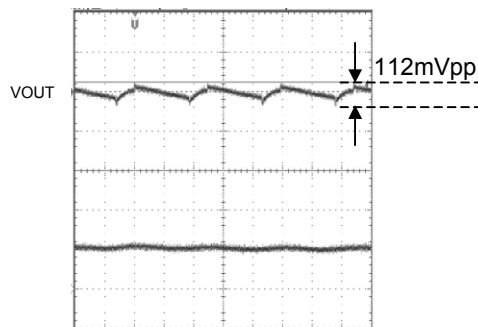


Fig.34 PCB layout

Connect the input bypass capacitor C_{IN} between V_{IN} and GND_A pin closely, as shown in the upper diagram. Thereby, the input voltage ripple of the IC can be reduced. And, connect the output capacitor C_{OUT} between V_{OUT} and GND pin closely. Thereby, the output voltage ripple of the IC can be reduced. Connect the current setting R_{LED} FB pin closely. Connect the GND closely connection side of R_{LED} directly to GND pin. Connect the GND_A pin directly to GND pin. When those pins are not connected directly near the chip, the performance of BD6074GUT shall be influenced and may limit the current drive performance. As for the wire to the inductor, make its resistance component small to reduce electric power consumption and increase the entire efficiency. Please keep away which are subject to be influenced like FB pin in wire connection with SW.

The layout pattern in consideration of these is shown in the next page.



($V_{BAT}=3.6V$, $T_a=25^\circ C$, $V_{OUT}=14V$. 20mA Load)

Fig.35 Output noise

●Recommended layout pattern

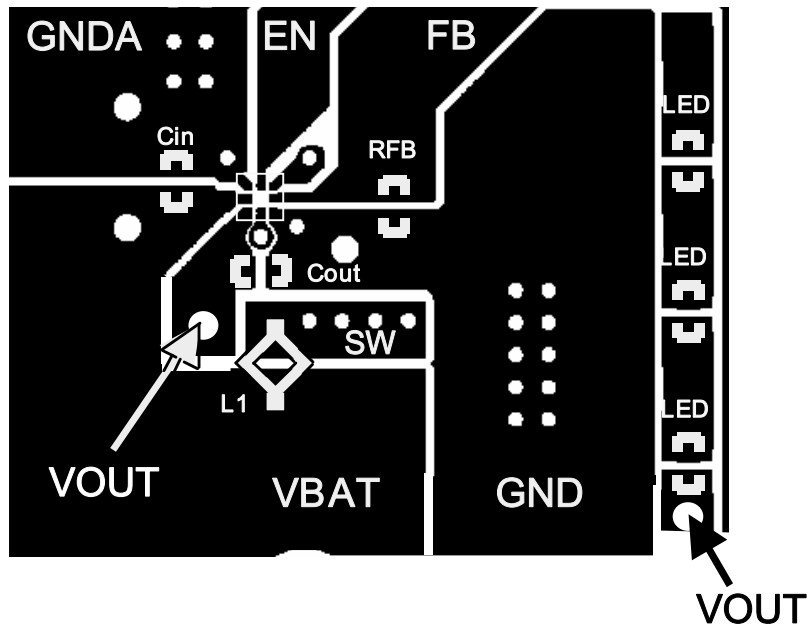


Fig.36 Front surface (TOP VIEW)

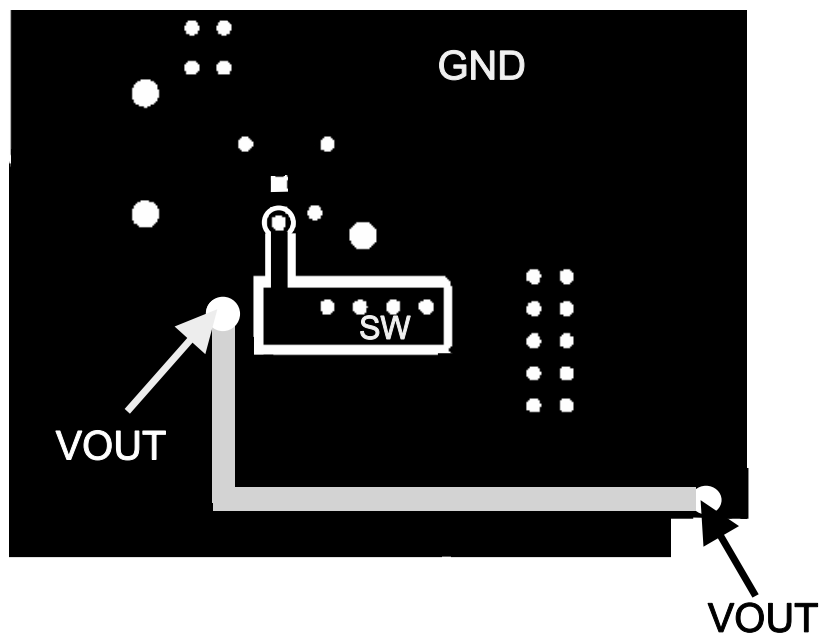


Fig.37 Rear surface (TOP VIEW)

●Attention point for PCB layout

For PCB design, the wire of power supply line should be low Impedance, and put bypass capacitor if necessary. Especially the wiring impedance must be low around DC/DC converter.

●About heat loss

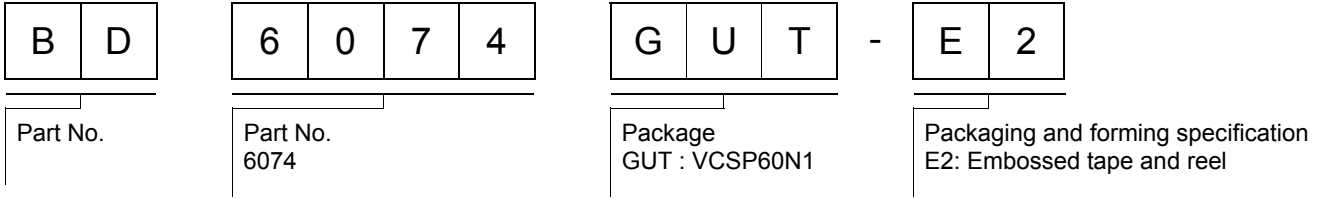
For heat design, operate DC/DC converter in the following condition.
(The following temperature is a guaranteed temperature, margin will be needed.)

1. Periphery temperature T_a must be less than 85°C .
2. The loss of IC must be less than dissipation P_d .

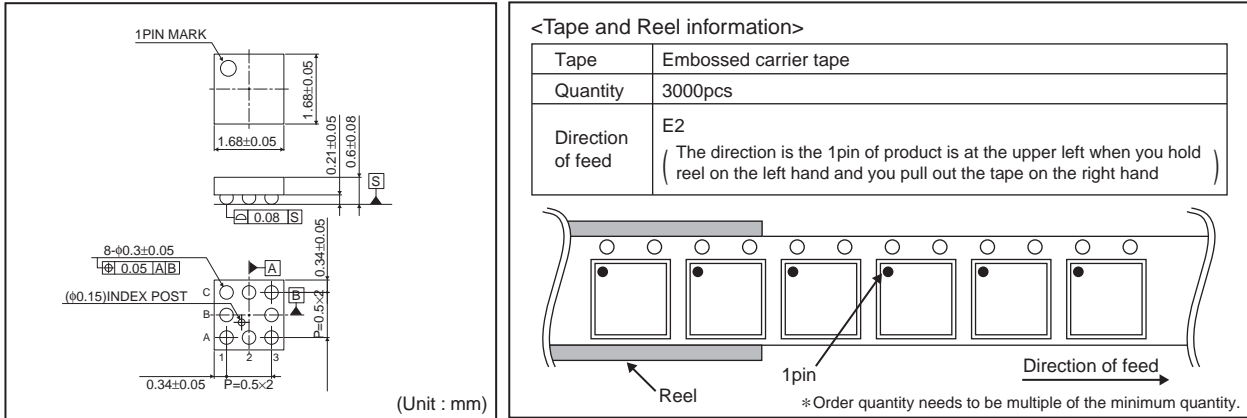
●Notes for use

- 1) Absolute Maximum Ratings
An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.
- 2) Operating conditions
These conditions represent a range within which characteristics can be provided approximately as expected. The electrical characteristics are guaranteed under the conditions of each parameter.
- 3) Reverse connection of power supply connector
The reverse connection of power supply connector can break down ICs. Take protective measures against the breakdown due to the reverse connection, such as mounting an external diode between the power supply and the IC's power supply terminal.
- 4) Power supply line
Design PCB pattern to provide low impedance for the wiring between the power supply and the GND lines. In this regard, for the digital block power supply and the analog block power supply, even though these power supplies has the same level of potential, separate the power supply pattern for the digital block from that for the analog block, thus suppressing the diffraction of digital noises to the analog block power supply resulting from impedance common to the wiring patterns. For the GND line, give consideration to design the patterns in a similar manner.
Furthermore, for all power supply terminals to ICs, mount a capacitor between the power supply and the GND terminal. At the same time, in order to use an electrolytic capacitor, thoroughly check to be sure the characteristics of the capacitor to be used present no problem including the occurrence of capacity dropout at a low temperature, thus determining the constant.
- 5) GND voltage
Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no terminals are at a potential lower than the GND voltage including an actual electric transient.
- 6) Short circuit between terminals and erroneous mounting
In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.
- 7) Operation in strong electromagnetic field
Be noted that using ICs in the strong electromagnetic field can malfunction them.
- 8) Inspection with set PCB
On the inspection with the set PCB, if a capacitor is connected to a low-impedance IC terminal, the IC can suffer stress. Therefore, be sure to discharge from the set PCB by each process. Furthermore, in order to mount or dismount the set PCB to/from the jig for the inspection process, be sure to turn OFF the power supply and then mount the set PCB to the jig. After the completion of the inspection, be sure to turn OFF the power supply and then dismount it from the jig. In addition, for protection against static electricity, establish a ground for the assembly process and pay thorough attention to the transportation and the storage of the set PCB.
- 9) Input terminals
In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals, such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.
- 10) Ground wiring pattern
If small-signal GND and large-current GND are provided, It will be recommended to separate the large-current GND pattern from the small-signal GND pattern and establish a single ground at the reference point of the set PCB so that resistance to the wiring pattern and voltage fluctuations due to a large current will cause no fluctuations in voltages of the small-signal GND. Pay attention not to cause fluctuations in the GND wiring pattern of external parts as well.
- 11) External capacitor
In order to use a ceramic capacitor as the external capacitor, determine the constant with consideration given to a degradation in the nominal capacitance due to DC bias and changes in the capacitance due to temperature, etc.
- 12) Thermal shutdown circuit (TSD)
When junction temperatures become 175°C (typ) or higher, the thermal shutdown circuit operates and turns a switch OFF. The thermal shutdown circuit, which is aimed at isolating the LSI from thermal runaway as much as possible, is not aimed at the protection or guarantee of the LSI. Therefore, do not continuously use the LSI with this circuit operating or use the LSI assuming its operation.
- 13) Thermal design
Perform thermal design in which there are adequate margins by taking into account the permissible dissipation (Pd) in actual states of use.
- 14) Selection of coil
Select the low DCR inductors to decrease power loss for DC/DC converter.

●Ordering part number



VCSP60N1 (BD6074GUT)



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