

isc Silicon PNP Darlington Power Transistor

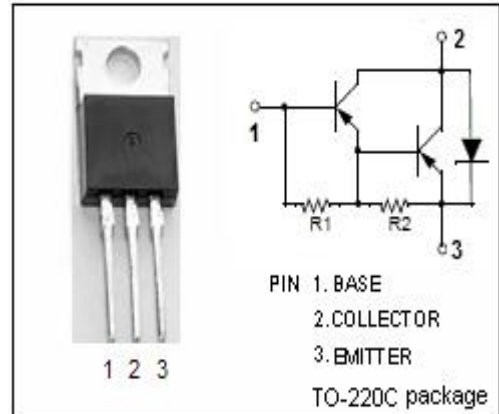
BD650

DESCRIPTION

- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -100V(\text{Min})$
- High DC Current Gain
: $h_{FE} = 750(\text{Min}) @ I_C = -3A$
- Low Saturation Voltage
- Complement to Type BD649
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

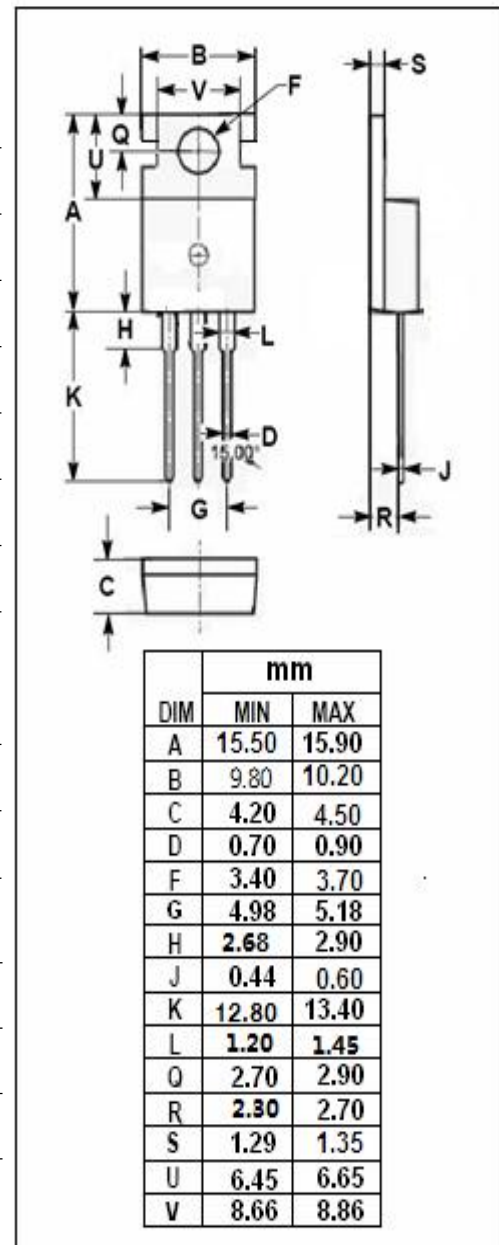
APPLICATIONS

- Designed for use as complementary AF push-pull output stage applications



ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-120	V
V_{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-8	A
I_{CP}	Collector Current-Peak	-12	A
I_B	Base Current-Continuous	-0.3	A
P_C	Collector Power Dissipation @ $T_a = 25^\circ\text{C}$	2	W
	Collector Power Dissipation @ $T_c = 25^\circ\text{C}$	62.5	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~150	$^\circ\text{C}$



THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	2	$^\circ\text{C/W}$
$R_{th\ j-a}$	Thermal Resistance, Junction to Ambient	62.5	$^\circ\text{C/W}$

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ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Breakdown Voltage	I _C = -30mA; I _B = 0	-100			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -12mA			-2.0	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -50mA			-2.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -5A; I _B = -50mA			-3.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -3A ; V _{CE} = -3V			-2.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-0.2	mA
		V _{CB} = -60V; I _E = 0; T _C = 150°C			-2.0	
I _{CEO}	Collector Cutoff Current	V _{CE} = -50V; I _B = 0			-0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-5	mA
h _{FE}	DC Current Gain	I _C = -3A ; V _{CE} = -3V	750			

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