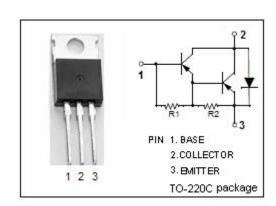


# **isc Silicon PNP Darlington Power Transistor**

**BD702** 

### **DESCRIPTION**

- Collector-Emitter Sustaining Voltage-
- : V<sub>CEO(SUS)</sub> = -100V(Min.)
- DC Current Gain-
  - :  $h_{FE} = 750(Min) @ I_{C} = -3A$
- · 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



## **APPLICATIONS**

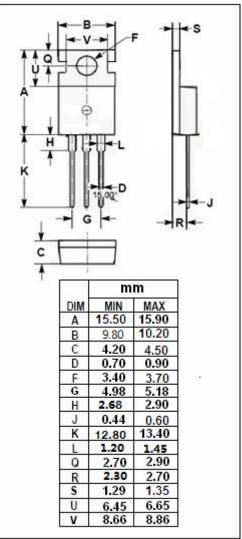
 Designed for output devices in complementary general-purpose amplifier applications.

## ABSOLUTE MAXIMUM RATINGS(T<sub>a</sub>=25°C)

| SYMBOL           | PARAMETER  | VALUE   | UNIT       |
|------------------|--|---------|------------|
| V <sub>CBO</sub> | Collector-Base Voltage                           | -100    | V          |
| V <sub>CEO</sub> | Collector-Emitter Voltage -100                   |         | V          |
| V <sub>EBO</sub> | Emitter-Base Voltage -5                          |         | V          |
| Ic               | Collector Current-Continuous -8                  |         | Α          |
| I <sub>B</sub>   | Base Current -0.1                                |         | Α          |
| Pc               | Collector Power Dissipation T <sub>C</sub> =25°C | 70      | W          |
| Ti               | Junction Temperature                             | 150     | $^{\circ}$ |
| T <sub>stg</sub> | Storage Temperature Range                        | -55~150 | $^{\circ}$ |

## THERMAL CHARACTERISTICS

| SYMBOL              | PARAMETER                            | MAX | UNIT |
|---------------------|--------------------------------------|-----|------|
| R <sub>th j-c</sub> | Thermal Resistance, Junction to Case |     | °C/W |





# **isc Silicon PNP Darlington Power Transistor**

**BD702** 

### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

| SYMBOL                | PARAMETER                            | CONDITIONS                                   | MIN  | MAX  | UNIT |
|-----------------------|--------------------------------------|--|------|------|------|
| V <sub>CEO(SUS)</sub> | Collector-Emitter Sustaining Voltage | I <sub>C</sub> = -30mA; I <sub>B</sub> = 0   | -100 |      | V    |
| V <sub>CE</sub> (sat) | Collector-Emitter Saturation Voltage | I <sub>C</sub> = -3A ,I <sub>B</sub> = -12mA |      | -2.5 | V    |
| V <sub>BE(on)</sub>   | Base-Emitter On Voltage              | I <sub>C</sub> = -3A; V <sub>CE</sub> = -3V  |      | -2.5 | V    |
| I <sub>CEO</sub>      | Collector Cutoff Current             | V <sub>CE</sub> = -100V; I <sub>B</sub> = 0  |      | -0.5 | mA   |
| I <sub>CBO</sub>      | Collector Cutoff Current             | V <sub>CB</sub> = -100V; I <sub>E</sub> = 0  |      | -0.2 | mA   |
| I <sub>EBO</sub>      | Emitter Cutoff Current               | V <sub>EB</sub> = -5V; I <sub>C</sub> = 0    |      | -2.0 | mA   |
| h <sub>FE</sub>       | DC Current Gain                      | I <sub>C</sub> = -3A ; V <sub>CE</sub> = -3V | 750  |      |      |

### **NOTICE:**

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