



isc Silicon PNP Power Transistor

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
- : $V_{CEO(SUS)} = -60V(Min)$
- · Low Saturation Voltage
- Complement to Type BD797
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

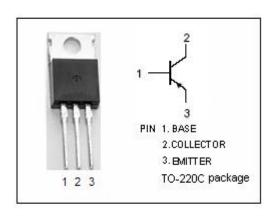
 Designed for a wide variety of medium-power switching and amplifier applications, such as series and shunt regulators and driver and output stages of high-fidelity amplifiers.

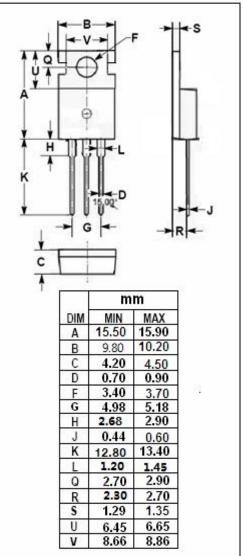


SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-60	٧
V _{CEO}	Collector-Emitter Voltage	-60	V
V _{EBO}	Emitter-Base Voltage	-5	V
lc	Collector Current-Continuous	-8	Α
I _B	Base Current-Continuous	-3	Α
Pc	Collector Power Dissipation T_c =25 $^{\circ}$ C	65	W
Tj	Junction Temperature 150		$^{\circ}$
T _{stg}	Storage Ttemperature Range	-55~150	${\mathbb C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER		UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	1.92	°C/W







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BD798

ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -30mA; I _B = 0	-60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.3A			-1	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -3A ; V _{CE} = -2V			-1.6	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V; I _E = 0			-0.1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-1	mA
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -2V	40			
h _{FE-2}	DC Current Gain	I _C = -3A ; V _{CE} = -2V	25			
f⊤	Current-Gain—Bandwidth Product	I _C = -0.25A ;V _{CE} = -10V,f _{test} = 1MHz	3			MHz

NOTICE:

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