

1 Channel Compact High Side Switch ICs 2.5A Current Limit High Side Switch ICs

BD82025FVJ

Description

BD82025FVJ is a Single Channel High Side Switch IC employing N-channel power MOSFET with low on resistance and low supply current for the power supply line of universal serial bus (USB).

This IC has a built-in over current detection circuit, thermal shutdown circuit, under voltage lockout and soft start circuits.

Features

- **Over-Current Protection : 2.5A**
- Control Input Logic : Active-Low
- **Output Discharge Function**
- Reverse Current Protection when Power Switch Off
- Thermal Shutdown
- **Open-Drain Fault Flag Output**
- Under-Voltage Lockout
- OCP Fast Response
- Soft-Start Circuit
- ESD Protection
- UL : File No. E243261
- IEC 60950-1 CB_scheme: File No.US-18106-UL

Applications

USB hub in consumer appliances, PC, PC peripheral equipment, and so forth

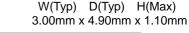
Typical Application Circuit

Key Specifications

Input Voltage Range:	2.8V to 5.5V
 ON Resistance: (VIN=5V) 	90mΩ(Typ)
Over Current Threshold:	2.5A
Standby Current:	0.01µA (Typ)
Operating Temperature Range:	-40 to +85

Operating Temperature Range: -40

Package TSSOP-B8J





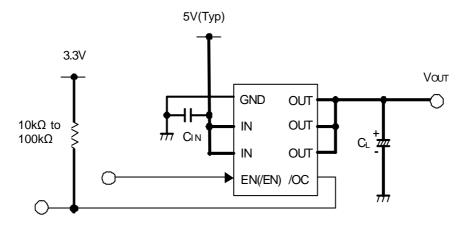


Figure 1. Typical Application Circuit

Block Diagram

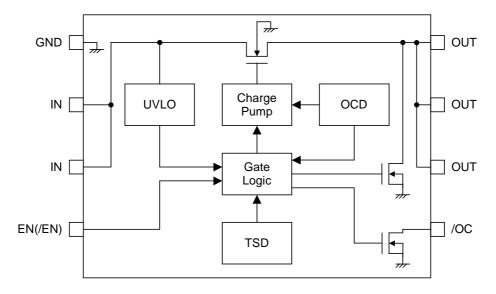


Figure 2. Block Diagram

Pin Configuration

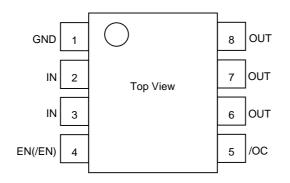


Figure 3. Pin Configuration (TOP VIEW)

Pin Descriptions

Pin No.	Symbol	I/O	Function
1	GND	-	Ground
2,3 IN I Input		I	Power supply input Input terminal to the power switch and power supply input terminal of the internal circuit Short these pins externally
4	EN, /EN	I	Enable input Active low power on switch High level input > 2.0V, Low level input < 0.8V
5	/OC	ο	Error flag output Low when over-current or thermal shutdown is activated Open drain output
6, 7, 8	OUT	0	Power switch output Short these pins externally

Absolute Maximum Ratings(Ta=25)

Parameter	Symbol	Rating	Unit
IN Supply Voltage	Vin	-0.3 to +6.0	V
/EN Input Voltage	V/en	-0.3 to +6.0	V
/OC Voltage	V/oc	-0.3 to +6.0	V
/OC Sink Current	I/oc	5	mA
OUT Voltage	Vout	-0.3 to +6.0	V
Storage Temperature	Tstg	-55 to +150	
Power Dissipation	Pd	587.5 ⁽¹⁾	mW

(1) Mounted on 70mm x 70mm x 1.6mm glass epoxy board. Reduce 4.7mW per 1 above 25

Recommended Operating Ratings

Parameter	Symbol				Unit
Falameter	Symbol	Min	Тур	Onit	
IN Operating Voltage	Vin	2.8	-	5.5	V
Operating Temperature	Topr	-40	-	+85	

Electrical Characteristics (V_{IN}= 5V, Ta= 25 $\,$, unless otherwise specified.)

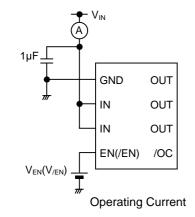
DC Characteristics

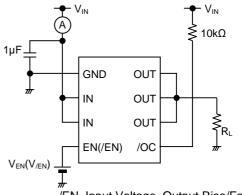
Deremeter	Parameter Symbol		Unit	Condition		
Parameter	Symbol	Min	Тур	Max	Unit	Condition
Operating Current	ldd	-	95	135	μA	V/EN = 0V, VOUT = open
Standby Current	Isтв	-	0.01	1	μA	V/EN = 5V, VOUT = open
	V/enh	2.0	-	-	V	High input
/EN Input Voltage	V/ENL	-	-	0.8	V	Low input
/EN Input Leakage	l/en	-1	0.01	+1	μA	V/EN = 0V or 5V
On Resistance	Ron	-	90	115	mΩ	IOUT = 1.0A
Reverse Leak Current	IREV	-	-	1	μA	Vout = 5.5V, Vin = 0V
Over-Current Threshold	Ітн	2.1	2.5	3.3	А	Current Load Slew rate 100A/s
Short Circuit Output Current	lsc	1.4	1.7	2.3	A	Vout=0V CL=100µF RMS
Output Discharge Resistance	RDISC	-	75	150	Ω	IOUT = 1mA, V/EN = 5V
/OC Output Low Voltage	V/oc	-	-	0.4	V	I/OC = 1mA
/OC Output Leak Current	IL/oc	-	0.01	1	μA	V/OC = 5V
	ντυνη	2.0	2.3	2.5	V	VIN increasing
UVLO Threshold	Vtuvl	1.9	2.2	2.4	V	VIN decreasing

AC Characteristics

Descention	Symbol	Limit		1.1	O an althing	
Parameter		Min	Тур	Max	Unit	Condition
Output Rise Time	tON1	-	0.4	10	ms	
Output Turn-on Time	tON2	-	0.6	20	ms	DI 400
Output Fall Time	tOFF1	-	2	20	μs	RL=10Ω
Output Turn-off Time	tOFF2	-	4	40	μs	
/OC Delay Time	t/oc	5	12	20	ms	

Measurement Circuit





/EN, Input Voltage, Output Rise/Fall Time

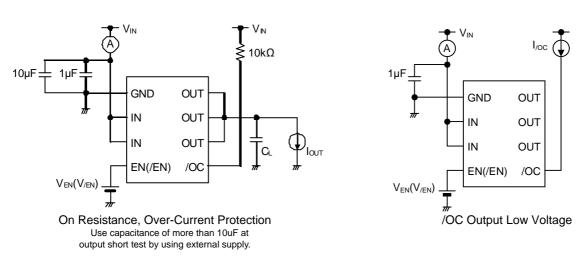


Figure 4. Measurement Circuit

Timing Diagram

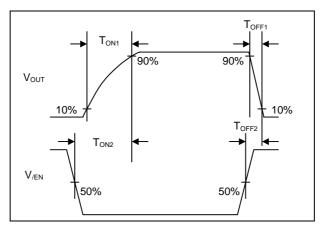
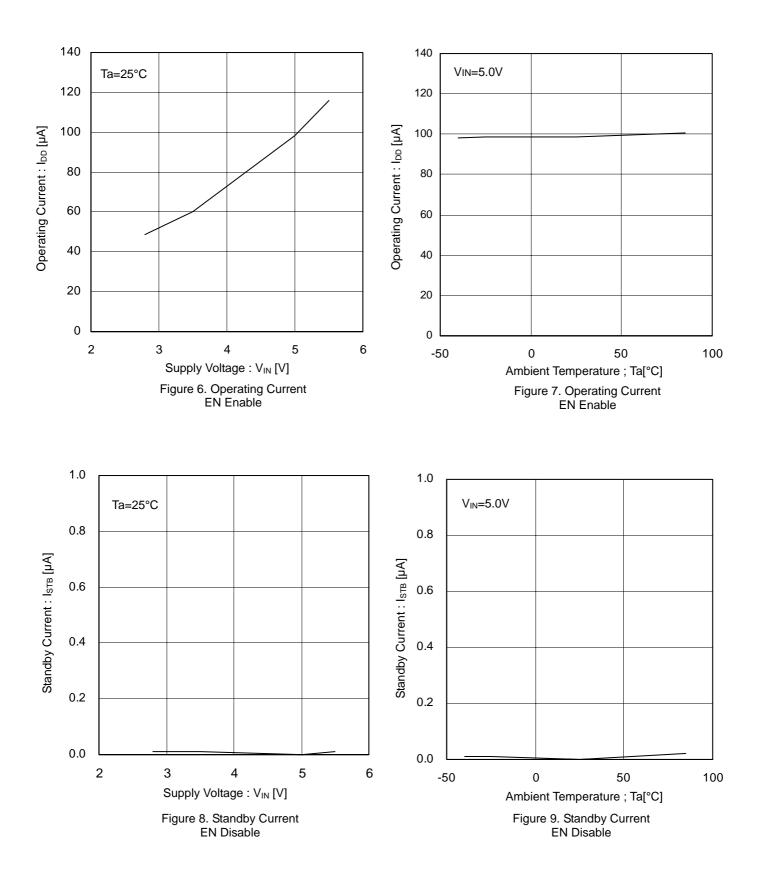
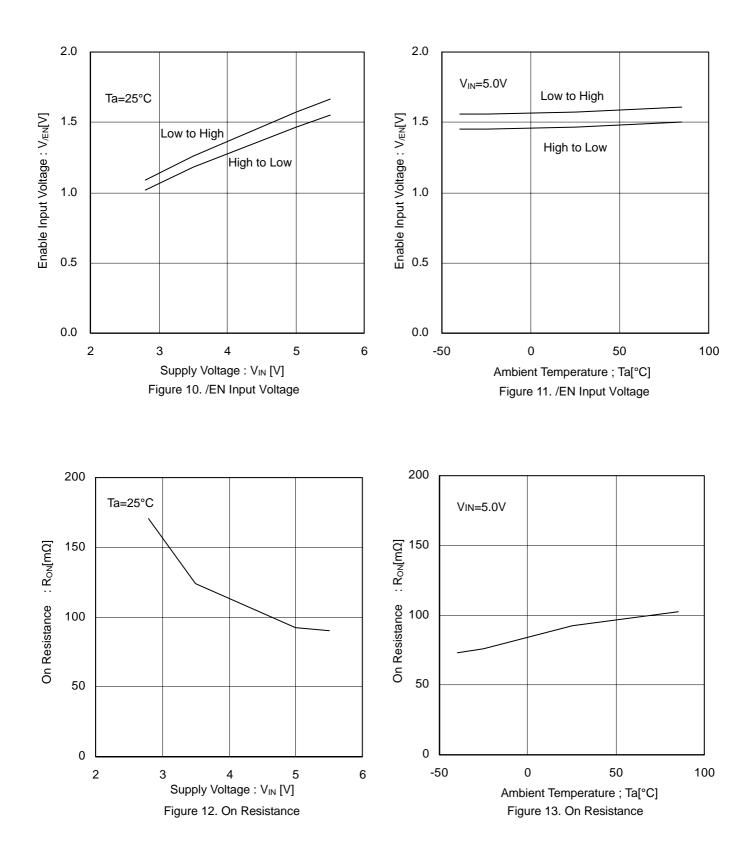
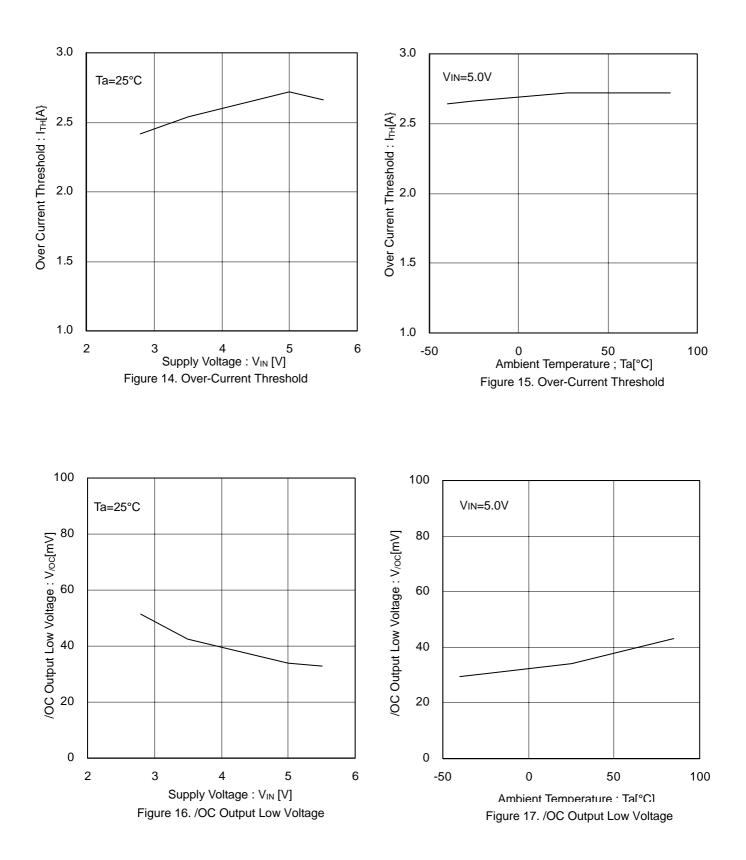


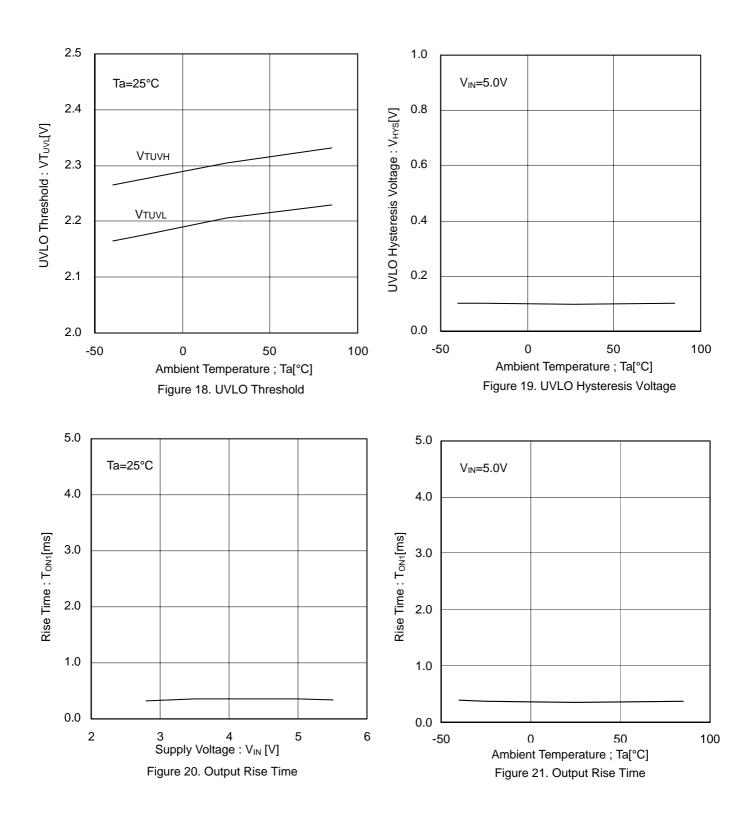
Figure 5. Output Rise/Fall Time

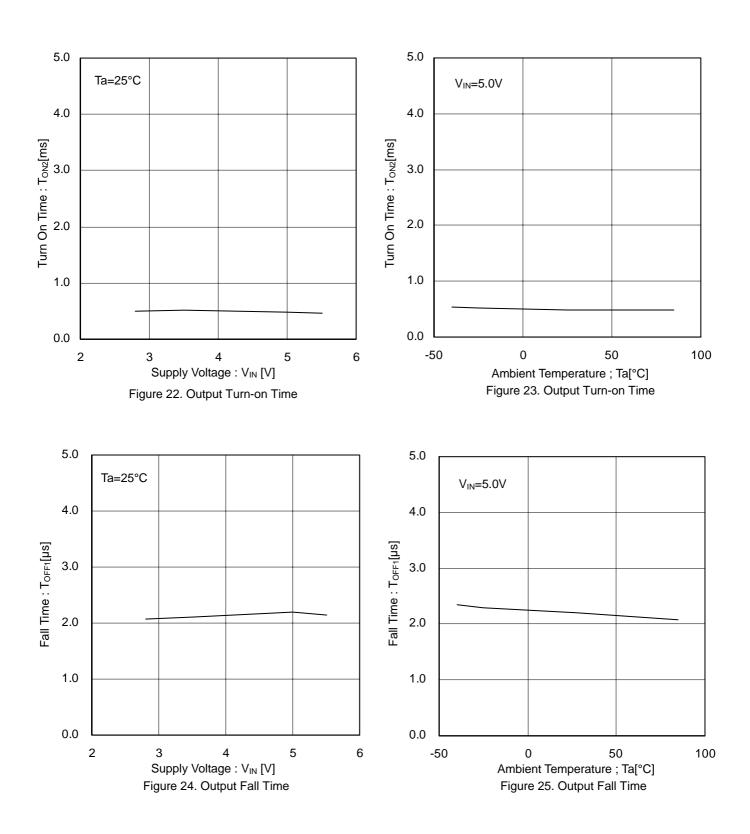
Typical Performance Curves

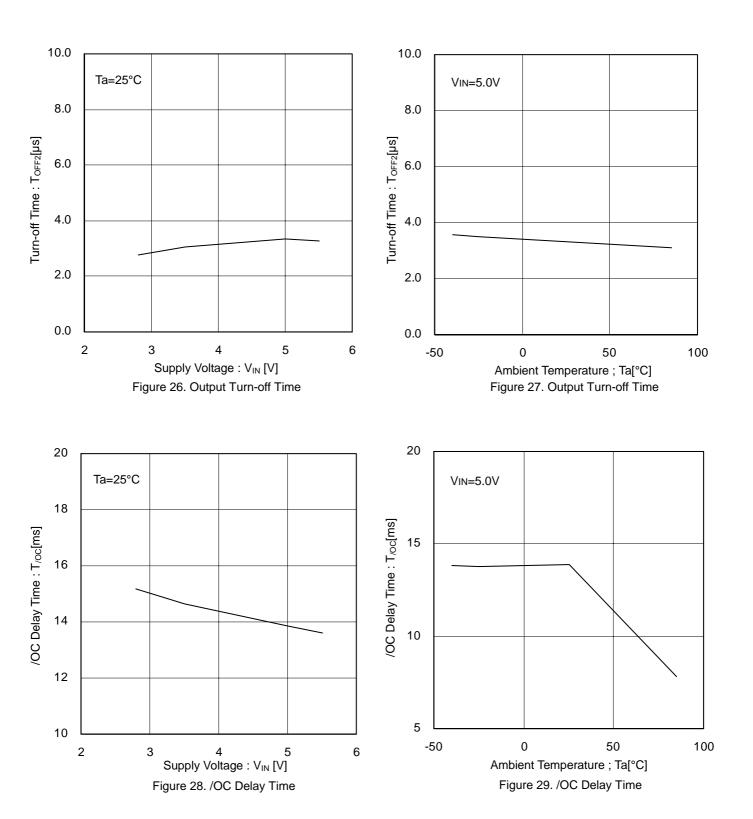


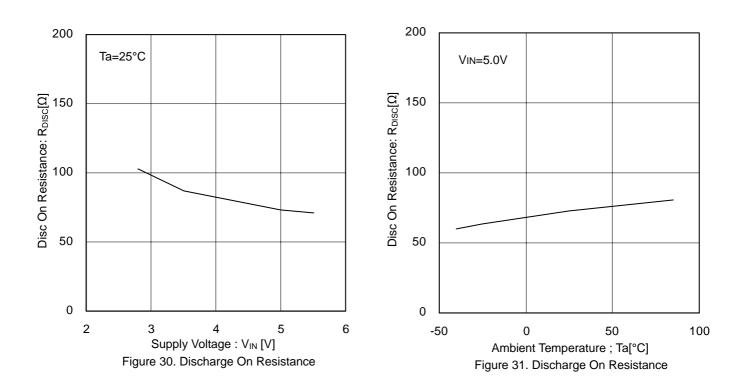




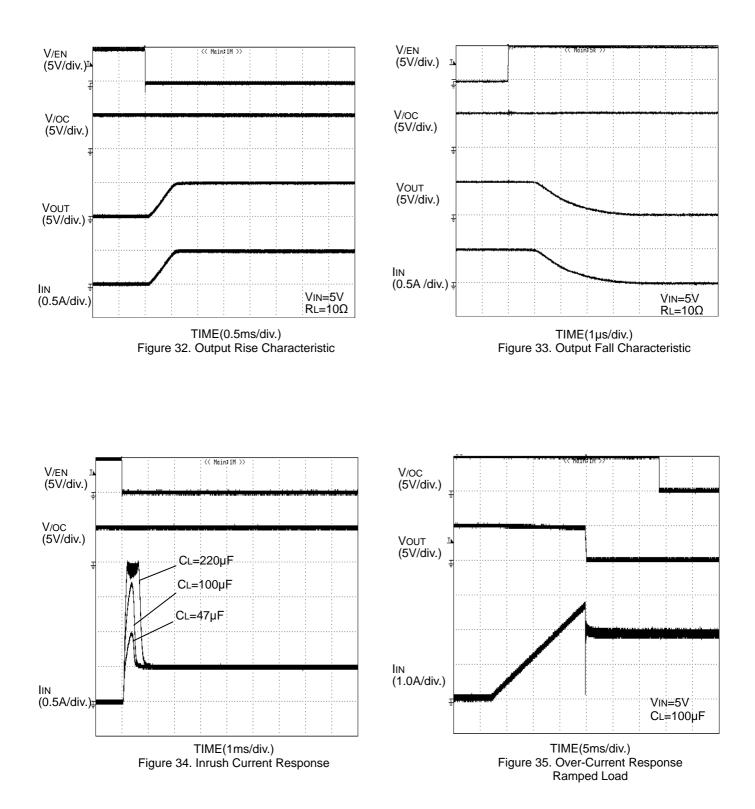




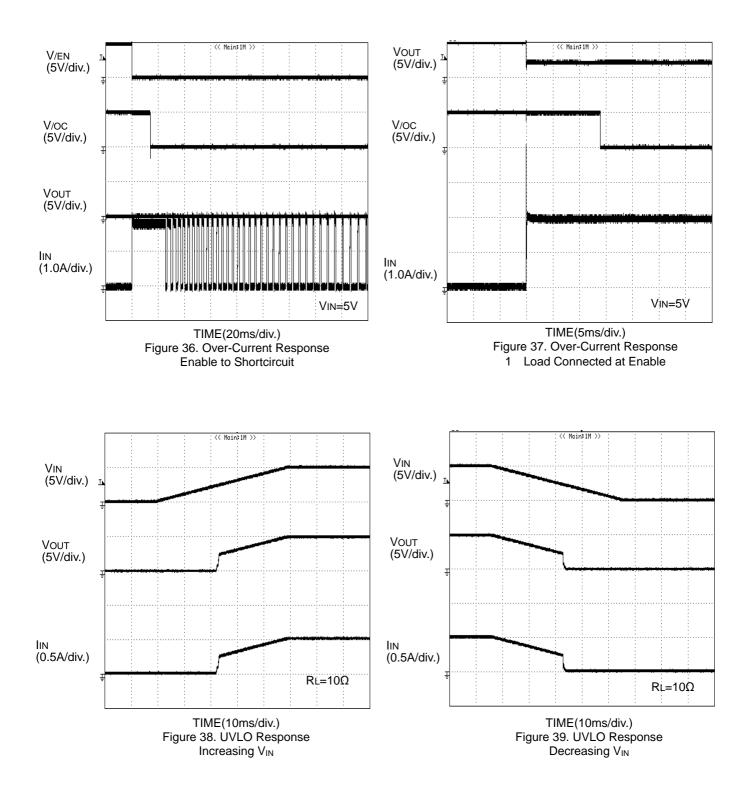




Typical Wave Forms (BD82025FVJ)



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Typical Application Circuit

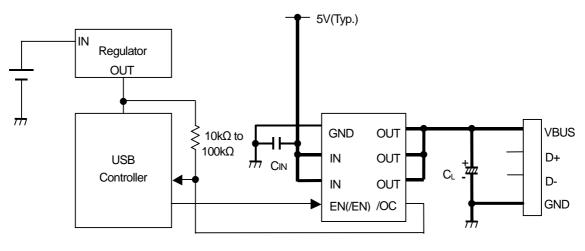


Figure 40. Typical Application Circuit

Application Information

When excessive current flows due to output short-circuit or overload ringing occurs because of inductance between power source line and IC. This may cause bad effects on IC operations. In order to avoid this case, connect a bypass capacitor CIN across IN terminal and GND terminal of IC. 1 μ F or higher is recommended. In order to decrease voltage fluctuations of power source line to IC, connect a low ESR capacitor in parallel with CIN. 10 μ F to 100 μ F or higher is recommended.

Pull up /OC output via resistance value of $10k\Omega$ to $100k\Omega$.

Set up a value for CL which satisfies the application.

This system connection diagram does not guarantee operation as the intended application.

When using the circuit with changes to the external circuit values, make sure to leave an adequate margin for external components including static and transitional characteristics as well as the design tolerances of the IC.

Functional Description

1. Switch Operation

IN terminal and OUT terminal are connected to the drain and the source of switch MOSFET respectively. The IN terminal is also used as power source input to internal control circuit.

When the switch is turned on from /EN control input, the IN terminal and OUT terminal are connected by a $90m\Omega(Typ)$ switch. In ON status, the switch is bidirectional. Therefore, when the potential of OUT terminal is higher than that of the IN terminal, current flows from OUT terminal to IN terminal.

Since the parasitic diode between the drain and the source of switch MOSFET is canceled current flow from OUT to IN is prevented during off state.

2. Thermal Shutdown Circuit (TSD)

If over current would continue, the temperature of the IC would increase drastically. If the junction temperature reaches beyond 130 (Typ) during the condition of over current detection, thermal shutdown circuit operates and turns power switch off and outputs an error flag (/OC). Then, when the junction temperature decreases below 120 (Typ), power switch is turned on and error flag (/OC) is cancelled. Unless the cause of the increase of the chip's temperature is removed or the output of power switch is turned off, this operation repeats.

The thermal shutdown circuit operates when the switch is on (/EN signal is active).

3. Over-Current Detection (OCD)

The over-current detection circuit (OCD) limits current (I_{SC}) and outputs error flag (/OC) when current flowing in each switch MOSFET exceeds a specified value. There are three cases when the OCD circuit is activated. The OCD operates when the switch is on (/EN signal is active).

- (1) When the switch is turned on while the output is in short-circuit status, the switch gets in current limit status immediately.
- (2) When the output short-circuits or when high current load is connected while the switch is on, very large current will flow until the over-current limit circuit reacts. When this happens, the over-current limit circuit is activated and the current limitation is carried out.
- (3) When the output current increases gradually, current limitation does not work until the output current exceeds the over-current detection value. When it exceeds the detection value, current limitation is carried out.
- 4. Under-Voltage Lockout (UVLO)

UVLO circuit prevents the switch from turning on until V_{IN} exceeds 2.3V(Typ). If V_{IN} drops below 2.2V(Typ) while the switch is still on, then the UVLO will shut off the power switch. UVLO has a hysteresis of 100mV(Typ). Under-voltage lockout circuit works when the switch is on (/EN signal is active).

5. Error Flag (/OC) Output

Error flag output is an N-MOS open drain output. Upon detection of over current or thermal shutdown, the output level becomes low.

Over-current detection has a delay filter. This delay filter prevents current detection flags from being sent during instantaneous events such as surge current due to switching or hot plug.

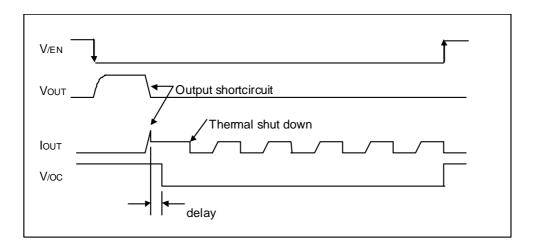


Figure 41. Over-Current Detection, Thermal Shutdown Timing

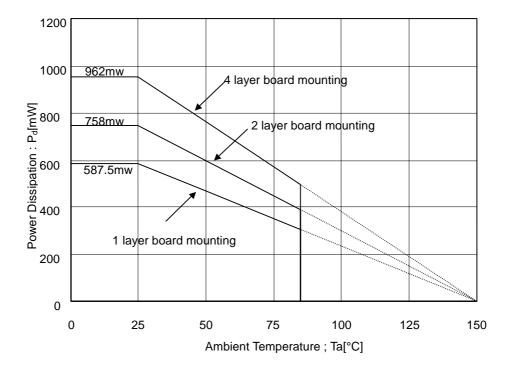
Power Dissipation

The power dissipation depends on output load, ambient temperature and PCB layout. The devices have current capacity of 2.0A respectively. Power dissipation can be calculated using the output current and the RON of the power switch as below.

 $Pd = Ron x Iout^2$

The derating curve is shown below

TSSOP-B8J (MSOP-8 JEDEC standard)



Note: IC is Mounted on 70mmx70mmx1.6mm glass-epoxy PCB. Derating is 4.7mW/ above Ta=25 . Figure 42. Power Dissipation Curve (Pd-Ta Curve)

I/O Equivalent Circuit

	an	
Symbol	Pin No.	Equivalent Circuit
EN(/EN) 4		
/OC	5	
OUT	6,7,8	

Operational Notes

1. Absolute Maximum Ratings

Operating the IC over the absolute maximum ratings may damage the IC. In addition, it is impossible to predict all destructive situations such as short-circuit modes, open circuit modes, etc. Therefore, it is important to consider circuit protection measures, like adding a fuse, in case the IC is operated in a special mode exceeding the absolute maximum ratings.

2. Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

3. Reverse Connection of Power Supply Connector

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply terminals.

4. Power Supply Line

Design the PCB layout pattern to provide low impedance ground and supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

5. GND Voltage

The voltage of the ground pin must be the lowest voltage of all pins of the IC at all operating conditions. Ensure that no pins are at a voltage below the ground pin at any time, even during transient condition.

- 6. Short Circuit between Terminals and Erroneous Mounting Ensure that when mounting the IC on the PCB the direction and position are correct. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.
- Operation in Strong Electromagnetic Field Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

8. Inspection with Set PCB

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from electro static discharge, ground the IC during assembly and use similar precautions during transport and storage.

9. Input Terminals

In the construction of this IC, P-N junctions are inevitably formed creating parasitic diodes or transistors. The operation of these parasitic elements can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions which cause these parasitic elements to operate, such as applying a voltage to an input pin lower than the GND voltage should be avoided. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. Even if the power supply voltage is applied, make sure that the input terminals have voltages within the values specified in the electrical characteristics of this IC..

10. Ground Wiring Pattern

When using both small-signal and large-current GND traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the GND traces of external components do not cause variations on the GND voltage. The power supply and ground lines must be as short and thick as possible to reduce line impedance.

11. External Capacitor

When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

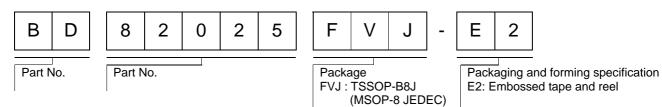
12. Thermal Shutdown Circuit (TSD)

The IC incorporates a built-in thermal shutdown circuit, which is designed to turn off the IC when the internal temperature of the IC reaches a specified value. Do not continue to operate the IC after this function is activated. Do not use the IC in conditions where this function will always be activated.

13. Thermal Design

Use a thermal design that allows for a sufficient margin by taking into account the permissible power dissipation (Pd) in actual operating conditions.

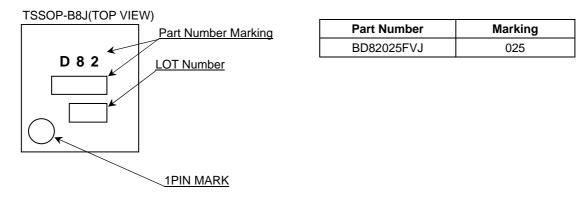
Ordering Information



Lineup

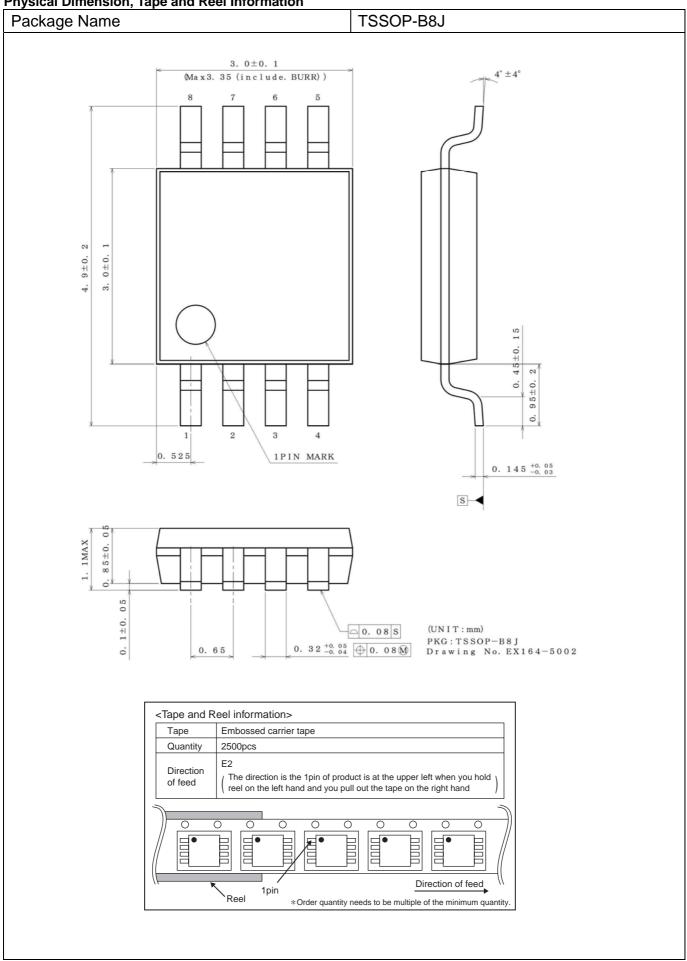
Over-Current Threshold	Control Logic	Part Number	
2.5A	Active- Low	BD82025FVJ	

Marking Diagram



Datasheet

Physical Dimension, Tape and Reel Information



Revision History

Date	Revision	Changes	
2.APR.2013	001	New Release	
18.SEP.2013	002	Revised derating of Power Dissipation	

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- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
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- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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 - [d] the Products are exposed to high Electrostatic
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