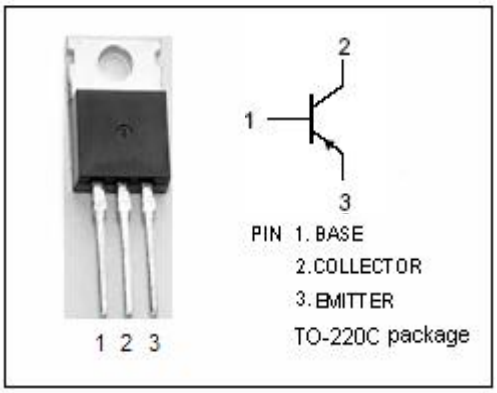
isc Silicon PNP Power Transistor — BDS19

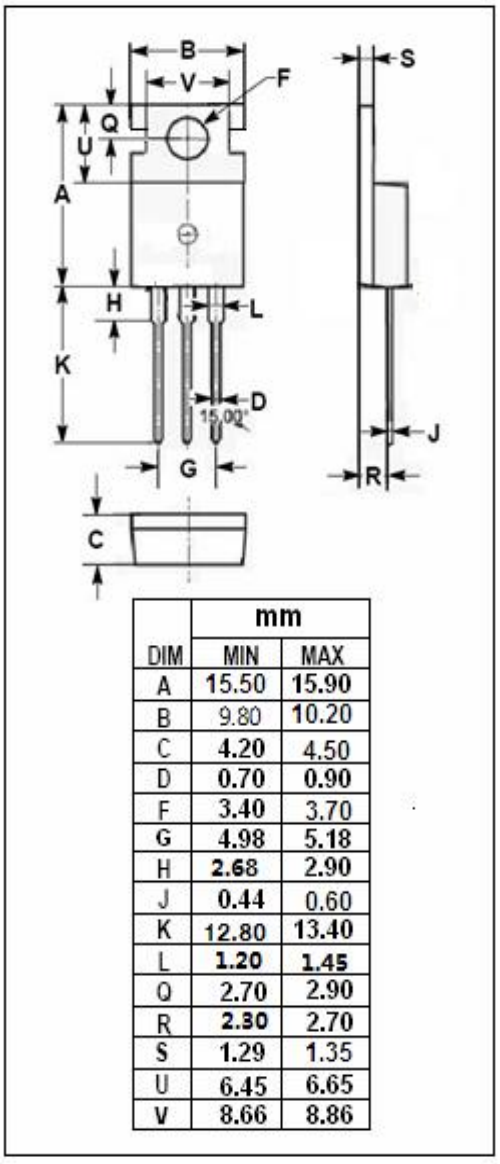
DESCRIPTION

- High Voltage: V_{CEV}= -150V(Min)
- Low Saturation Voltage-
  : $V_{CE(sat)}$= -1.5V(Max)@ I_C= -4A
- High Reliablity
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

- Designed for power linear and switching application and General puepose power.



DIM	mm MIN	mm MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-150	V
V_{CEO}	Collector-Emitter Voltage	-150	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-8	A
I_B	Base Current	-2	A
P_C	Collector Power Dissipation @ T_C=25℃	50	W
T_J	Junction Temperature	200	℃
T_{stg}	Storage Temperature Range	-65~200	℃

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j\text{-}c}$	Thermal Resistance, Junction to Case	2.5	℃/W
$R_{th\ j\text{-}a}$	Thermal Resistance, Junction to Ambient	62.5	℃/W

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(SUS)}$	Collector-Emitter Sustaining Voltage	I_C= -30mA ;I_B= 0	-150			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= -4A; I_B= -0.4A			-1.5	V
$V_{BE(on)}$	Base-Emitter On Voltage	I_C= -1A ; V_{CE}= 2V			-1.0	V
I_{CBO}	Collector Cutoff Current	V_{CE}= -150V; V_{BE}= 0			-0.02	mA
I_{CEO}	Collector Cutoff Current	V_{CE}= -75V; V_{BE}= 0			-0.1	mA
I_{EBO}	Emitter Cutoff Current	V_{EB}= -5V; I_C=0			-0.01	mA
h_{FE-1}	DC Current Gain	I_C= -0.5A; V_{CE}= -2V	40		250	
h_{FE-2}	DC Current Gain	I_C=-4A; V_{CE}= -2V	15		150	
f_T	Current-Gain—Bandwidth Product	I_C= -0.5A ; V_{CE}= -10V	30			MHz

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