High voltage, high current amplifier

The BFR 19 in an NPN silicon planar epitaxial transistor designed for amplifier applications over a wide range of voltage and current. It features a useful beta range from 100 μA to 500 mA.

ELECTRICAL CHARACTERISTICS
(T_A = 25°C unless otherwise noted)

Symbol	Characteristic and test conditions		Min.	Typ.	Max.	Unit
h_{FE}	DC Current Gain (5)					
	I_C = 100 μA	V_{CE} = 1V	30	60		
	I_C = 5 mA	V_{CE} = 1V	70	95	120	
	I_C = 50 mA	V_{CE} = 1V		95		
	I_C = 150 mA	V_{CE} = 1V	40	80	120	
	I_C = 500 mA	V_{CE} = 1V	30	45		
$V_{BE\ sat}$	Base Saturation Voltage (5)					
	I_C = 50 mA	I_B = 5 mA		0.77		V
	I_C = 150 mA	I_B = 15 mA		0.85	1	V
	I_C = 1A	I_B = 0.1A		1.35	1.6	V
$V_{BE\ on}$	Base Emitter On Voltage					
	I_C = 5 mA	V_{CE} = 1V		0.65		V
$V_{CE\ sat}$	Collector Saturation Voltage (5)					
	I_C = 50 mA	I_B = 5 mA		0.08		V
	I_C = 150 mA	I_B = 15 mA		0.13	0.25	V
	I_C = 1A	I_B = 0.1A		0.65	1	V
I_{CES}	Collector Reverse Current					
	V_{CE} = 50V	V_{EB} = 0		0.1	10	nA
$I_{CES\ (150°C)}$	Collector Reverse Current					
	V_{CE} = 50V	V_{EB} = 0		0.1	10	μA
I_{EBO}	Emitter Reverse Current					
	V_{EB} = 5V	I_C = 0		0.1	10	nA
BV_{CES}	Collector to Emitter Breakdown Voltage					
	I_C = 100 μA	V_{EB} = 0	75			V
BV_{EBO}	Emitter to Base Breakdown Voltage					
	I_E = 100 μA	I_C = 0	7			V
LV_{CEO}	Collector to Emitter Sustaining Voltage (4 and 5)					
	I_C = 30 mA	I_B = 0	35			V
h_{fe}	Small Signal Current Gain (f=1kHz)					
	I_C = 1 mA	V_{CE} = 5V		90		
h_{ie}	Input Resistance (f=1 kHz)					
	I_C = 1 mA	V_{CE} = 5V		2.2		kΩ
h_{oe}	Output Conductance (f=1kHz)					
	I_C = 1 mA	V_{CE} = 5V		8.5		μmho
h_{re}	Voltage Feedback Ratio (f=1kHz)					
	I_C = 1 mA	V_{CE} = 5V		2.4		$\times 10^{-4}$
h_{fe}	High Freq. Current Gain (f=20MHz)					
	I_C = 50 mA	V_{CE} = 10V	3	5		
C_{TE}	Emitter Transition Capacitance					
	I_C = 0	V_{EB} = 0.5V		50	80	pF
C_{obo}	Base Collector Capacitance					
	I_E = 0	V_{CB} = 10V		12.5	20	pF
NF	Narrow Band Noise Figure (6)					
	I_C = 30 μA	V_{CE} = 10V		2	7	dB

NOTES:

(1) These ratings are limiting values above which the serviceability of any individual semiconductor device may be impaired.
(2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
(3) These ratings give a maximum junction temperature of 200°C and junction - to - case thermal resistance of 35°C/W (derating factor of 28.6 mW/°C); junction - to - ambient thermal resistance of 219°C/W (derating factor of 4.67 mW/°C).
(4) These ratings refer to a high - current point where collector - to - emitter voltage is lowest. For more information send for SGS - AR 5.
(5) Measured under pulse conditions: pulse length = 300 μsec; duty cycle = 1%.
(6) f = 1 kHz; R_S = 1 kΩ; Power Bandwith of 200 Hz.

ABSOLUTE MAXIMUM RATINGS (1)
(T_A = 25°C unless otherwise noted)

Voltages

Collector to Emitter (4)	V_{CEO}	35 V
Collector to Emitter	V_{CES}	75 V
Emitter to Base	V_{EBO}	7 V

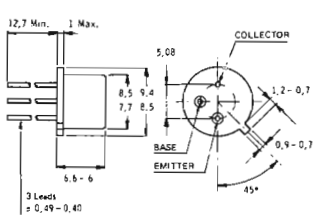
Temperatures

Storage Temperature	T_{STG}	- 55°C to 200°C
Junction Temperature	T_J	200°C
Lead Temperature (Soldering 10 sec.)	T_L	260°C

Power (2 - 3)

Dissipation at 25°C Case Temperature	P_D	5 W
Dissipation at 25°C Ambient Temperature	P_D	0.8 W

PHYSICAL DIMENSIONS
Similar to Jedec TO-5



Note All dimensions are in mm