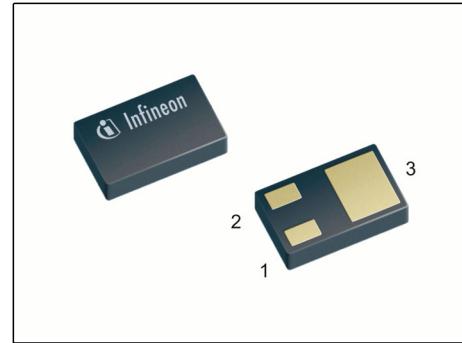


Linear Low Noise SiGe:C Bipolar RF Transistor

- High gain ultra low noise RF transistor
- Based on Infineon's reliable high volume Silicon Germanium technology
- Provides outstanding performance for a wide range of wireless applications up to 10 GHz
- Ideal for WLAN and all 5-6 GHz applications
- High $OIP3$ and P_{-1dB} for driver stages
- High maximum stable and available gain
 $G_{ms} = 21$ dB at 1.8 GHz, $G_{ma} = 11.5$ dB at 6 GHz
- Pb-free (RoHS compliant) and halogen-free very thin small leadless package (package height 0.32 mm max. ideal for modules)
- Qualification report according to AEC-Q101 available



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration			Package
BFR750L3RH	R8	1=B	2=C	3=E	TSLP-3-9

Maximum Ratings at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage $T_A = 25^\circ\text{C}$	V_{CEO}	4	V
$T_A = -55^\circ\text{C}$		3.5	
Collector-emitter voltage	V_{CES}	13	
Collector-base voltage	V_{CBO}	13	
Emitter-base voltage	V_{EBO}	1.2	
Collector current	I_C	90	mA
Base current	I_B	9	
Total power dissipation ¹⁾ $T_S \leq 96^\circ\text{C}$	P_{tot}	360	mW
Junction temperature	T_J	150	°C
Storage temperature	T_{Stg}	-55 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R_{thJS}	150	K/W

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage $I_C = 3 \text{ mA}, I_B = 0$	$V_{(BR)CEO}$	4	4.7	-	V
Collector-emitter cutoff current $V_{CE} = 13 \text{ V}, V_{BE} = 0$	I_{CES}	-	-	100	µA
Collector-base cutoff current $V_{CB} = 5 \text{ V}, I_E = 0$	I_{CBO}	-	-	100	nA
Emitter-base cutoff current $V_{EB} = 0.5 \text{ V}, I_C = 0$	I_{EBO}	-	-	10	µA
DC current gain $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}$, pulse measured	h_{FE}	160	250	400	-

¹ T_S is measured on the emitter lead at the soldering point to the pcb

²For the definition of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)

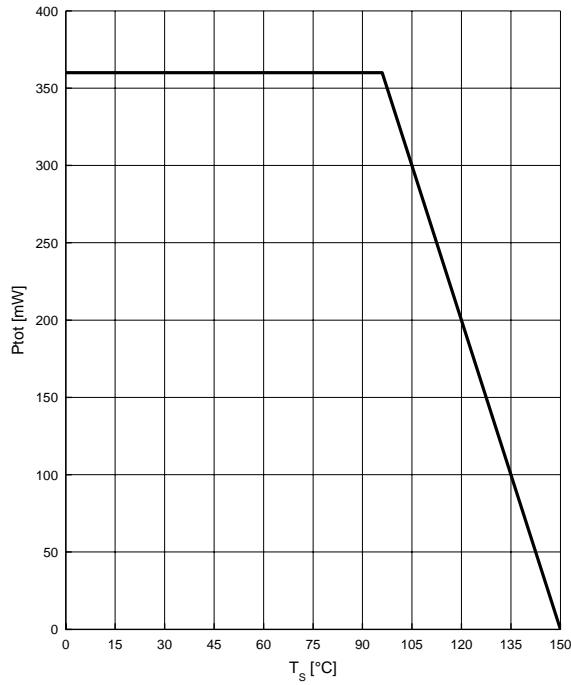
Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}, f = 2 \text{ GHz}$	f_T	-	37	-	GHz
Collector-base capacitance $V_{CB} = 3 \text{ V}, f = 1 \text{ MHz}, \text{emitter grounded}$	C_{cb}	-	0.24	0.42	pF
Collector emitter capacitance $V_{CE} = 3 \text{ V}, f = 1 \text{ MHz}, \text{base grounded}$	C_{ce}	-	0.31	-	
Emitter-base capacitance $V_{EB} = 0.5 \text{ V}, f = 1 \text{ MHz}, \text{collector grounded}$	C_{eb}	-	0.97	-	
Minimum noise figure $I_C = 25 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}, Z_S = Z_{Sopt}$ $I_C = 25 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6 \text{ GHz}, Z_S = Z_{Sopt}$	NF_{\min}	-	0.6	-	dB
Power gain, maximum stable ¹⁾ $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}, Z_S = Z_{Sopt},$ $Z_L = Z_{Lopt}, f = 1.8 \text{ GHz}$	G_{ms}	-	21	-	dB
Power gain, maximum available ¹⁾ $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}, Z_S = Z_{Sopt},$ $Z_L = Z_{Lopt}, f = 6 \text{ GHz}$	G_{ma}	-	11.5	-	dB
Transducer gain $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}, Z_S = Z_L = 50 \Omega,$ $f = 1.8 \text{ GHz}$ $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}, Z_S = Z_L = 50 \Omega,$ $f = 6 \text{ GHz}$	$ S_{21e} ^2$	-	18	-	dB
Third order intercept point at output ²⁾ $V_{CE} = 3 \text{ V}, I_C = 60 \text{ mA}, f = 1.8 \text{ GHz},$ $Z_S = Z_L = 50 \Omega$	$IP3$	-	29.5	-	dBm
1dB compression point at output $I_C = 60 \text{ mA}, V_{CE} = 3 \text{ V}, Z_S = Z_L = 50 \Omega,$ $f = 1.8 \text{ GHz}$	$P_{-1\text{dB}}$	-	16.5	-	

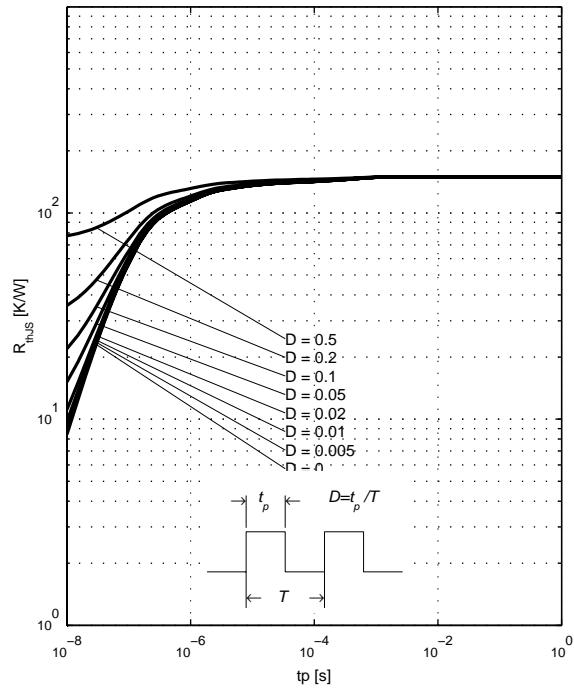
¹ $G_{ma} = |S_{21e}| / S_{12e} (k - (k^2 - 1)^{1/2})$, $G_{ms} = |S_{21e}| / S_{12e}|$
²IP3 value depends on termination of all intermodulation frequency components.

Termination used for this measurement is 50Ω from 0.1 MHz to 6 GHz

Total power dissipation $P_{\text{tot}} = f(T_S)$

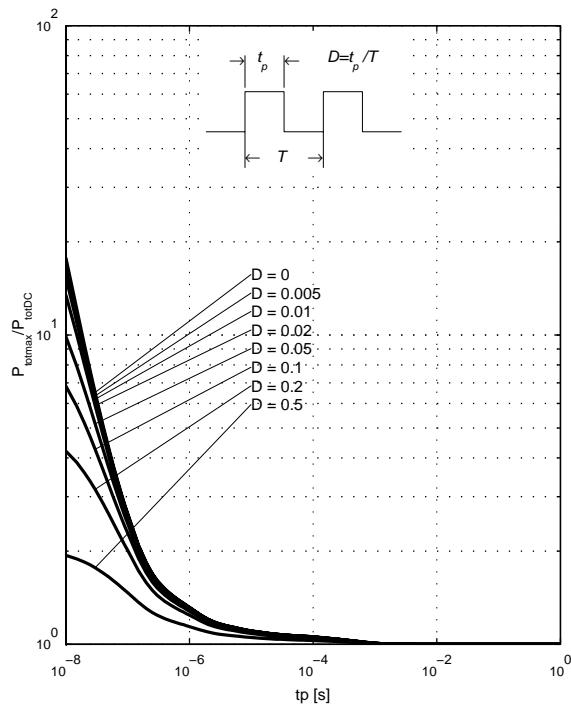


Permissible Puls Load $R_{\text{thJS}} = f(t_p)$



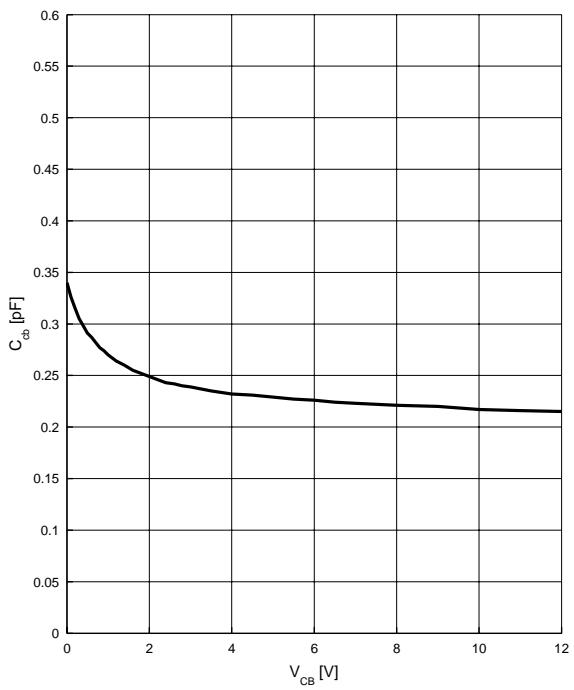
Permissible Pulse Load

$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$



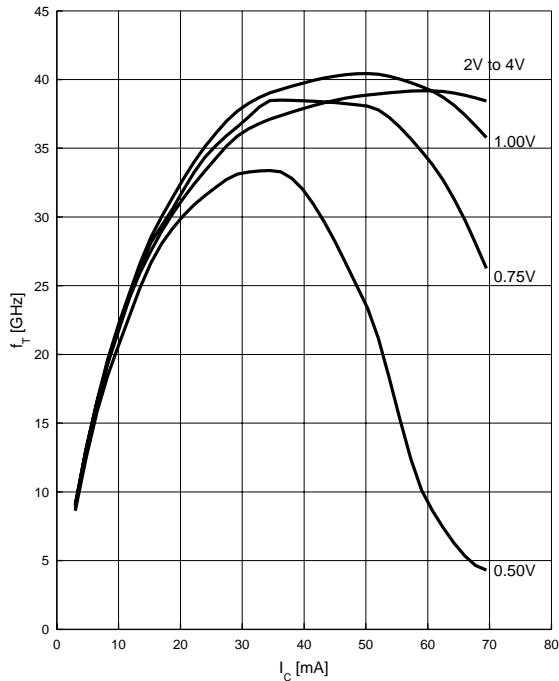
Collector-base capacitance $C_{\text{cb}} = f(V_{\text{CB}})$

$f = 1 \text{ MHz}$



Transition frequency $f_T = f(I_C)$

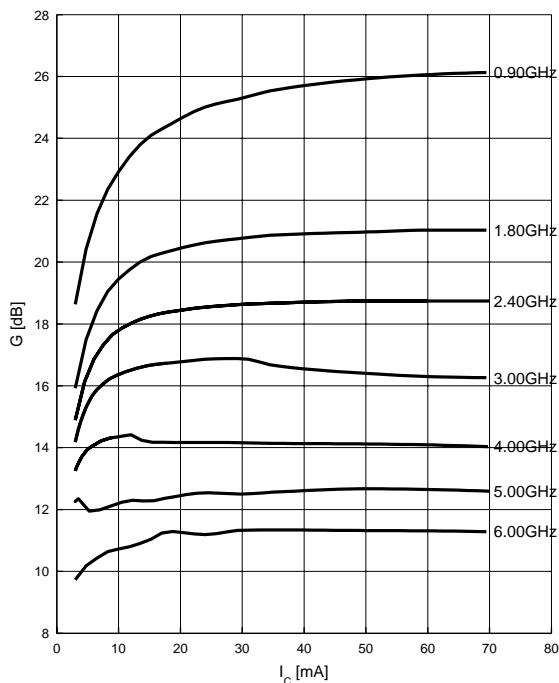
V_{CE} = parameter, $f = 1$ GHz



Power gain $G_{ma}, G_{ms} = f (I_C)$

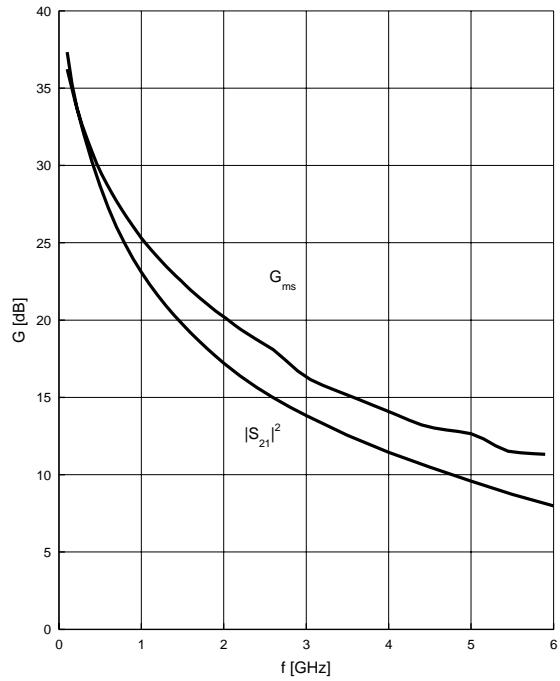
$V_{CE} = 3$ V

f = parameter



Power gain $G_{ma}, G_{ms} = f (f)$

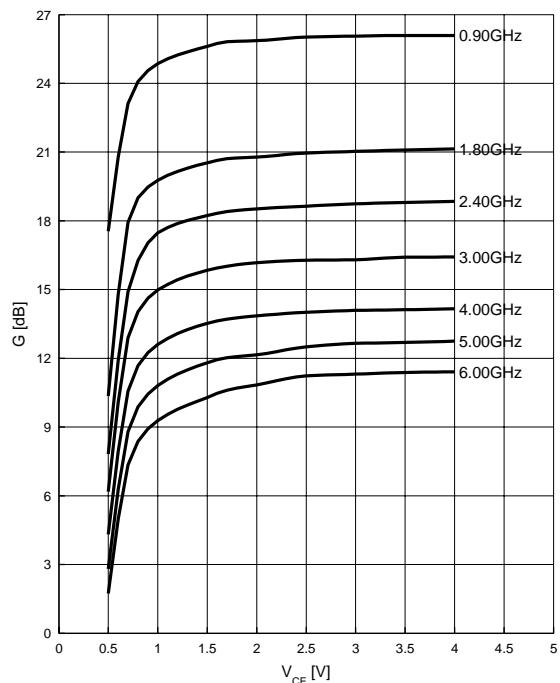
$V_{CE} = 3$ V, $I_C = 60$ mA



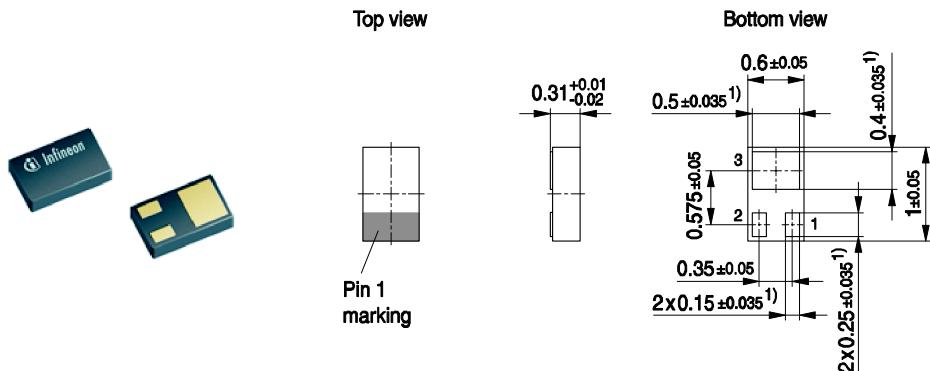
Power gain $G_{ma}, G_{ms} = f (V_{CE})$

$I_C = 60$ mA

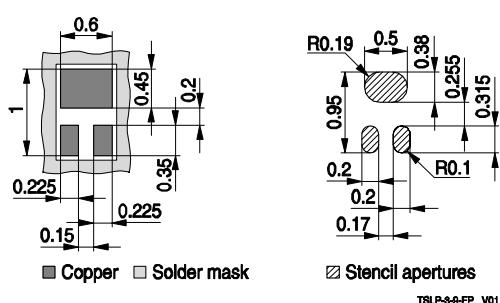
f = parameter



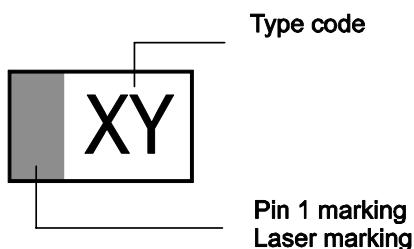
Package Outline



Foot Print

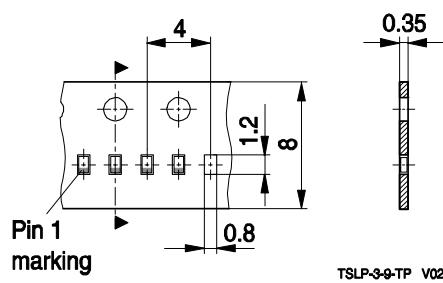


Marking Layout (Example)



Standard Packing

Reel Ø 180 mm: 15.000 Pieces/Reel
 Reel Ø 330 mm: 50.000 Pieces/Reel



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