

# BFX74-BFX74A

## GENERAL PURPOSE TYPES

### PNP DIFFUSED SILICON PLANAR TRANSISTORS

GENERAL DESCRIPTION - The BFX74 is a PNP silicon PLANAR transistor suitable for high performance amplifiers, where high voltages and high current gain are requested. It features 50 Volts of LVCE and an  $f_T$  of 90 MHz typ.  
For improved performance use the BFX74A.

#### ABSOLUTE MAXIMUM RATINGS (Note 1)

##### Maximum Temperatures

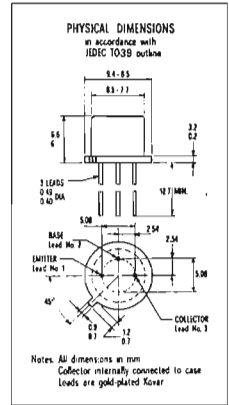
$T_{STG}$	Storage Temperature	-55°C to +200°C
$T_J$	Operating Junction Temperature	+200°C
$T_L$	Lead Temperature (Soldering, 10 sec. time limit)	+260°C

##### Maximum Power Dissipations (Notes 2 and 3)

	BFX 74	BFX 74A	
$P_D$	Total Dissipation of 25°C Case Temperature	2 Watts	4 Watts
	at 100°C Case Temperature	1 Watt	
	at 25°C Ambient Temperature	0.6 Watt	0.8 Watt

##### Maximum Voltages ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

$V_{CB0}$	Collector to Base Voltage	-50 Volts	-60 Volts
$V_{CE0}$	Collector to Emitter Voltage (Note 4)	-35 Volts	-60 Volts
$V_{CER}$	Collector to Emitter Voltage ( $R_{BE} = 10\ \Omega$ ) (Note 4)	-50 Volts	
$V_{EB0}$	Emitter to Base Voltage	-5 Volts	-5 Volts



#### ELECTRICAL CHARACTERISTICS (25°C free air temperature unless otherwise noted)

SYMBOL	CHARACTERISTIC	BFX 74			BFX 74A			UNIT	TEST CONDITIONS
		MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
$h_{FE}$	DC Current Gain						50	$I_C = 0.1\ \text{mA}$	$V_{CE} = -10\ \text{V}$
$h_{FE}$	DC Pulse Current Gain (Note 5)	25	55		25	60		$I_C = 5\ \text{mA}$	$V_{CE} = -10\ \text{V}$
$h_{FE}$	DC Pulse Current Gain (Note 5)	30	45	90	30	50		$I_C = 150\ \text{mA}$	$V_{CE} = -10\ \text{V}$
$h_{FE}$	DC Pulse Current Gain (Note 5)					40		$I_C = 500\ \text{mA}$	$V_{CE} = -10\ \text{V}$
$V_{BE}(\text{sat})$	Base Saturation Voltage (Note 5)	-0.95	-1.3		-0.8	-1	V	$I_C = 150\ \text{mA}$	$I_B = 15\ \text{mA}$
$V_{CE}(\text{sat})$	Collector Saturation Voltage (Note 5)	-1	-1.5		-0.2	-0.3	V	$I_C = 150\ \text{mA}$	$I_B = 15\ \text{mA}$
$V_{CE}(\text{sat})$	Collector Saturation Voltage (Note 5)					-0.5	V	$I_C = 500\ \text{mA}$	$I_B = 50\ \text{mA}$
$I_{EBO}$	Emitter Cutoff Current	0.1	1				$\mu\text{A}$	$I_C = 0$	$V_{BE} = -2\ \text{V}$
$I_{CBO}$	Collector Cutoff Current	0.01	1				$\mu\text{A}$	$I_E = 0$	$V_{CB} = -30\ \text{V}$
$I_{CBO}(125^\circ\text{C})$	Collector Cutoff Current	2	100				$\mu\text{A}$	$I_E = 0$	$V_{CB} = -30\ \text{V}$
$I_{EBO}$	Emitter Cutoff Current					50	nA	$I_C = 0$	$V_{BE} = -4\ \text{V}$
$I_{CBO}$	Collector Cutoff Current				0.2	50	nA	$I_E = 0$	$V_{CB} = -50\ \text{V}$
$I_{CBO}(125^\circ\text{C})$	Collector Cutoff Current				0.2	50	$\mu\text{A}$	$I_E = 0$	$V_{CB} = -50\ \text{V}$
$BV_{CBO}$	Collector to Base Breakdown Voltage	-50					V	$I_E = 0$	$I_C = 100\ \mu\text{A}$
$BV_{CBO}$	Collector to Base Breakdown Voltage				-60		V	$I_E = 0$	$I_C = 10\ \mu\text{A}$
$BV_{EBO}$	Emitter to Base Breakdown Voltage	-5					V	$I_C = 0$	$I_E = 100\ \mu\text{A}$
$BV_{EBO}$	Emitter to Base Breakdown Voltage				-5		V	$I_C = 0$	$I_E = 10\ \mu\text{A}$
$V_{LVCER}$	Collector to Emitter Sustaining Voltage (Notes 4 and 5)	-50					V	$R_{BE} \leq 10\ \Omega$	$I_C = 100\ \text{mA}$ (pulsed)
$V_{LVCEO}$	Collector to Emitter Sustaining Voltage (Notes 4 and 5)	-35			-60		V	$I_B = 0$	$I_C = 10\ \text{mA}$ (pulsed)

# Silicon Planar Transistor **BFX74 • BFX74A**

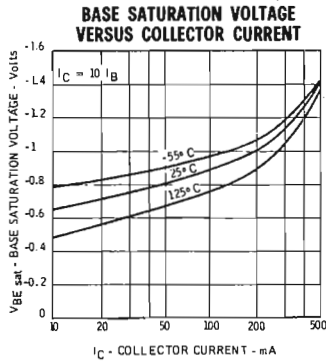
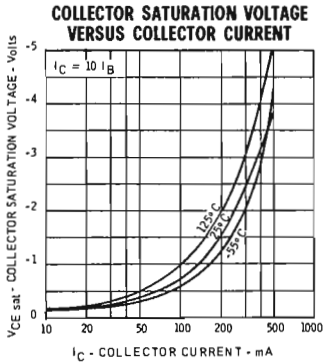
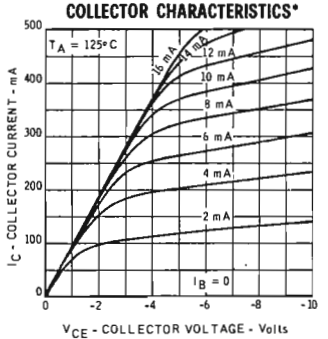
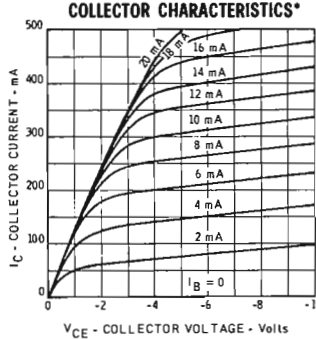
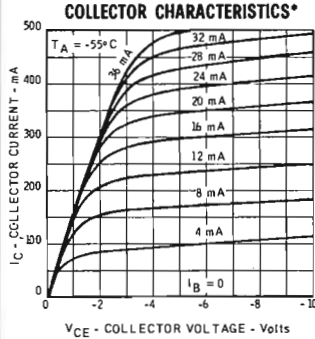
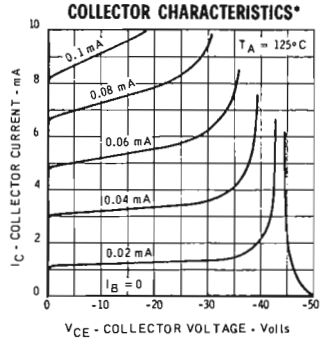
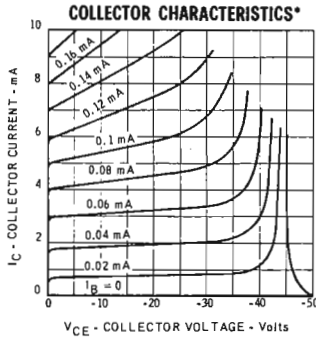
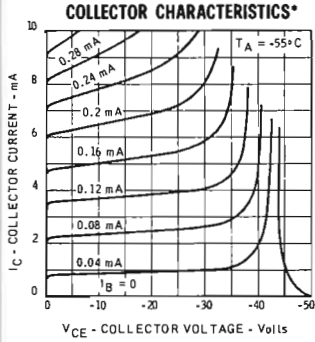
ELECTRICAL CHARACTERISTICS (25°C free air temperature unless otherwise noted)

SYMBOL	CHARACTERISTIC	BFX74		BFX74A		UNIT	TEST CONDITIONS
		MIN.	TYP. MAX.	MIN.	TYP. MAX.		
$h_{fe}$	Small Signal Current Gain ( $f = 1$ KHz)	30	60				$I_C = 5$ mA $V_{CE} = -10$ V
$h_{ib}$	Input Resistance ( $f = 1$ KHz)	6.2	10			$\Omega$	$I_C = 5$ mA $V_{CB} = -10$ V
$h_{ob}$	Output Conductance ( $f = 1$ KHz)	0.6	5			$\mu\text{mho}$	$I_C = 5$ mA $V_{CB} = -10$ V
$h_{rb}$	Voltage Feedback Ratio ( $f = 1$ KHz)	2	8			$\times 10^{-4}$	$I_C = 5$ mA $V_{CB} = -10$ V
$h_{fe}$	High Frequency Current Gain ( $f = 20$ MHz)	3	4.5				$I_C = 50$ mA $V_{CE} = -10$ V
$h_{fe}$	High Frequency Current Gain ( $f = 100$ MHz)			1	1.5		$I_C = 50$ mA $V_{CE} = -10$ V
$C_{TE}$	Emitter Transition Capacitance	57	80	75	110	pF	$I_C = 0$ $V_{EB} = -0.5$ V
$C_{obo}$	Common Base, Open Circuit, Output Capacitance	31	45	15	20	pF	$I_E = 0$ $V_{CB} = -10$ V

**NOTES :**

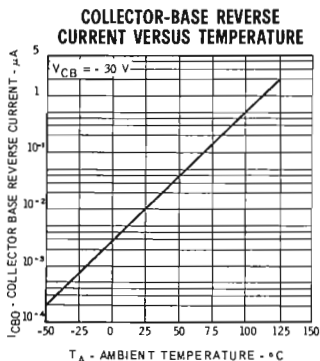
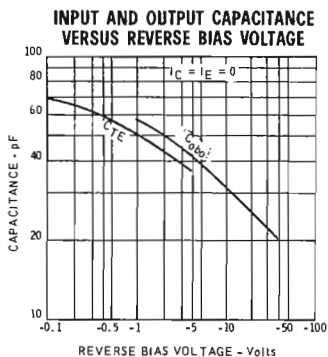
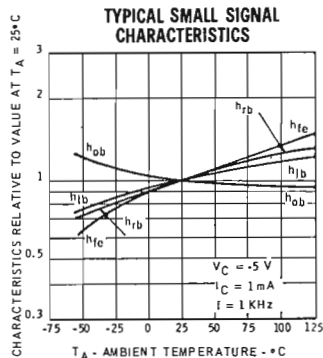
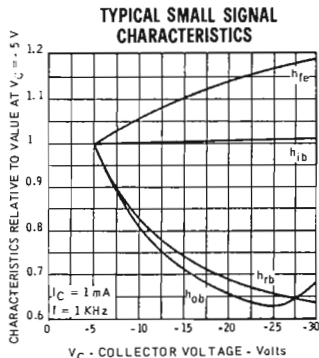
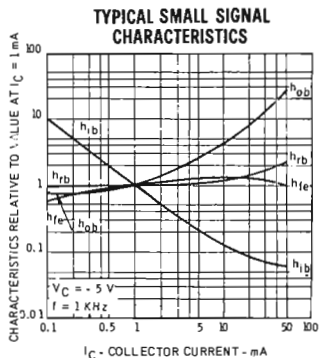
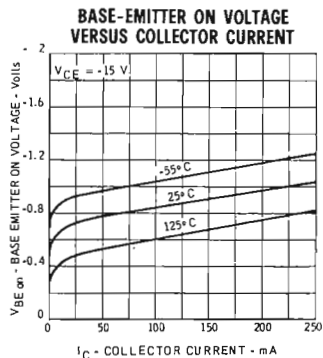
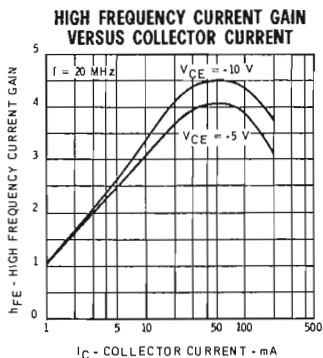
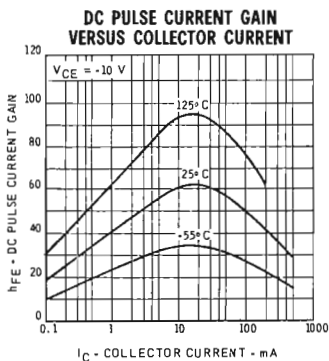
- (1) These ratings are limiting values above which the serviceability of any individual semiconductor device may be impaired.
- (2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- (3) (BFX 74 only) These ratings give a maximum junction temperature of 175°C and junction - to - case thermal resistance of 75°C/watt (derating factor of 13.3 mW/°C); junction - to - ambient thermal resistance of 250°C/watt (derating factor of 4 mW/°C).  
(BFX 74A only) These ratings give a maximum junction temperature of 200°C and junction - to - case thermal resistance of 43.7°C/watt (derating factor of 22.8 mW/°C); junction - to - ambient thermal resistance of 219°C/watt (derating factor of 4.56 mW/°C).
- (4) These ratings refers to a high - current point where collector - to - emitter voltage is lowest. For more information send for SGS - AR 5.
- (5) Pulse Conditions : length = 300  $\mu\text{sec}$ ; duty cycle = 1%.

## TYPICAL ELECTRICAL CHARACTERISTICS (25°C free air temperature unless otherwise noted)

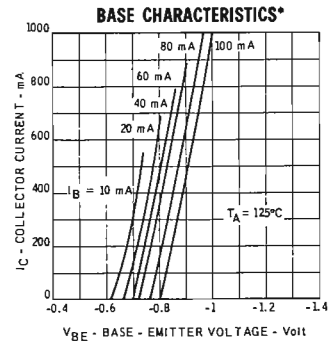
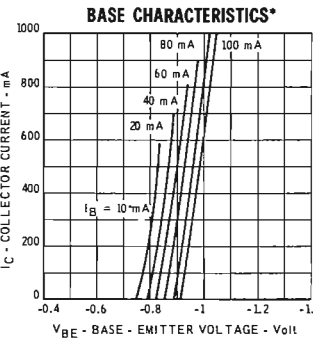
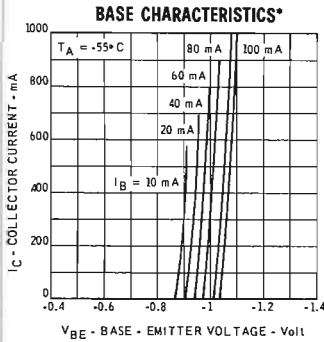
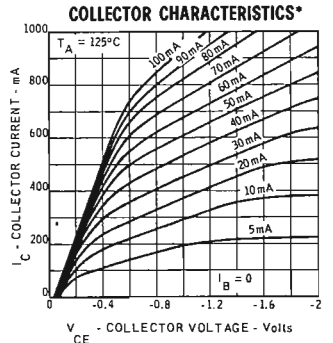
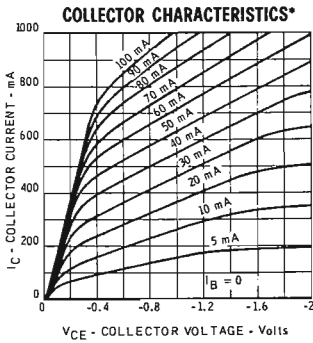
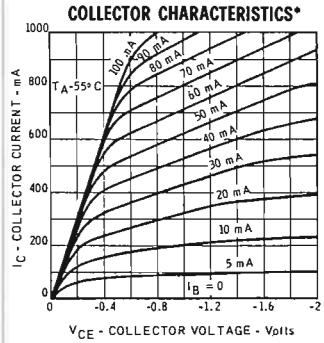
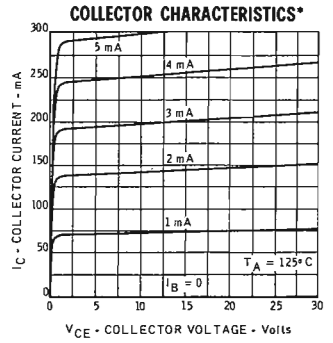
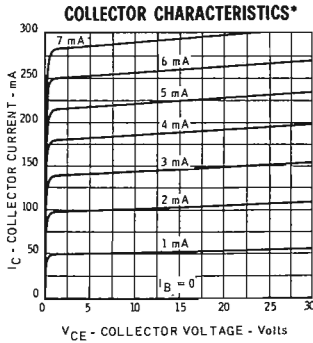
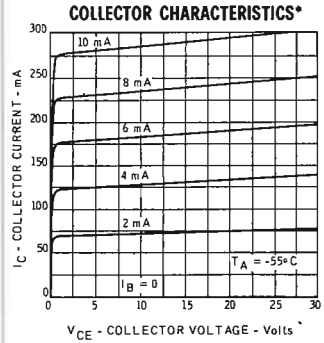


\* Single family characteristics on Transistor Curve Tracer.

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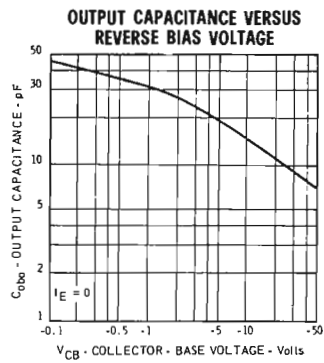
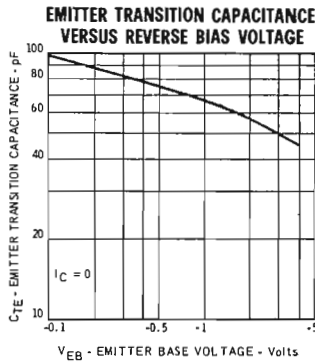
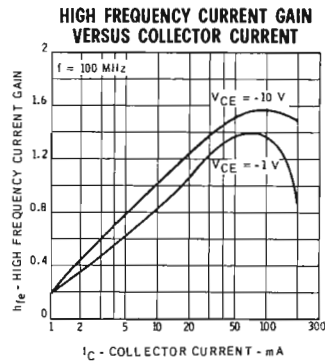
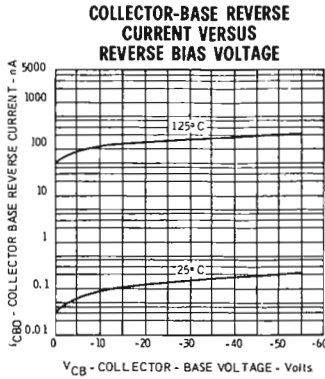
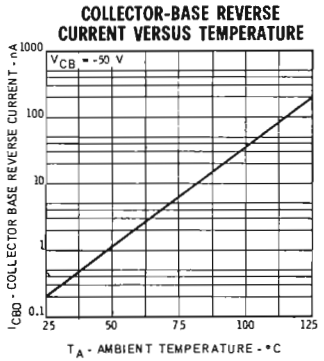
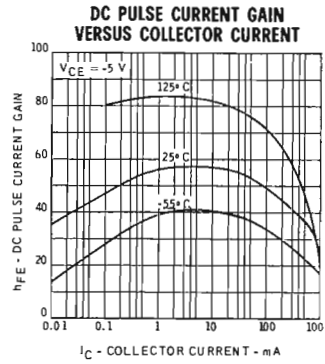
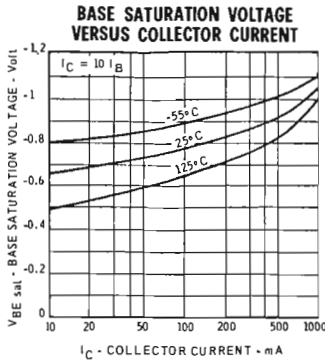
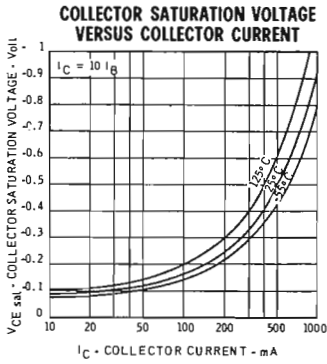


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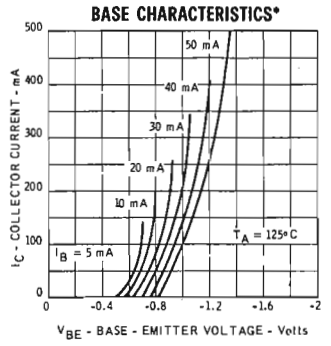
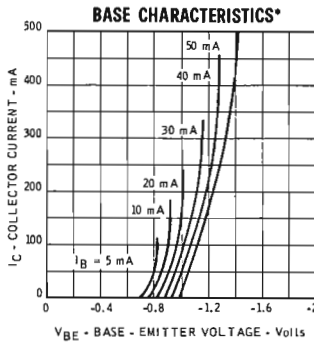
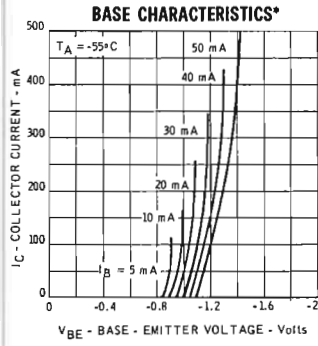


\*Single family characteristics on Transistor Curve Tracer.

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