



LCD MODULE SPECIFICATION

MODEL NO.

BG24064A series ver03

FOR MESSRS:

ON DATE OF:

APPROVED BY:



History of Version

Version	Contents	Date	Note
01	NEW VERSION	2006/06/06	SPEC.
02	ADD Ultra wide temperature range	2007/09/26	spec
03	MODIFY LED/EDGE/WHITE INFORMATION	2008/01/17	



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1. Numbering System

<u>B</u>	<u>G</u>	<u>24064</u>	<u>A</u>	<u>G</u>	<u>P</u>	<u>L</u>	<u>7</u>	<u>B</u>	<u>xxx</u>
0	1	2	3	4	5	6	7	8	9

0	Brand	Bolymin	
1	Module Type	C= character type G= graphic type P= TAB/TCP type	O= COG type F= COF type
2	Format	2002=20 characters, 4 lines 12232= 122 x 32 dots	
3	Version No.	A type	
4	LCD Color	G=STN/gray Y=STN/yellow-green C=color STN	B=STN/blue F=FSTN T=TN
5	LCD Type	R=positive/reflective P=positive/transflective	M=positive/transmissive N=negative/transmissive
6	Backlight type/color	L=LED array/ yellow-green H=LED edge/white R=LED array/red G=LED edge/yellow-green	D=LED edge/blue E=EL/white B=EL/blue C=CCFL/white
7	CGRAM Font (applied only on character type)	J=English/Japanese Font E=English/European Font	C=English/Cyrillic Font H=English/Hebrew Font
8	View Angle/ Operating Temperature	B=Bottom/Normal Temperature H=Bottom/Wide Temperature U=Bottom/Ultra wide Temperature	T=Top/Normal Temperature W=Top/Wide Temperature C=9H/Normal Temperature
9	Special Code	3=3 volt logic power supply n=negative voltage for LCD c=cable/connector xxx=to be assigned on data sheet	t=temperature compensation for LCD p=touch panel



2. General Specification

(1) Mechanical Dimension

Item	Standard Value	Unit
Number of dots	240 × 64	dots
Module dimension (L x W x H)	180.0 x 65.0 x 15.1(Max)-LED array B/L 187.0 x 65.0 x 11.9(Max)-LED white B/L, CCFL 180.0 x 65.0 x 9.2(Max)- E/L or No B/L	mm
View area	133.0(W) × 39.0(H)	mm
Active area	127.16(W) × 33.88(H)	mm
Dot size	0.49(W) × 0.49(H)	mm
Dot pitch	0.53(W) × 0.53(H)	mm

(2) Controller IC: **T6963C controller OR equivalent**

(3) Temperature Range

	Normal	Wide	Ultra Wide
Operating	0 ~+50°C	-20 ~+70°C	-30 ~+80°C
Storage	-10 ~+60°C	-30 ~+80°C	-30 ~+80°C

3. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T _{OP}	-20	—	+70	°C
Storage Temperature	T _{ST}	-30	—	+80	°C
Input Voltage	V _I	V _{SS}	—	V _{DD}	V
Supply Voltage For Logic	V _{DD} -V _{SS}	0	—	+7	V
Supply Voltage For LCD	V _{DD} -V _O	0	—	15	V
LED Forward Current(array)	I _F	—	—	900	mA
LED Forward Current(white LED)	I _F	—	—	100	mA



4. Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply Voltage For Logic	Vdd-Vss	—	4.75	5.0	5.5	V
Supply Voltage For LCD	Vdd-Vo	Ta=-20°C	—	14.5	—	V
		Ta=25°C	—	12.5	—	V
		Ta=+70°C	—	10.5	—	V
Input High Vol	V _{IH}	—	2.2	—	Vdd	V
Input Low Vol	V _{IL}	—	0	—	0.8	V
Output High Vol	V _{OH}	—	2.4	—	Vdd	V
Output Low Vol.	V _{OL}	—	—	—	0.4	V
Supply Current(no N.V built in)	I _{dd}	Vdd=5V	—	20.0	—	mA
Supply Current(with N.V built in)	I _{dd}	Vdd=5V	—	40.0	—	mA

5. Optical Characteristics

a. STN

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
View Angle	(V) θ	CR \geq 2	10		45	deg
	(H) φ	CR \geq 2	-30		30	deg
Contrast Ratio	CR	—		3		—
Response Time 25°C	T rise	—		100	150	ms
	T fall	—		150	200	ms

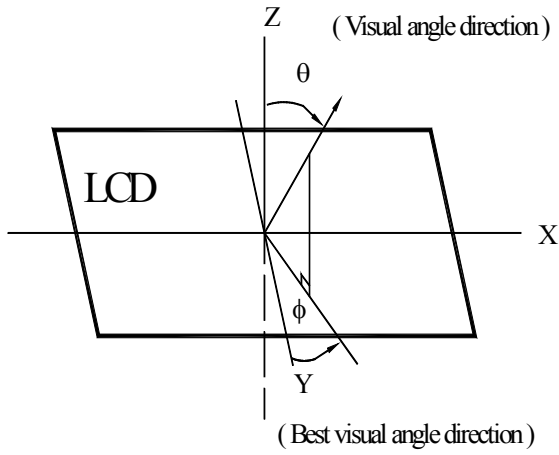
b. FSTN

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
View Angle	(V) θ	CR \geq 3	10		60	deg
	(H) φ	CR \geq 3	-45		45	deg
Contrast Ratio	CR	—		5		—
Response Time 25°C	T rise	—		100	150	ms
	T fall	—		150	200	ms

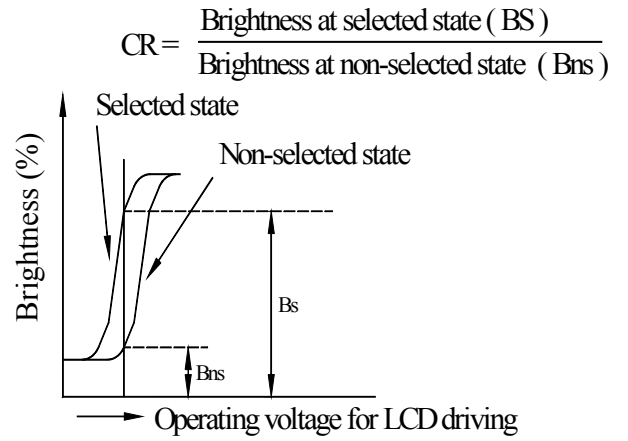


5.1 Definitions

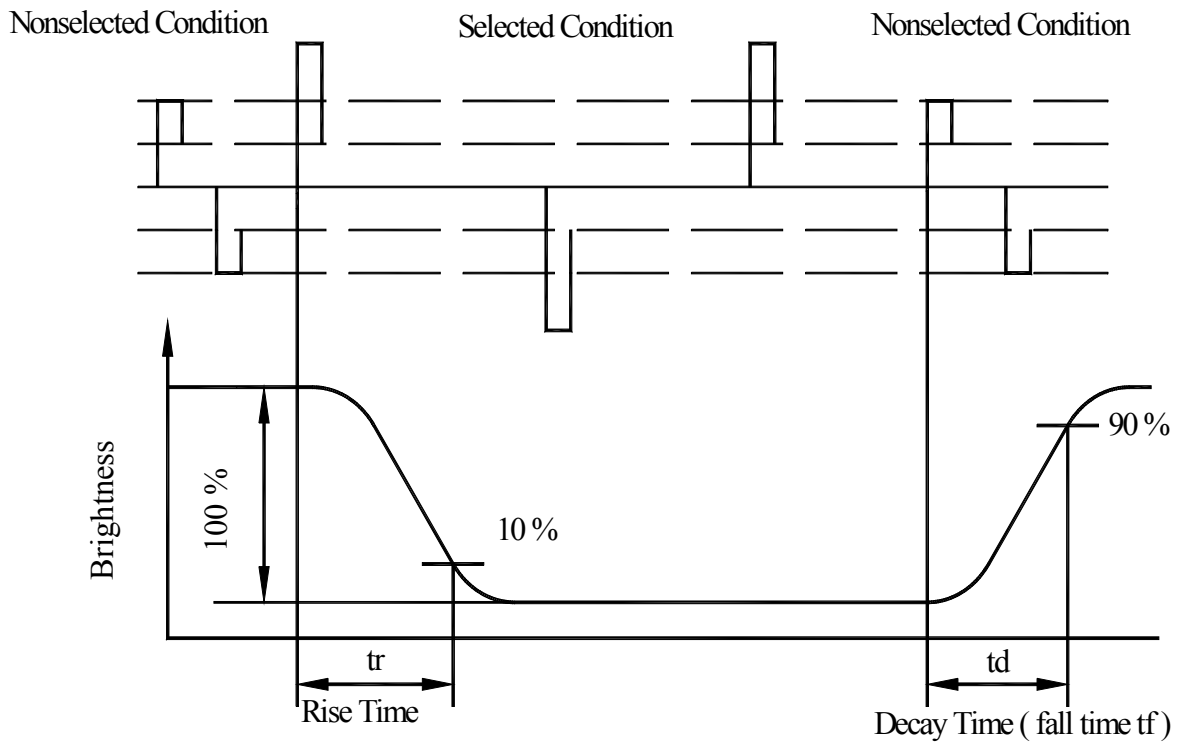
■ View Angles



■ Contrast Ratio



■ Response time





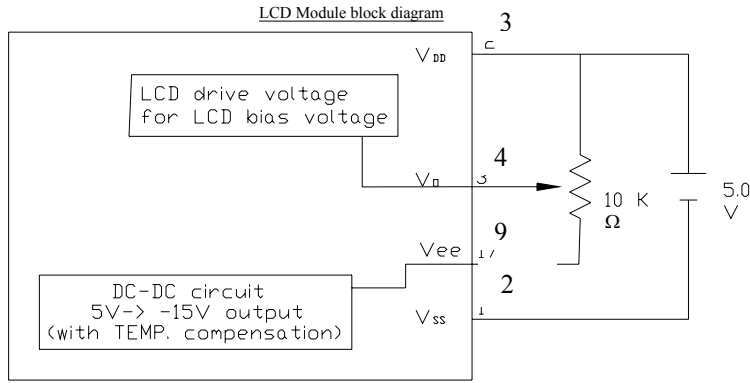
6. Interface Pin Function

Pin No.	Symbol	Level	Description
1	FG		Frame ground (Connected to bezel)
2	V _{ss}		GND
3	V _{dd}		Power supply (+5 V)
4	V _o		Power supply for LCD driver
5	WR	L	Data write. Write data into T6963C when WR = L
6	RD	L	Data read. Read data from T6963C when RD = L
7	CE	L	L: Chip enable
8	C/D	H / L	WR=L, C/D=H: Command Write C/D=L: Data write RD=L, C/D=H: Status Read C/D=L: Data read
9	V _{ee}		Negative Voltage output -13.6 V
10	RESET	H / L	H: Normal; L: Initialize T6963C
11	DB0	H / L	Data bus line
12	DB1	H / L	Data bus line
13	DB2	H / L	Data bus line
14	DB3	H / L	Data bus line
15	DB4	H / L	Data bus line
16	DB5	H / L	Data bus line
17	DB6	H / L	Data bus line
18	DB7	H / L	Data bus line
19	FS	H / L	Pins for selection of font; H: 6 * 8, L: 8 * 8
20	N.C		No connection



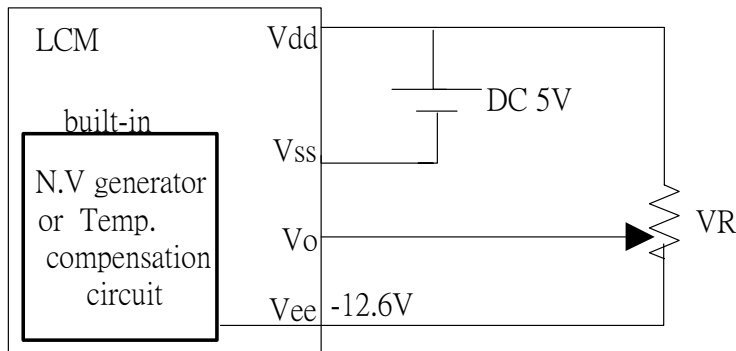
7. Power Supply for LCD Module and LCD Operating Voltage a Adjustment

* LCM operating on " DC 5V " input with built-in negative voltage and temperature compensation circuit

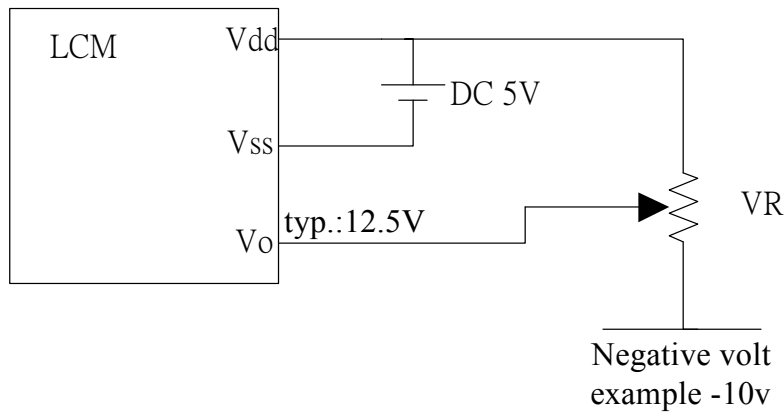


V_{ee} voltage will be adjusted automatically on different temperature

LCM operating on " DC 5V " input with built-in negative voltage



* (Option) LCM operating on " DC 5V " input with external negative voltage





8. Backlight Information

8.1 Specification

(1) LED array / yellow-green

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Current	I _{LED}	—	450	680	mA	V=4.2V
Supply Voltage	V	—	4.2	4.3	V	
Reverse Voltage	V _R	—	—	8	V	
Luminous Intensity	I _V	—	—	—	cd/m ²	I _{LED} =450mA
Wave Length	λ _p		575		nm	I _{LED} =450mA
Life Time		—	100000	—	Hr.	V ≤ 4.2V
Color	Yellow Green					

(2) LED edge / white

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Current	I _{LED}	—	75		mA	V=3.1V
Supply Voltage	V	—	3.1		V	—
Reverse Voltage	V _R	—	—	8	V	—
Luminous Intensity	I _V	—	80	—	cd/m ²	I _{LED} =75mA
Wave Length	λ _p	—		—	nm	I _{LED} =75mA
Life Time	—	—	20000	—	Hr.	V ≤ 3.1V
Color	White					



(3) EL / white

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Drive Voltage	Vmax	—	110	170	Vrms	25°C
Drive Wave	Fmax	—	400	1000	Hz	25°C
Brightness		48	60	—	cd/m ²	110V/400Hz
Power Consumption		—	80	—	mW	110V/400Hz
Chromatism	X	—	0.3173	—	—	110V/400Hz
	Y	—	0.3995	—	—	110V/400Hz
Life time		5000			hour	110V/400Hz
Color		White			—	Light on 110V/400Hz

(4) CCFL

No.2 3 4 5 shall be lighted at constant lamp current (IL : 5.0 mA) and shall be measured 3 minutes after the table below. The measurement shall be conducted on the condition that ambient temperature : 25 ± 2 °C humidity : 30 ~ 85%, with no wind.

NO	Items	Requirements	Remarks
1	Lamp Current (IL)	5.0 ± 0.5 (mArms)	
2	Lamp Voltage (VL)	205 ± 20 (Vrms)	
3	Lamp Power (P) (Reference Value)	1.03 (Wrms)	VL * IL
4	Luminance	250 min (cd/m)	Note 1
5	Chromaticity (X) (Y)	0.308 ± 0.01 0.330 ± 0.01	Note 2
6	Starting Voltage (VS)	400 MAX (25°C) (Vrms) 600 MAX (0°C) (Vrms)	Note 3
7	Life time	10000 min (h)	Note 4

Note 1. The average value is measured through the glass.

Note 2. The tube center / center point shall be measured.

Note 3. All the tubes shall be lighted. Slidein method shall be used for voltage application.

Note 4. Life

Judgement conditions.

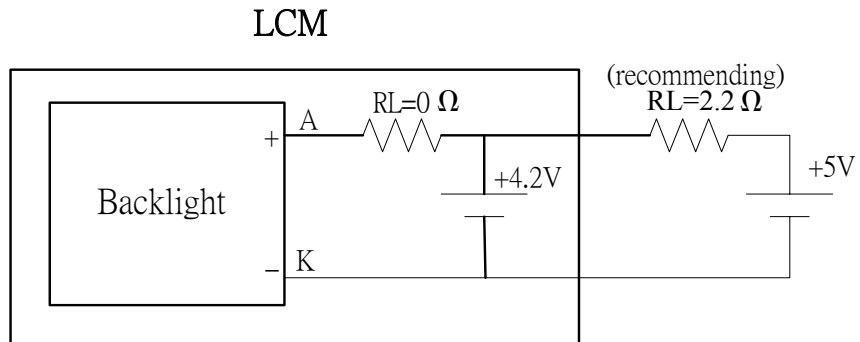
- A The luminance becomes 50% of the initial luminance.
- B Not normal lighting.
- C When a severe appearance failure is found.



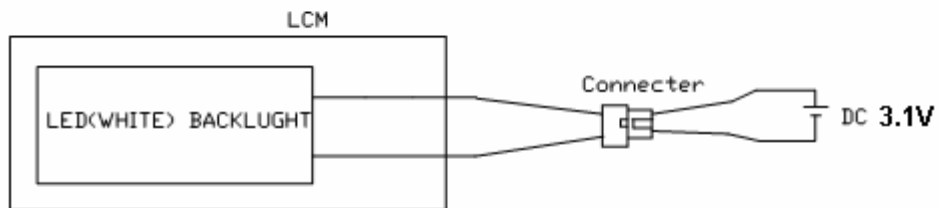
8.2 Backlight driving methods

a. LED B/L drive from A.K directly

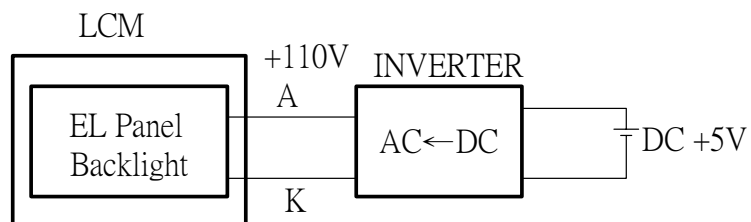
a.1 array (yellow-green)



a.2 edge / white

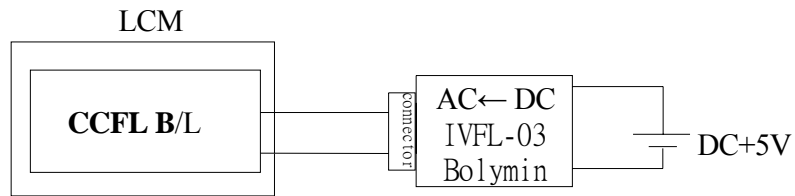


b. E/L B/L driven from A.K cable directly





c. CCFL B/L drive directly from connector





9. Quality Assurance

◆ Screen Cosmetic Criteria

No.	Defect	Judgement Criterion	Partition
1	Spots	<p>A)Clear</p> <p><u>Size:d mm</u> <u>Acceptable Qty in active area</u></p> <p>d ≤0.1 Disregard</p> <p>0.1<d≤0.2 6</p> <p>0.2<d≤0.3 2</p> <p>0.3<d 0</p> <p>Note:Including pin holes and defective dots which must be within one pixel size.</p>	Minor
		<p>B)Unclear</p> <p><u>Size:d mm</u> <u>Acceptable Qty in active area</u></p> <p>d ≤0.2 Disregard</p> <p>0.2<d≤0.5 6</p> <p>0.5<d≤0.7 2</p> <p>0.7<d 0</p>	
2	Bubbles in Polarizer	<p><u>Size:d mm</u> <u>Acceptable Qty in active area</u></p> <p>d≤0.3 Disregard</p> <p>0.3<d≤1.0 3</p> <p>1.0<d≤1.5 1</p> <p>1.5<d 0</p>	Minor
3	Scratch	In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor
4	Allowable Density	Above defects should be separated more than 30mm each other.	Minor
5	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-light type should be judged with back-light on state only.	Minor



10. Reliability

■ Content of Reliability Test

Environmental Test				
No.	Test Item	Content of Test	Test Condition	Applicable Standard
1	High Temperature storage	Endurance test applying the high storage temperature for a long time.	60°C 200hrs	—
2	Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-10°C 200hrs	—
3	High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	50°C 200hrs	—
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	0°C 200hrs	—
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	70°C, 90%RH 96hrs	—
6	High Temperature/ Humidity Operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	40°C, 90%RH 96hrs	—
7	Temperature Cycle	<p>Endurance test applying the low and high temperature cycle.</p> <p style="text-align: center;">← -10°C 25°C 60°C →</p> <p style="text-align: center;">30min 5min 30min</p> <p style="text-align: center;">————— 1 cycle</p>	-10°C/60°C 10 cycles	—
Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	—
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msdc 3 times of each direction	—
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs	—
Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V, RS=1.5kΩ CS=100pF 1 time	—

***Supply voltage for logic system=5V. Supply voltage for LCD system = Operating voltage at 25°C



11-2 T6963C controller data

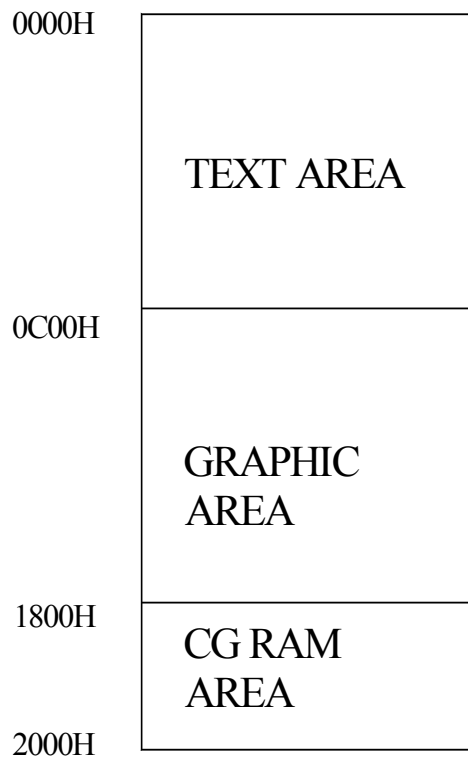
11-2.1 Display control instruction

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM (refer to Table 1.), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

•RAM Interface

The external RAM is used to store display data(text, graphic and external CG data). It can be freely allocated to the memory area(8 Kbyte max).

Recommend





- Flowchart of communications with MPU

(1)Status Read

A status check must be performed before data is read or written.

Status check

The Status of T6963C can be read from the data lines.

\overline{RD}	L
\overline{WR}	H
\overline{CE}	L
C/D	H
Do to D7	H

The T6963C status word format is as follows:

MSB							LSB
STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

STA0	Check command execution capability	0:Disable 1:Enable
STA1	Check data read/write Capability	0:Disable 1:Enable
STA2	Check Auto mode data read capability	0:Disable 1:Enable
STA3	Check Auto mode data write capability	0:Disable 1:Enable
STA4	Not used	
STA5	Check controller operation capability	0:Disable 1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy commands.	0:No error 1>Error
STA7	Check the blink condition	0:Disable off 1:Normal display



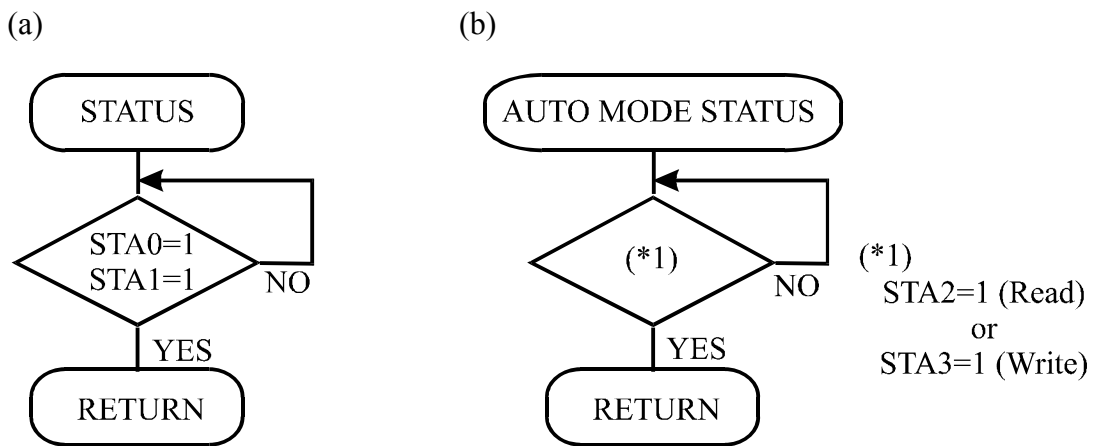
(Note 1) It is necessary to check STA0 and STA1 at the same time.

There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

Status Checking flow



(Note 4) When using the MSB=0 command, a Status Read must be performed.

If a status check is not carried out, the T6963C cannot operate normally, even after a delay time.

The hardware interrupt occurs during the address calculation period (at the end of each line).

If a MSB=0 command is sent to the T6963C during this period, the T6963C enters Wait status.

If a status check is not carried out in this state before the next command is sent, there is the possibility that the command or data data will not be received.

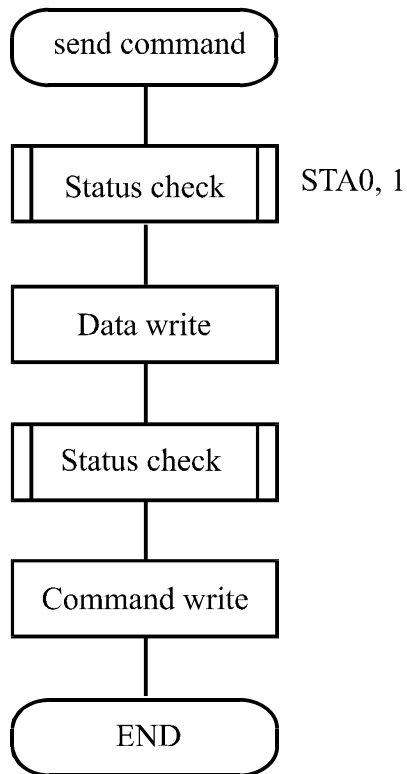


(2)Setting date

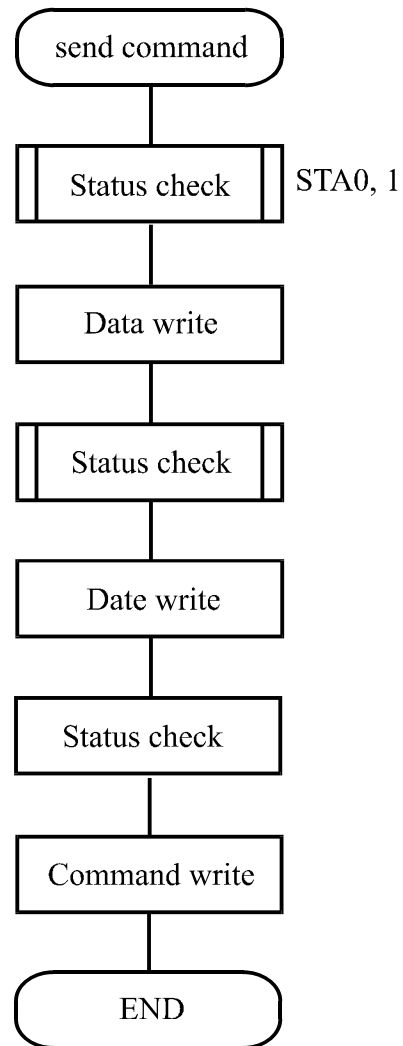
When using the T6963C, first set the data, then set the command.

Procedure for sending a command

(a)The case of 1 date



(b)The case of 2 data



(Note) When sending more than two data, the last datum (or last two data)is valid.



11-3.2 Command definitions

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001	X address	Y address	Set Cursor Pointer
	00100010	Date	00H	Set Offset Register
	00100100	Low address	High address	Set Address Pointer
SET CONTROL WORD	01000000	Low address	High address	Set Text Home Address
	01000001	Columns	00H	Set Text Area
	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00H	Set Graphic Area
MODE SET	1000x000	—	—	OR mode
	1000x001	—	—	EXOR mode
	1000x011	—	—	AND mode
	1000x100	—	—	Text Attribute mode
	10000xxx	—	—	Internal CG ROM mode
	10001xxx	—	—	External CG RAM mode
DISPLAY MODE	10010000	—	—	Display off
	1001xx10	—	—	Cursor on, blink off
	10001xx11	—	—	Cursor on, blink on
	100101xx	—	—	Text on, graphic off
	100110xx	—	—	Text off, graphic on
	100111xx	—	—	Text on, graphic on
CURSOR PATTERN SELECT	10100000	—	—	1-line cursor
	10100001	—	—	2-line cursor
	10100010	—	—	3-line cursor
	10100011	—	—	4-line cursor
	10100100	—	—	5-line cursor
	10100101	—	—	6-line cursor
	10100110	—	—	7-line cursor
	10100111	—	—	8-line cursor
DATA AUTO READ/WRITE	10110000	—	—	Set Data Auto Write
	10110001	—	—	Set Data Auto Read
	10110010	—	—	Auto Reset
DATA READ/WRITE	11000000	Data	—	Data Write and Increment ADP
	11000001	—	—	Data Read and Increment ADP
	11000010	Data	—	Data Write and Decrement ADP
	11000011	—	—	Data Read and Decrement ADP
	11000100	Data	—	Data Write and Nonvariable ADP
	11000101	—	—	Data Read and Nonvariable ADP
SCREEN PEEK	11100000	—	—	Screen Peek

X : invalid



COMMAND	CODE	D1	D2	FUNCTION
SCREEN COPY	11101000			Screen Copy
BIT SET/RESET	11110xxx	—	—	Bit Reset
	11111xxx	—	—	Bit Set
	1111x000	—	—	Bit 0 (LSB)
	11111x001	—	—	Bit 1
	11111x010	—	—	Bit 2
	11111x011	—	—	Bit 3
	11111x100	—	—	Bit 4
	11111x101	—	—	Bit 5
	11111x110	—	—	Bit 6
	11111x111	—	—	Bit 7 (MSB)

X : invalid

• Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	Y ADRS
00100010	22H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1)Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only be moved by this command. Data read/write from the MPU never changes the cursor pointer. X ADRS and Y ADRS are specified as follows.

X ADRS 00H to 4FH (lower 7 bits are valid)

Y ADRS 00H to 1FH (lower 5 bits are valid)

Single-Scan

X ADRS 00 to 4FH

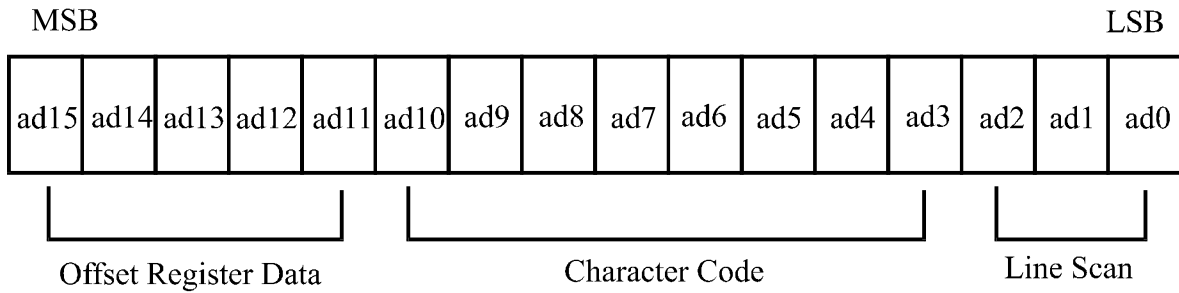
Y ADRS 00H to 0FH



(2)Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.



T6963C assign External character generator, when character code set 80H TO FFH in using internal character generator. Character code 00H to 80H assign External character generator, when External generator mode.

The senior five bits define the start address in external memory of the CG RAM area. The next eight bits represent the character code of the character. In internal CG ROM, character codes 00H to 7FH represent the predefined “internal” CG ROM characters, and codes 80H to FFH represent the user’s own “external” characters. In external CG ROM mode, all 256 codes from 00H to FFH can be used to represent the user’s own characters. The three least significant bits indicate one of the eight rows of eight dots that define the character’s shape.

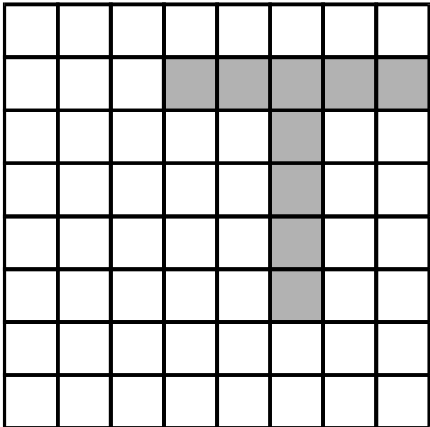


The relationship between display RAM address and offset register

Offset register data	CG RAM hex. address (start to end)
00000	0000 to 07 FFH
00001	0800 to 0FFFH
00010	1000 to 17FFH
11100	E000 to E7FFH
11101	E800 to EFFFH
11110	F000 to F7FFH
11111	F800 to FFFFH

(Example 1)

Offset register	02H
Character code	80H
Character generator RAM start address	0001 0100 0000 0000
	1 4 0 0 H

	(address)	(data)
	1400H	00H
	1401H	1FH
	1402H	04H
	1403H	04H
	1404H	04H
	1405H	04H
	1406H	04H
	1407H	00H

(Example 2) The relationship between display RAM data and display characters

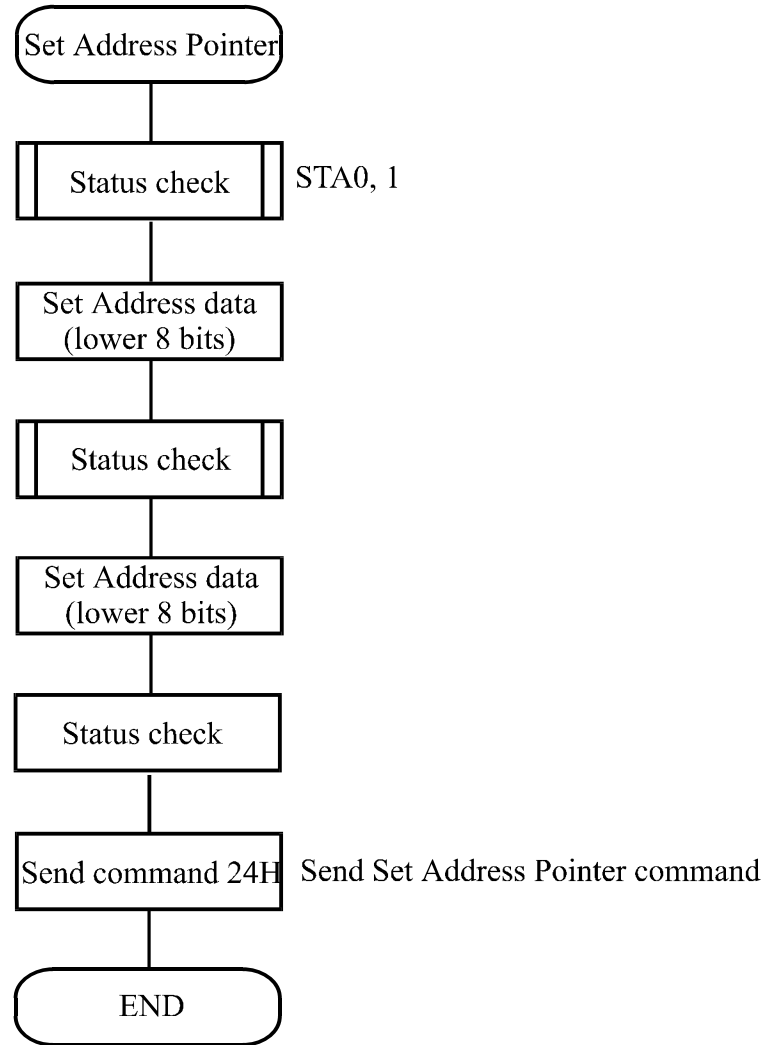
γ and ζ are displayed by character generator RAM.



(3)Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from)external RAM.

The Flowchart for Set Address Pointer command





• Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1)Set Text Home Address

The starting address in the external display RAM for text display is defined by this command.

The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH		TH+CL
TH+TA		TH+TA+CL
(TH+TA)+TA		TH+2TA+CL
(TH+2TA)+TA		TH+3TA+CL
TH+(n-1)TA		TH+(n-1)TA+CL

TH:Text home address

TA:Text area number (columns)

CL:Columns are fixed by hardware (pin-programmable).



(Example)

Text home address : 0000H
 Text area : 0020H
 : 32 Columns
 : 4 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	002FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

(2)Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this command. The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH		GH+GL
GH+GA		GH+GA+CL
(GH+GA)+GA		GH+2GA+CL
(GH+2GA)+GA		GH+3GA+CL
GH+(n-1)GA		GH+(n-1)GA+CL

GH:Graphic home address

GA:Graphic area number (columns)

CL:Columns are fixed by hardware (pin-programmable).



(Example)

Graphic home address : 0000H
 Graphic area : 0020H
 : 32 Columns
 : 2 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	003FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH
0080H	0081H		009EH	009FH
00A0H	00A1H		00BEH	00BFH
00C0H	00C1H		00DEH	00DFH
00E0H	00E1H		00FEH	00FFH
0100H	0101H		011EH	011FH
0120H	0121H		013EH	013FH
0140H	0141H		015EH	014FH
0160H	0161H		017EH	017FH
0180H	0181H		109EH	019FH
01A0H	01A1H		01BEH	01BFH
01C0H	01C1H		01DEH	01DFH
01E0H	01E1H		01FEH	01FFH



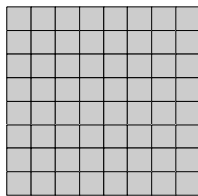
• Mode set

CODE	FUNCTION	OPERAND
1000x000	OR Mode	—
1000x001	EXOR Mode	—
1000x011	AND Mode	—
1000x100	TEXT ATTRIBUTE Mode	—
10000xxx	Internal Character Generator Mode	—
10001xxx	External Character Generator Mode	—

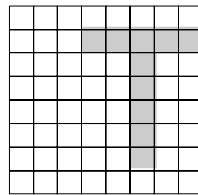
X:invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

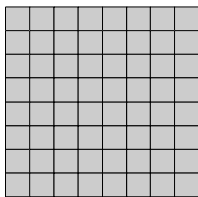
(Example)



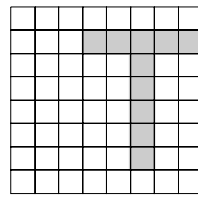
GRAPHIC



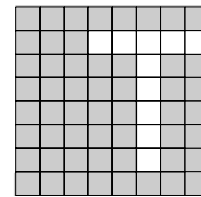
TEXT



“OR”



“AND”



“TXOR”

(Note)Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.



Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

The attribute data for each character in the text area is written to the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1byte

×	×	×	×	d3	d2	d1	d0
---	---	---	---	----	----	----	----

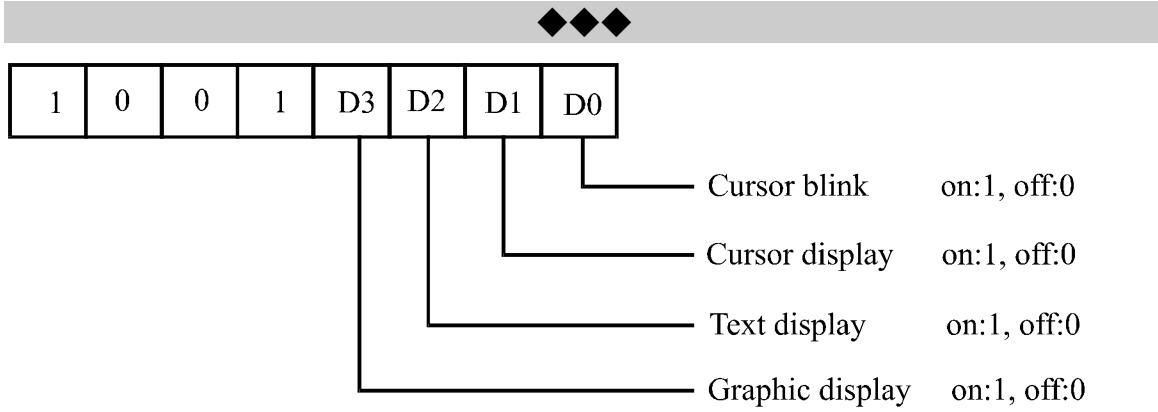
d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

X:invalid

• Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	—
1001xx10	Cursor on, blink off	—
1001xx11	Cursor on, blink on	—
100101xx	Text on, graphic off	—
100110xx	Text off, graphic on	—
100111xx	Text on, graphic on	—

X:invalid



(Note)It is necessary to turn on “Text display” and “Graphic display” in the following cases.

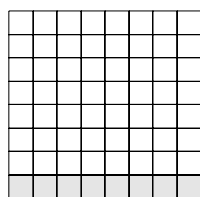
- a)Combination of text/graphic display
- b)Attribute function

• Cursor pattern select

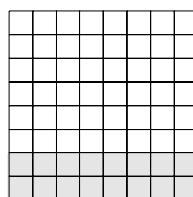
CODE	FUNCTION	OPERAND
10100000	1-line cursor	—
10100001	2-line cursor	—
10100010	3-line cursor	—
10100011	4-line cursor	—
10100100	5-line cursor	—
10100101	6-line cursor	—
10100110	7-line cursor	—
10100111	8-line cursor	—

When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines.

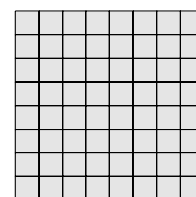
The cursor address is defined by the Cursor Pointer Set command.



1-line cursor



2-line cursor



8-line cursor



- Data Auto Read/Write

CODE	HEX.	FUNCTION	OPERAND
10110000	B0H	Set Data Auto Write	—
1011001	B1H	Set Data Auto Read	—
10110010	B2H	Auto Reset	—

The command is convenient for sending a full screen of data from the external display RAM. After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command. After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

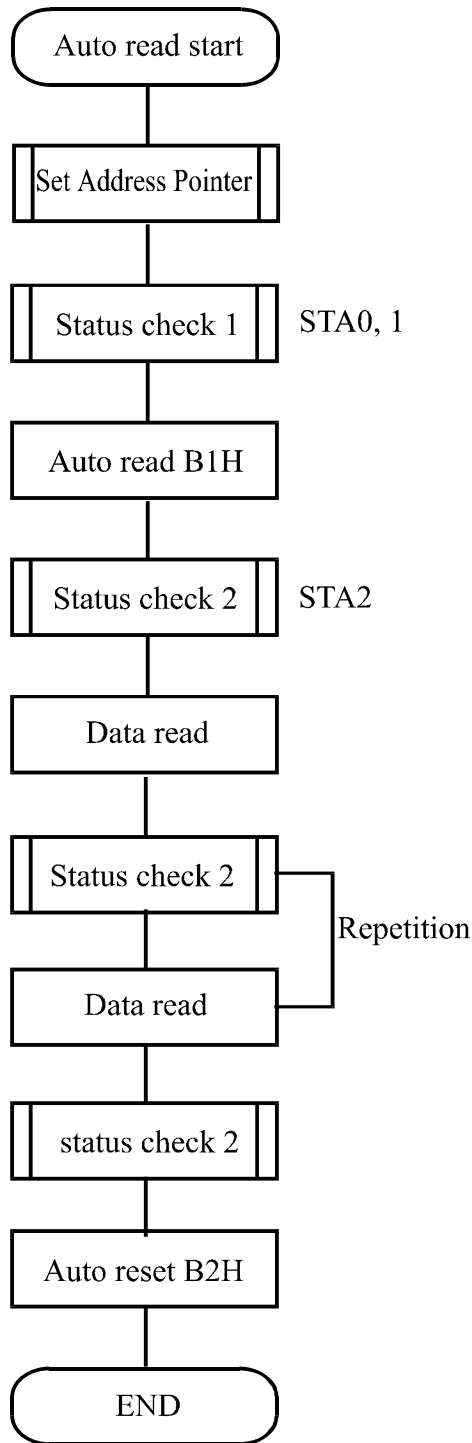
The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode.

(Note)A Status check for Auto mode

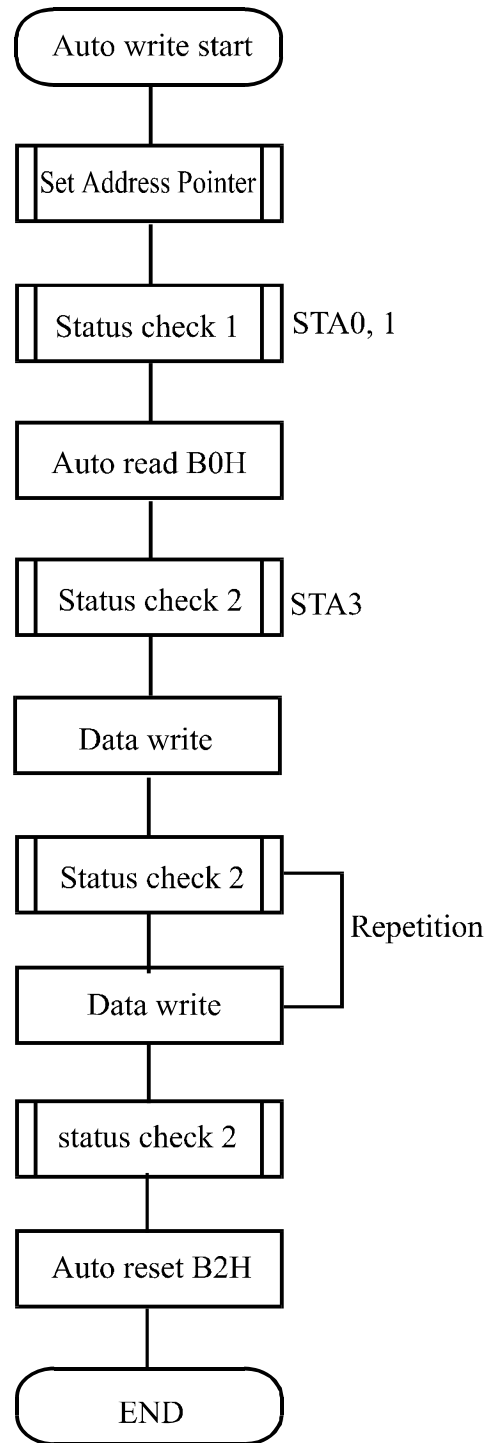
(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.

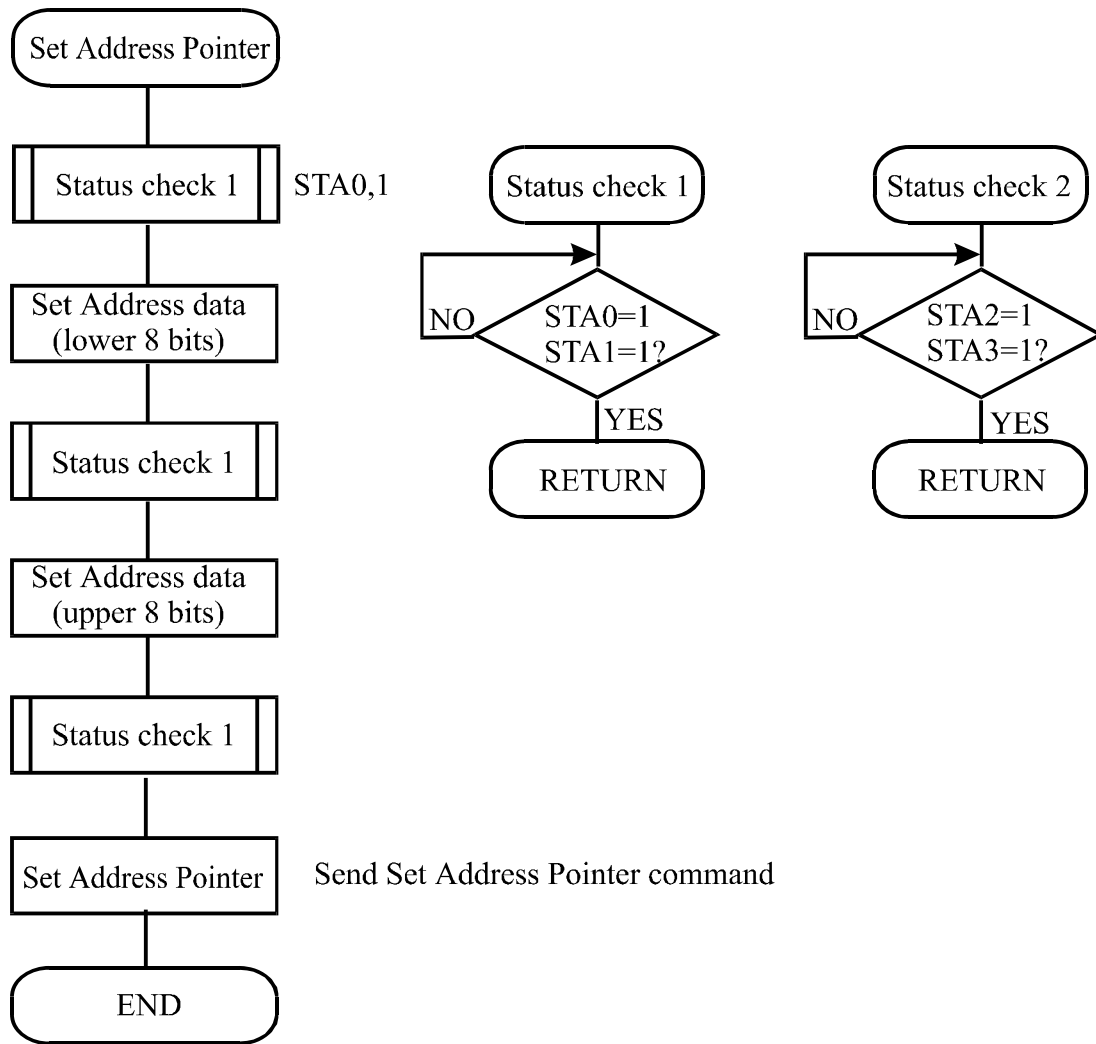


a) Auto Read mode



b) Auto Write mode







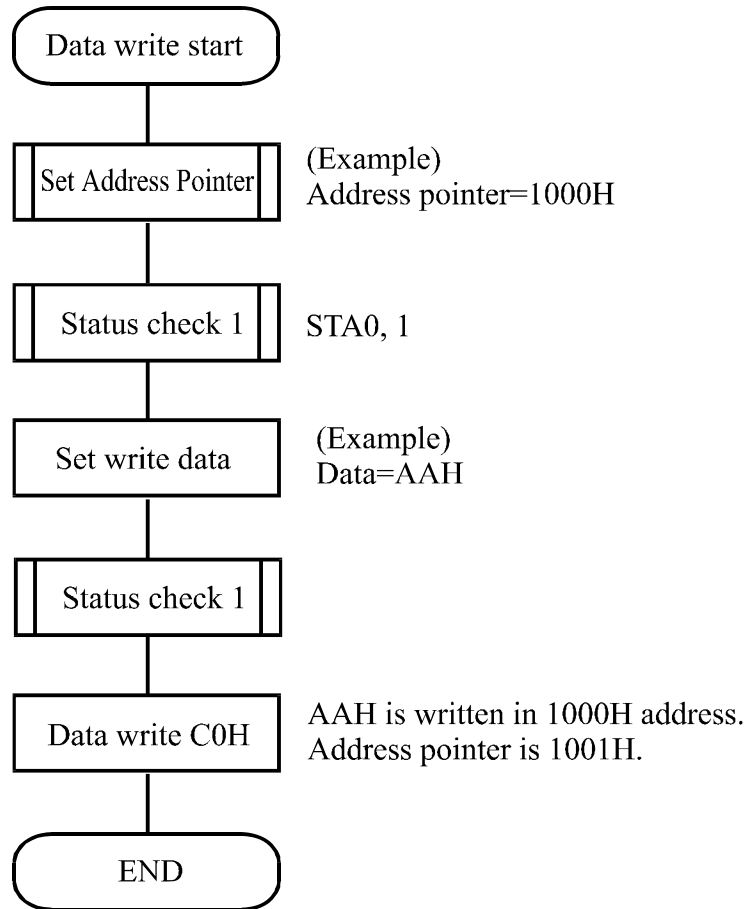
• Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	C0H	Data Write and Increment ADP	Data
11000001	C1H	Data Read and Increment ADP	—
11000010	C2H	Data Write and Decrement ADP	Data
11000011	C3H	Data Read and Decrement ADP	—
11000100	C4H	Data Write and Nonvariable ADP	Data
11000101	C5H	Data Read and Nonvariable ADP	—

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note) This command is necessary for each 1-byte datum.

Refer to the following flowchart.





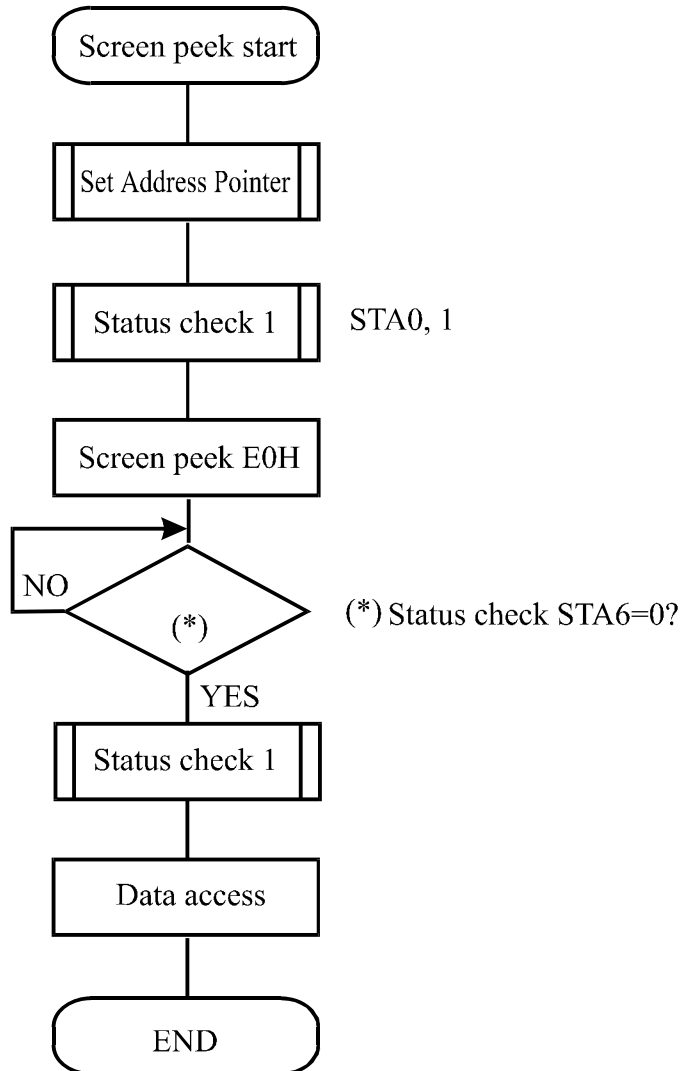
• Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	E0H	Screen Peek	-e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this command is ignored and a status flag (STA6) is set.

Refer to the following flowchart.





• Screen Copy

CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	—

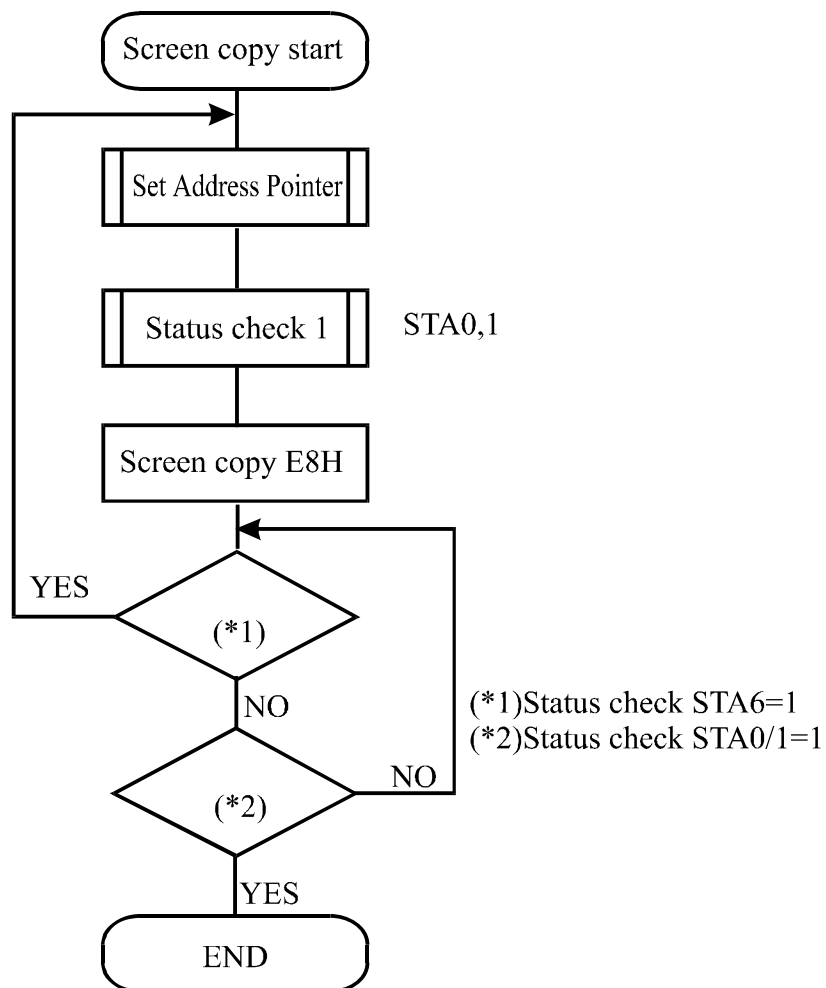
This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1) If the attribute function is being used, this command is not available.

(With Attribute data is graphic area data.)

Refer to the following flowchart.



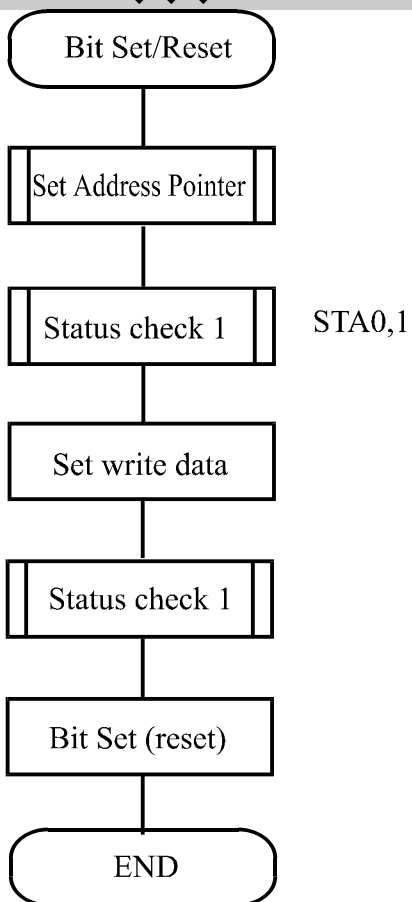
• Bit Set/Reset



CODE	FUNCTION	OPERAND
11110xxx	Bit Reset	—
11111xxx	Bit Set	—
1111x000	Bit 0 (LSB)	—
1111x001	Bit 1	—
1111x010	Bit 2	—
1111x011	Bit 3	—
1111x100	Bit 4	—
1111x101	Bit 5	—
1111x110	Bit 6	—
1111x111	Bit 7 (MSB)	—

X:invalid

This command use to set or reset a bit of the byte specified by the address pointer.
 Only one bit can be set/reset at a time.
 Refer to the following flowchart.





11-3.3 Character code

Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH
LLLL		0	1	2	3	4	5	6
LLLH	!	7	8	9	A	B	C	D
LLHL	"	E	F	G	H	I	J	K
LLHH	L	M	N	O	P	Q	R	S
LHLL	T	U	V	W	X	Y	Z	[
LHLH	^	_	0	1	2	3	4	5
LHHL	6	7	8	9	:	;	<	=
LHHH	>	?	@	A	B	C	D	E
HLLL	F	G	H	I	J	K	L	M
HLLH	N	O	P	Q	R	S	T	U
HLHL	V	W	X	Y	Z	[]	^
HLHH	_	0	1	2	3	4	5	6
HHLL	7	8	9	:	;	<	=	>
HHLH	?	@	A	B	C	D	E	F
HHHL	G	H	I	J	K	L	M	N
HHHH	O	P	Q	R	S	T	U	V



11-3.4. Timing characteristics

Bus Timing

($V_{SS} = 0\text{ V}$, $V_{DD} = 5\text{ V}$)

Item	Symbol	Min	Typ	Max	Unit
C/D Set-up Time	t _{CDS}	100	—	—	ns
C/D Hold Time	t _{CDH}	10	—	—	ns
CE,RD,WR Pulse Width	t _{CDS} ,t _{RD} ,t _{WR}	80	—	—	ns
Data Set-up Time	t _{DS}	80	—	—	ns
Data Hold Time	t _{DH}	40	—	—	ns
Access Time	t _{ACC}	—	—	150	ns
Output Hold Time	t _{OH}	10	—	50	ns

