

## VHF amplifier module

BGY145C

T-74-09-01

### DESCRIPTION

An RF amplifier module designed for use in transmitters of mobile communications equipment powered by a 12.5 V battery supply.

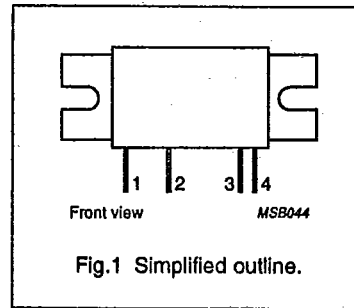
The module is a two stage transistor amplifier and consists of two RF npn transistors mounted on a ceramic substrate, together with surface mounted components that make up the matching and bias circuits.

The module will provide 27 W RF power into a 50  $\Omega$  load, when operated at nominal conditions within the frequency range of 174 to 200 MHz.

### PINNING - SOT183A

| PIN    | DESCRIPTION |
|--------|-------------|
| 1      | output      |
| 2      | $V_{S2}$    |
| 3      | $V_{S1}$    |
| 4      | input       |
| flange | ground      |

### PIN CONFIGURATION



### QUICK REFERENCE DATA

Mode of operation: continuous wave.

| SYMBOL           | PARAMETER             | CONDITIONS             | MIN. | TYP. | MAX. | UNIT     |
|------------------|-----------------------|------------------------|------|------|------|----------|
| f                | frequency range       |                        | 174  | —    | 200  | MHz      |
| $P_L$            | RF output power       | $P_0 = 300 \text{ mW}$ | 27   | —    | —    | W        |
| $G_p$            | RF power gain         | $P_L = 27 \text{ W}$   | 19.5 | —    | —    | dB       |
| $\eta$           | efficiency            | $P_L = 27 \text{ W}$   | 35   | —    | —    | %        |
| $V_{S1}, V_{S2}$ | DC supply voltage     |                        | —    | 12.5 | —    | V        |
| $Z_i$            | input impedance       |                        | —    | 50   | —    | $\Omega$ |
| $Z_L$            | output load impedance |                        | —    | 50   | —    | $\Omega$ |

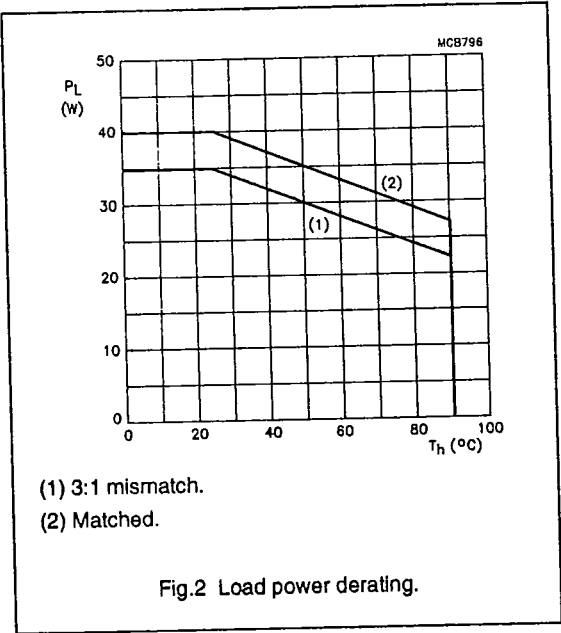
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LIMITING VALUES  
In accordance with the Absolute Maximum System (IEC 134).

| SYMBOL           | PARAMETER                      | MIN. | MAX. | UNIT |
|------------------|--------------------------------|------|------|------|
| $V_{S1}, V_{S2}$ | DC supply voltage              | -    | 15.5 | V    |
| $\pm V_i$        | RF input terminal voltage      | -    | 25   | V    |
| $\pm V_o$        | RF output terminal voltage     | -    | 25   | V    |
| $P_D$            | RF input power                 | -    | 450  | mW   |
| $P_L$            | RF output power (see Fig.2)    | -    | 40   | W    |
| $T_{stg}$        | storage temperature range      | -30  | 100  | °C   |
| $T_h$            | heatsink operating temperature | -    | 90   | °C   |



CHARACTERISTICS  
 $T_h = 25\text{ °C}$ ;  $V_{S1} = V_{S2} = 12.5\text{ V}$ ;  $R_S = R_L = 50\text{ }\Omega$ ; frequency range = 174 to 200 MHz.

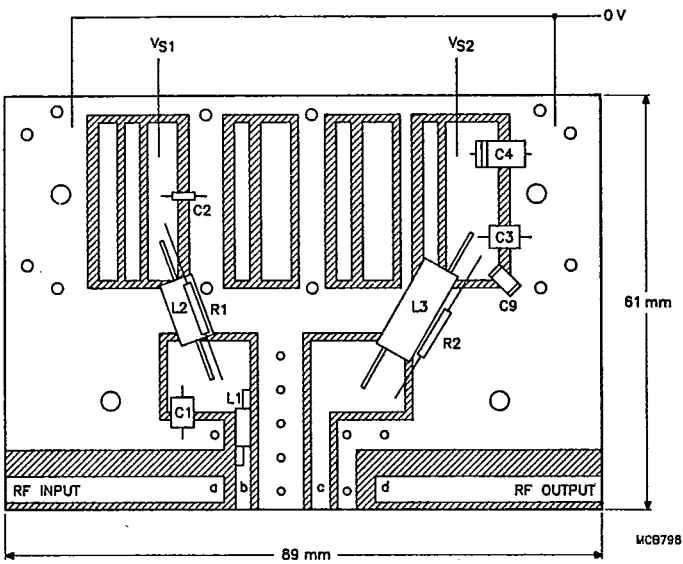
| SYMBOL   | PARAMETER                              | CONDITIONS            | MIN. | MAX. | UNIT |
|----------|----------------------------------------|-----------------------|------|------|------|
| $I_{Q1}$ | quiescent current                      | $P_D = 0$             | -    | 25   | mA   |
| $I_{Q2}$ | quiescent current                      | $P_D = 0$             | -    | 55   | mA   |
| $P_L$    | RF output power                        | $P_D = 300\text{ mW}$ | 27   | -    | W    |
| $G_p$    | RF power gain                          | $P_L = 27\text{ W}$   | 19.5 | -    | dB   |
| $\eta$   | efficiency                             | $P_L = 27\text{ W}$   | 35   | -    | %    |
| $H_{R2}$ | 2nd harmonic output                    | $P_L = 27\text{ W}$   | -    | -30  | dBc  |
| $H_{R3}$ | 3rd harmonic output                    | $P_L = 27\text{ W}$   | -    | -40  | dBc  |
|          | input VSWR with respect to 50 $\Omega$ | $P_L = 27\text{ W}$   | -    | 2:1  |      |

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APPLICATION INFORMATION



Epoxy fibre-glass board, 1/16 inch thick,  $\epsilon_r = 4.5$ .  
L2 to be 90 degrees to L3.  
Track widths 'a' and 'd' ( $50 \Omega$ ) = 4 mm.  
Track width 'b' = 2.5 mm.  
Track width 'c' = 3 mm.  
Circles indicate the positions of through rivets.

Fig.3 Test jig.

LIST OF COMPONENTS (Fig.3)

| DESIGNATION | DESCRIPTION                             | VALUE       | DIMENSIONS       |
|-------------|-----------------------------------------|-------------|------------------|
| C1, C3      | 63 V miniature polyester film capacitor | 470 nF      |                  |
| C2          | 35 V tantalum bead capacitor            | 4.7 $\mu$ F |                  |
| C4          | 35 V tantalum capacitor                 | 1 $\mu$ F   |                  |
| C9          | 50 V multilayer ceramic capacitor       | 47 nF       |                  |
| L1          | 4 turns 0.45 mm diameter copper wire    |             | int. dia. 2.5 mm |
| L2          | 4 turns 1 mm diameter copper wire       |             | int. dia. 9 mm   |
| L3          | 12 turns 1 mm diameter copper wire      |             | int. dia. 9 mm   |
| R1, R2      | 0.5 W resistor                          | 12 $\Omega$ |                  |

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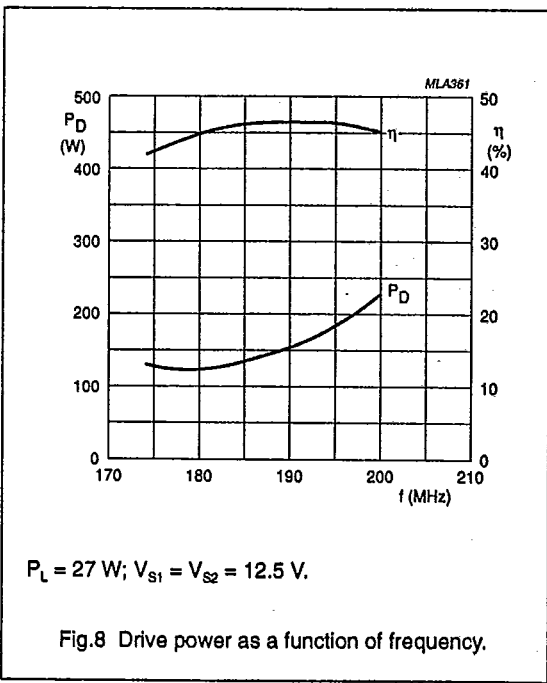
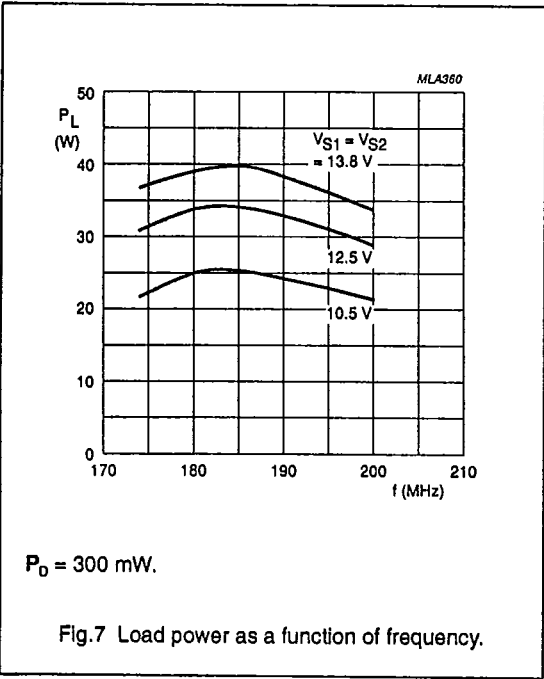
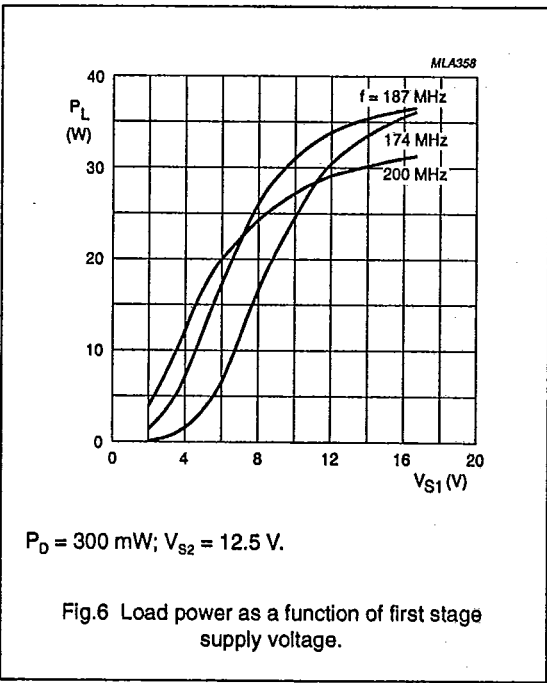
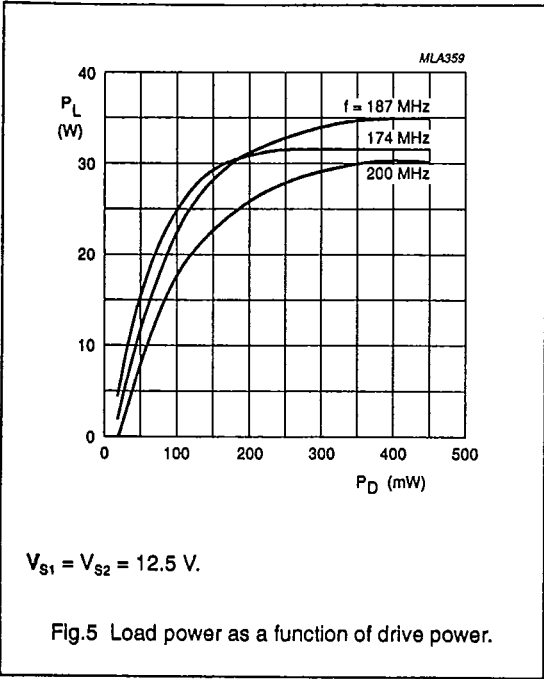
The schematic diagram illustrates a microwave amplifier circuit. At the top, a rectangular block contains two triangular symbols representing transistors connected in series. The input port of the first transistor is labeled "RF INPUT" and has terminals 3 and 4. The output port of the second transistor is labeled "RF OUTPUT" and has terminals 1 and 2. A 30 nH inductor connects terminal 3 to a node between a 470 nF, 63 V miniature polycapacitor and ground. This node also branches to a parallel combination of a 100 nH inductor and a 12 Ω resistor, which then connects to a node between another 4.7 μF, 35 V tantalum capacitor and ground. Terminal 4 is connected to this same node and is labeled "VS1". Terminal 2 is connected to a series stack of components leading to terminal 1 (labeled "VS2"): a 12 Ω resistor, a 47 nF, 35 V ceramic multilayer chip capacitor, a 470 nF, 63 V miniature polycapacitor, and a 1 μF, 35 V tantalum capacitor.

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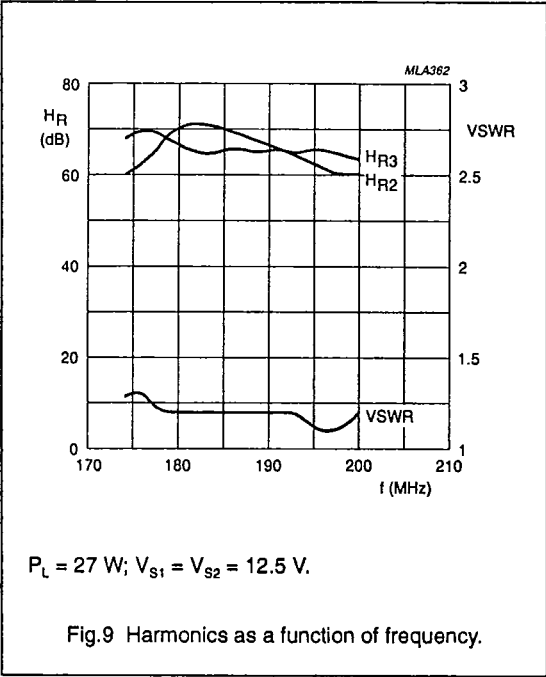
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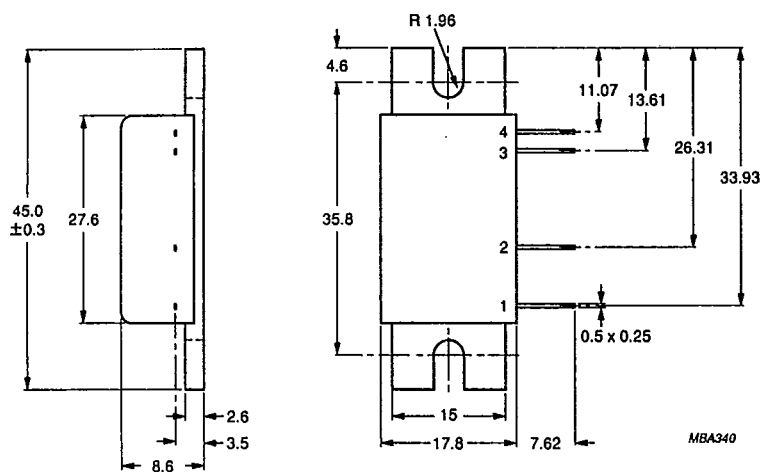


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PACKAGE OUTLINE



Dimensions in mm.

Fig.10 SOT183A.