

### **Features**

- 650 V, 50 A, Low Collector-Emitter Saturation Voltage (V<sub>CE(sat)</sub>)
- Trench-Gate Field-Stop technology
- Optimized for conduction
- RoHS compliant\*

# **Applications**

- Switch-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)
- Inverters

# **BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)**

### **General Information**

The Bourns® Model BIDW50N65T IGBT device combines technology from a MOS gate and a bipolar transistor for an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics with a lower Collector-Emitter Saturation Voltage (V<sub>CE(sat)</sub>) and fewer switching losses. In addition, this structure provides a lower thermal resistance R<sub>(th)</sub>.

### **Additional Information**

Click these links for more information:











TECHNICAL INVENTORY SAMPLES

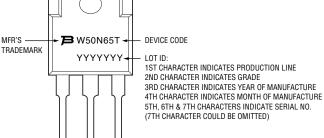
### Maximum Electrical Ratings (T<sub>C</sub> = 25 °C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CES</sub>	650	V
Continuous Collector Current (T <sub>C</sub> = 25 °C), limited by T <sub>jmax</sub>	Ic	100	А
Continuous Collector Current ( $T_C = 100  ^{\circ}\text{C}$ ), limited by $T_{jmax}$	Ic	50	А
Pulsed Collector Current, t <sub>p</sub> limited by T <sub>jmax</sub>	I <sub>CP</sub>	150	А
Gate-Emitter Voltage	V <sub>GE</sub>	±20	V
Continuous Forward Current (T <sub>C</sub> = 100 °C), limited by T <sub>jmax</sub>	I <sub>F</sub>	50	А
Short-circuit Withstand Time (V <sub>CE</sub> = 300 V, V <sub>GE</sub> = 15 V)	T <sub>SC</sub>	10	μs
Total Power Dissipation	P <sub>total</sub>	416	W
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C
Operating Junction Temperature	Tj	-55 to +150	°C

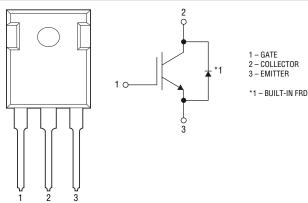
### **Thermal Resistance**

**Typical Part Marking** 

Parameter	Symbol	Max	Unit
IGBT Thermal Resistance Junction - Case	R <sub>th(j-c)_IGBT</sub>	0.3	°C/W
Diode Thermal Resistance Junction - Case	R <sub>th(j-c)_Diode</sub>	0.65	°C/W









## Static Electrical Characteristics (T<sub>C</sub> = 25 °C, Unless Otherwise Specified)

Peremeter	Complete	Conditions	Value			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Oilit	
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	650	_	_	V	
Collector-Emitter Saturation Voltage	V	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A T <sub>C</sub> = 25 °C	_	1.65	2.2	V	
	V <sub>CE(sat)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A T <sub>C</sub> = 125 °C	_	1.9	_		
Diada Famuard On Valtana		I <sub>F</sub> = 50 A, T <sub>C</sub> = 25 °C	_	1.7	2.5	V	
Diode Forward On-Voltage	V <sub>F</sub>	I <sub>F</sub> = 50 A, T <sub>C</sub> = 125 °C	_	1.3	_	V	
Gate Threshold Voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}, I_{C} = 250 \mu\text{A}$	4.0	5.0	7.0	V	
Collector Cut-off Current	I <sub>CES</sub>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 650 V	_	_	200	μΑ	
Gate-Emitter Leakage Current	I <sub>GES</sub>	$V_{CE} = 0 \text{ V}, V_{GE} = \pm 20 \text{ V}$	_	_	±400	nA	

### Dynamic Electrical Characteristics (T<sub>C</sub> = 25 °C, Unless Otherwise Specified)

Parameter	Complete	Symbol Conditions —	Value			l l mid
	Symbol		Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>		_	2723	_	
Output Capacitance	C <sub>oes</sub>	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$ f = 1  MHz	_	230	_	pF
Reverse Transfer Capacitance	C <sub>res</sub>		_	55	_	
Total Gate Charge	Qg		_	123	_	
Gate-Emitter Charge	Q <sub>ge</sub>	$V_{CE} = 400 \text{ V}, V_{GE} = 15 \text{ V}$ $I_{C} = 50.0 \text{ A}$	_	31	_	nC
Gate-Collector Charge	Q <sub>gc</sub>	]	_	48	_	

## IGBT Switching Characteristics (Inductive Load, T<sub>C</sub> = 25 °C, unless otherwise specified)

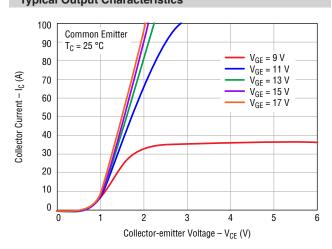
Parameter	Cumbal	Symbol Conditions —	Value			Unit
	Symbol		Min.	Тур.	Max.	Onit
Turn-on Delay Time	t <sub>d(on)</sub>		_	37	_	ns
Current Rise Time	t <sub>r</sub>		_	133	_	ns
Turn-off Delay Time	t <sub>d(off)</sub>		_	125	_	ns
Current Fall Time	t <sub>f</sub>	$V_{CE} = 400 \text{ V}, V_{GE} = 15 \text{ V}$ $I_{C} = 50.0 \text{ A}, R_{G} = 10 \Omega$	_	121	_	ns
Turn-on Switching Energy	E <sub>on</sub>	( = 30.0 A, Fig = 10.32	_	3.0	_	mJ
Turn-off Switching Energy	E <sub>off</sub>		_	1.1	_	mJ
Total Switching Energy	E <sub>ts</sub>		_	4.1	_	mJ

### Diode Switching Characteristics (T<sub>C</sub> = 25 °C, unless otherwise specified)

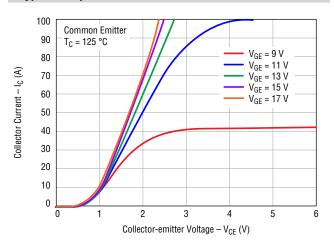
Parameter	Symbol	Conditions	Value			Unit
Parameter	Symbol	Min.	Тур.	Max.	Onit	
Reverse Recovery Time	t <sub>rr</sub>	$dI_F/dt = 200 A/\mu s$	_	37.5	_	ns
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 50.0 A	_	78	_	nC

### **Electrical Characteristic Performance**

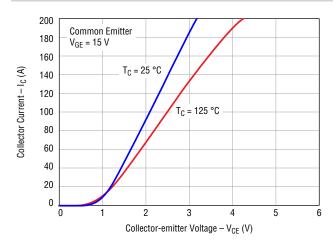
# **Typical Output Characteristics**



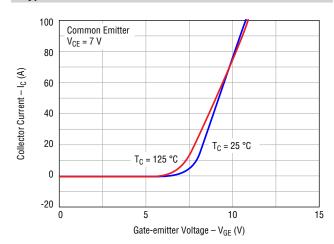
### **Typical Output Characteristics**



### **Typical Saturation Voltage Characteristics**

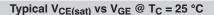


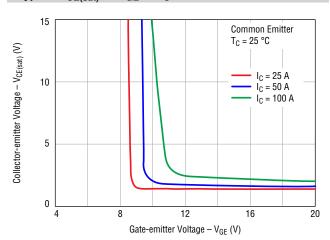
### **Typical Transfer Characteristics**



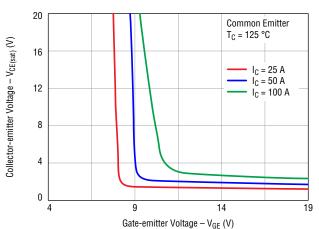
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### **Electrical Characteristic Performance (continued)**

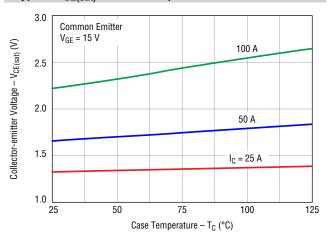




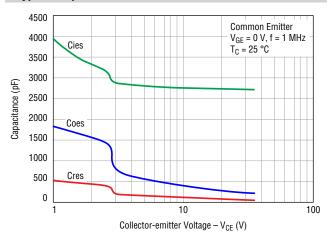
# Typical $V_{CE(sat)}$ vs $V_{GE}$ @ $T_{C}$ = 125 °C



### Typical V<sub>CE(sat)</sub> vs Case Temperature



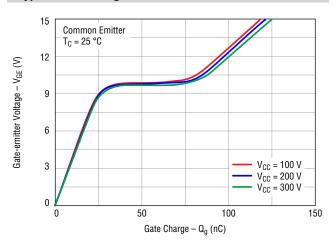
### **Typical Capacitance Characteristics**

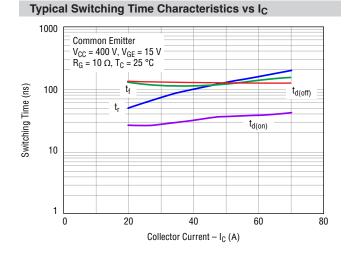


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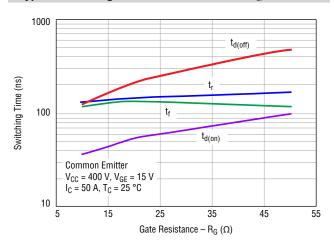
### **Electrical Characteristic Performance (continued)**

### **Typical Gate Charge Characteristics**

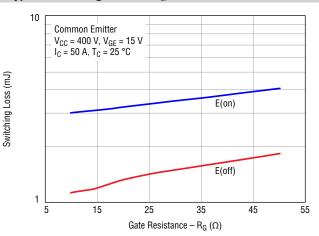




### Typical Switching Time Characteristics vs R<sub>G</sub>

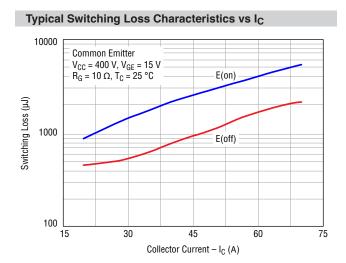


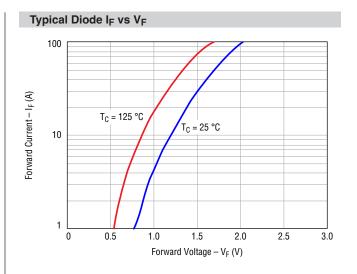
### Typical Switching Loss vs R<sub>G</sub>

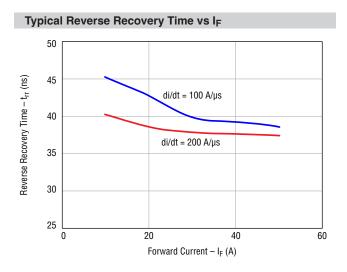


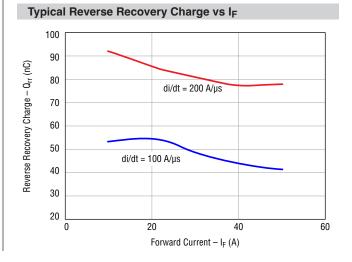
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### **Electrical Characteristic Performance (continued)**



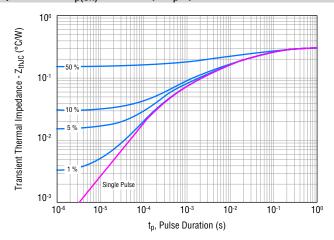




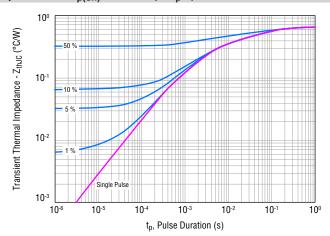


### **Electrical Characteristic Performance (continued)**

## IGBT Transient Thermal Impedance vs tp(on) Duration (D=tp/T)



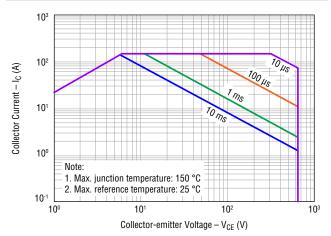
# Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration (D= $t_p/T$ )



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### **Electrical Characteristic Performance (continued)**

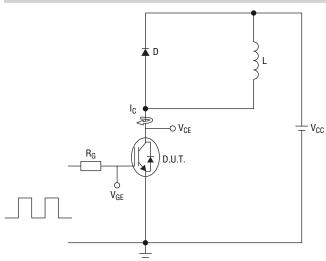
## Forward Bias Safe Operating Area



### **How to Order**

# B I D W 50 N 65 T B = Bourns® I = IGBT Type D = Discrete Package Code W = TO-247-3L Current Rating 50 = 50 A Device Type N = N-channel Nominal Voltage (divided by 10) 65 = 650 V Optimization T = Medium Speed

### **Inductive Load Test Circuit**



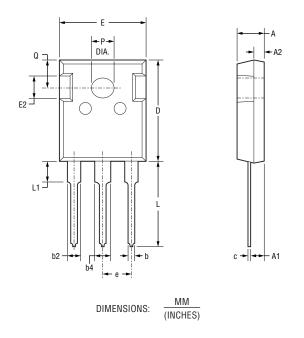
L=1.12 mH,  $V_{CE}=400$  V,  $V_{GE}=15$  V,  $I_{C}=50$  A,  $R_{G}=10~\Omega$ 

### **Environmental Characteristics**

ESD Class (HBM).....2

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### **Product Dimensions**



Packaging Specifications	

BIDW50N65T ......30 pieces per tube

Symbol	Min.	Nom.	Max.
А	4.80	5.00	5.20
	(.189)	(.197)	(.205)
A1	2.21	2.41	2.59
	(.087)	(.095)	(.102)
A2	1.85	2.00	2.15
	(.073)	(.079)	(.085)
b	1.11 (.044)	_	1.36 (.054)
b2	1.91 (.075)	_	2.25 (.089)
b4	2.91 (.115)	_	3.25 (.128)
С	0.51 (.020)	_	0.75 (.030)
D	20.80	<u>21.00</u>	21.30
	(.819)	(.827)	(.839)
Е	15.50	15.80	16.10
	(.610)	(.622)	(.634)
E2	4.40	<u>5.00</u>	5.20
	(.173)	(.197)	(.205)
е		$\frac{5.44}{(.214)}$ BSC	
L	19.72	19.92	20.22
	(.776)	(.784)	(.796)
L1	_	_	4.30 (.169)
Р	3.40 (.134)	_	3.80 (.150)
Q	5.60	5.80	6.00
	(.220)	(.228)	(.236)

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