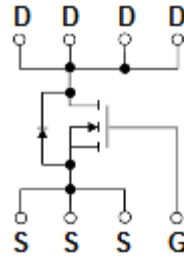


Features

- Super low gate charge
- Green device available
- Excellent c_{dv} / d_t effect decline
- Advanced high cell density trench technology

HF



PDFN5x6-8L

Mechanical Data

- Case: PDFN5x6-8L
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL055N06T-5DL8	PDFN5x6-8L	5000 pcs / Tape & Reel	055N06T

Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	60	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$ silicon limited)	I_D	115	A
Continuous Drain Current ($T_C = 100^\circ\text{C}$)		74	A
Pulsed Drain Current ($t_p=10\mu\text{s}$)	I_{DM}	160	A
Single Pulse Avalanche Energy ^{*3}	E_{AS}	80	mJ

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	125	W
Thermal Resistance Junction-to-Case ^{*4}	$R_{\theta JC}$	1	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	47	$^\circ\text{C/W}$
Operating Junction Temperature Range	T_J	$-55 \sim +150$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 48V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Static Drain-Source On-resistance *2	V _{GS} = 10V,I _D = 30A	-	-	5.5	mΩ
		V _{GS} = 4.5V,I _D = 20A	-	-	9	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1	-	2.5	V
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = 30V f = 1.0MHz	-	1830	-	pF
C _{OSS}	Output Capacitance		-	550	-	
C _{RSS}	Reverse Transfer Capacitance		-	25	-	
Q _G	Total Gate-Charge (V _{GS} = 4.5V)	V _{DD} = 48V V _{GS} = 10V I _D = 15A	-	17.8	-	nC
Q _{GS}	Gate to Source Charge		-	5.8	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	7.9	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time	V _{DD} = 15V V _{GS} = 10V R _G = 3.3Ω I _D = 15A	-	7.5	-	ns
t _r	Turn-on Rise Time		-	6	-	
t _{d(OFF)}	Turn-Off Delay Time		-	29	-	
t _f	Turn-Off Fall Time		-	7.5	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage	I _S = 5A, V _{GS} = 0V, T _J = 25°C	-	-	1.2	V
t _{rr}	Reverse Recovery Time	I _F = 30A, T _J = 25°C	-	23	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt = 100A/μs	-	60	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 0.5mH$
- Thermal resistance from junction to soldering point (on the exposed drain pad).

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

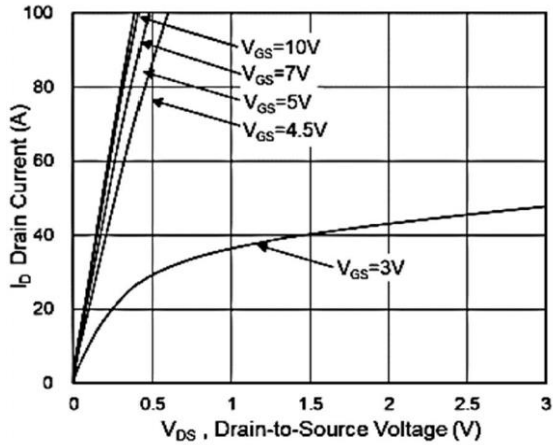


Fig 1 Output Characteristics

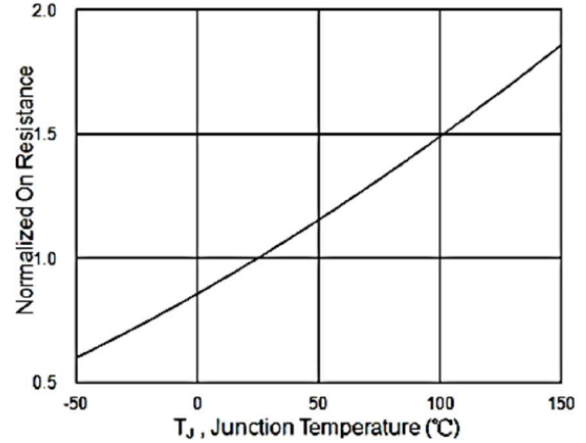


Fig 2 On-Resistance vs. Junction Temperature

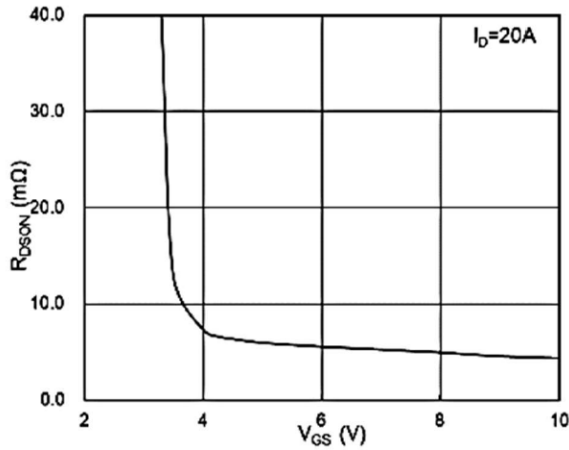


Fig 3 On-Resistance vs. Gate-Source Voltage

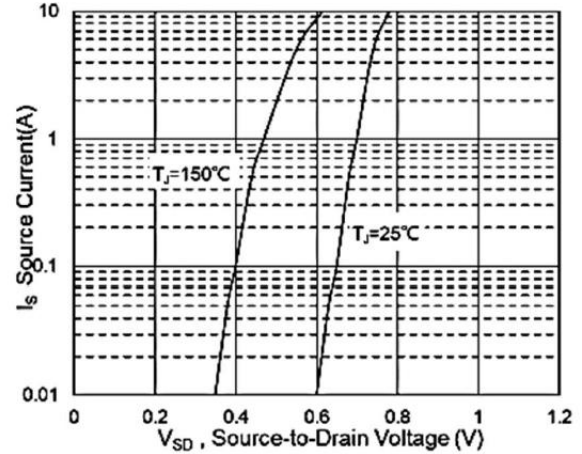


Fig 4 Body-Diode Characteristics

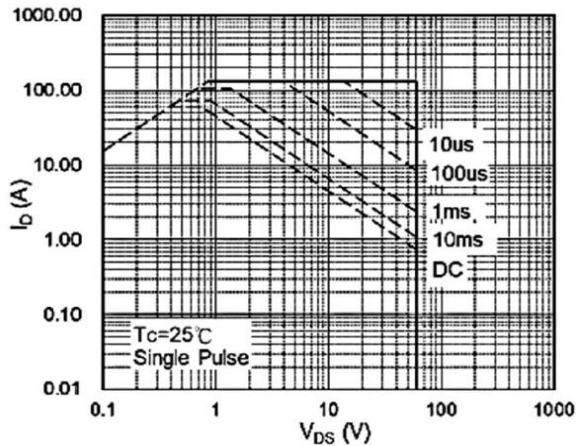


Fig 5 Maximum Safe Operating Area

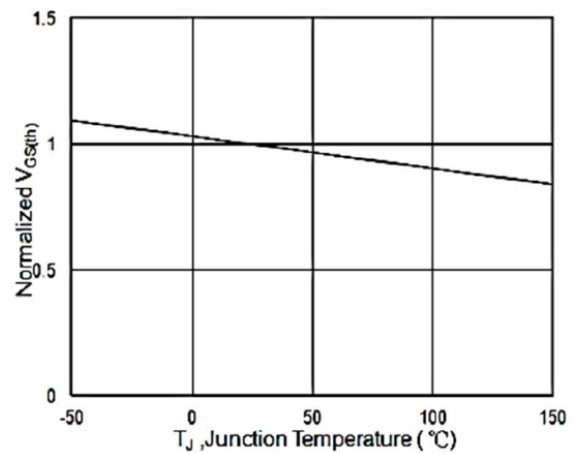


Fig 6 Gate Threshold Voltage vs. Junction Temperature

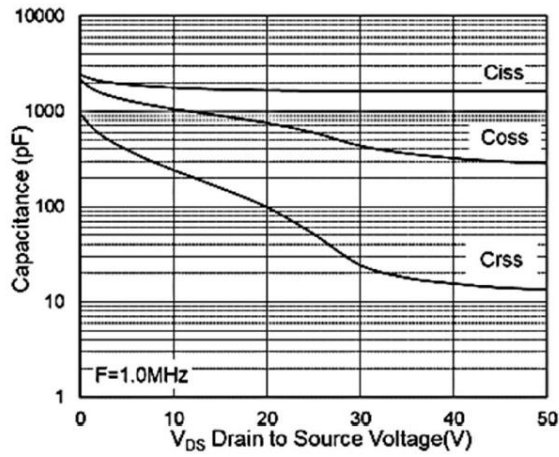


Fig 7 Capacitance Characteristics

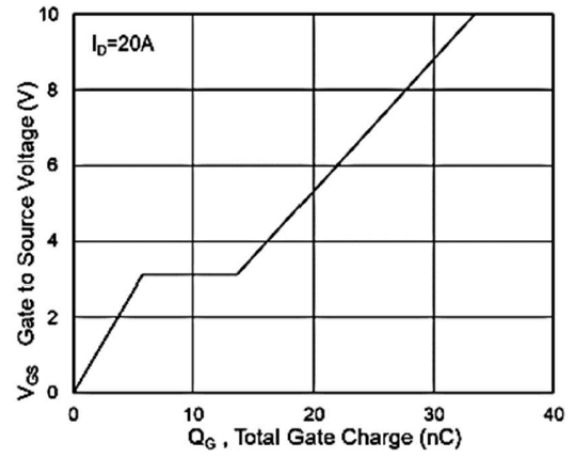
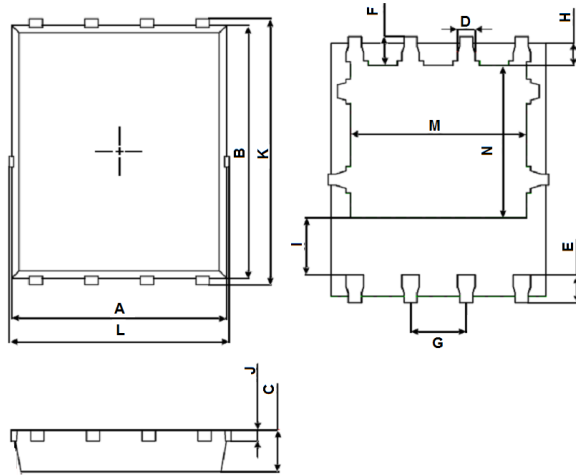


Fig 8 Gate-Charge Characteristics

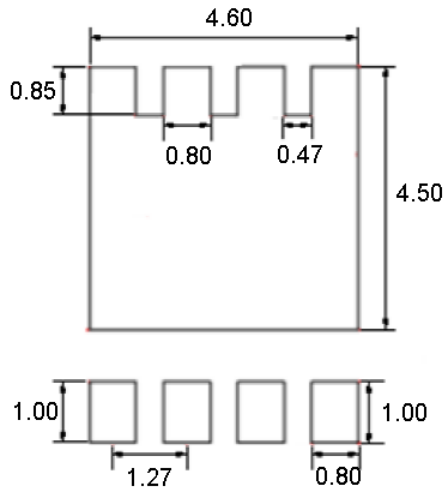
Package Outline Dimensions (Unit: mm)



PDFN5x6-8L		
Dimension	Min.	Max.
A	4.824	4.976
B	5.674	5.826
C	0.900	1.000
D	0.350	0.450
E	0.559	0.711
F	0.574	0.726
G	1.250	1.290
H	0.424	0.576
I	1.190	1.390
J	0.154	0.354
K	5.974	6.126
L	4.944	5.096
M	3.910	4.110
N	3.375	3.575

Mounting Pad Layout (Unit: mm)

PDFN5x6-8L



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