



## P-Channel High Density Trench MOSDET

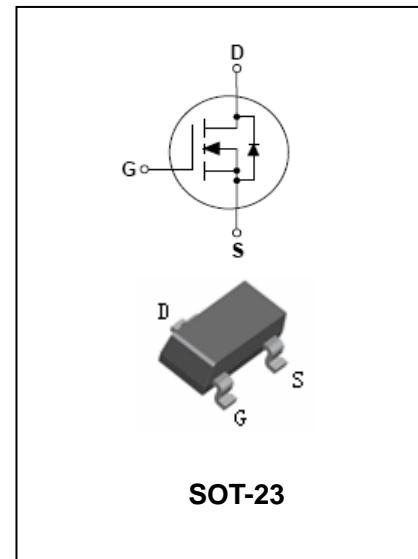
BL3401

### FEATURES

- Super high dense cell trench design for low  $R_{DS(ON)}$ .
- Rugged and Reliable.



Lead-free



### APPLICATIONS

- P-channel enhancement mode effect transistor.
- Switching application.

### ORDERING INFORMATION

Type No.	Marking	Package Code
BL3401	A19TF	SOT-23

### MAXIMUM RATING @ $T_a=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain-Source voltage	-25	V
$V_{GSS}$	Gate -Source voltage	$\pm 12$	V
$I_D$ $I_{DM}$	Drain Current–Continuous <sup>a</sup> @ $T_A = 25^\circ\text{C}$ -Pulse <sup>b</sup>	-4.2 -16	A
$I_S$	Drain–Source Diode Forward Current <sup>a</sup>	-2.2	A
$P_D$	Power Dissipation	1.25	W
$R_{\theta JA}$	Thermal resistance,Junction-to-Ambient	75	$^\circ\text{C}/\text{W}$
$T_J, T_{stg}$	Junction and Storage Temperature	-55 to +150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS @  $T_a=25^\circ C$  unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
<b>STATIC PARAMETERS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-25	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-body Leakage	$I_{GSS}$	$V_{DS}=0V, V_{GS}=-12V$	-	-	-100	nA
<b>ON CHARACTERISTICS<sup>b</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.7	-1.0	-1.3	V
Static drain-Source on-resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-4.2A$ $V_{GS}=-4.5V, I_D=-4.0A$ $V_{GS}=-2.5V, I_D=-1.0A$	-	37	50	mΩ
			-	36	65	
			-	67	120	
<b>DRAIN-SOURCE DIODE CHARACTERISTICS<sup>b</sup></b>						
Drain-Source diode forward voltage	$V_{SD}$	$V_{GS}=0V, I_D=-1A$	-	-	-1.0	V
<b>DYNAMIC CHARACTERISTICS<sup>c</sup></b>						
Input capacitance	$C_{ISS}$	$V_{DS}=15V, V_{GS}=0V, f=1.0MHz$	-	1325	-	pF
Output capacitance	$C_{OSS}$		-	172	-	
Reverse transfer capacitance	$C_{RSS}$		-	140	-	
<b>SWITCHING CHARACTERISTICS<sup>c</sup></b>						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DS} = -15V, I_D = -1A$ $R_L = 15\Omega, V_{GEN} = -4.5V$ , $R_{GEN} = 10\Omega$	-	5	-	ns
Rise Time	$tr$		-	3	-	ns
Turn-Off Delay Time	$t_{D(OFF)}$		-	30	-	ns
Fall Time	$tf$		-	10	-	ns
Total Gate Charge	$Qg$	$V_{DS} = -15V$ $I_D = -1A$ $V_{GS} = -10V$ ,	-	27.8	-	nC
Gate-Source Charge	$Qgs$		-	3.2	-	nC
Gate-Drain Charge	$Qgd$		-	2.72	-	nC

NOTE:

b. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

C. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTICS @  $T_a=25^\circ\text{C}$  unless otherwise specified

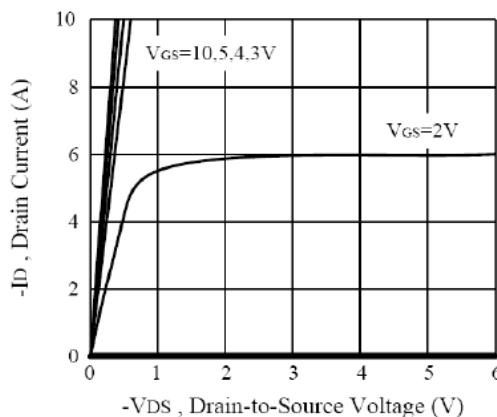


Figure 1. Output Characteristics

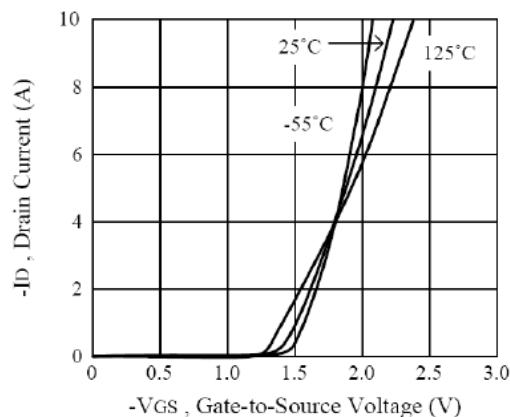


Figure 2. Transfer Characteristics

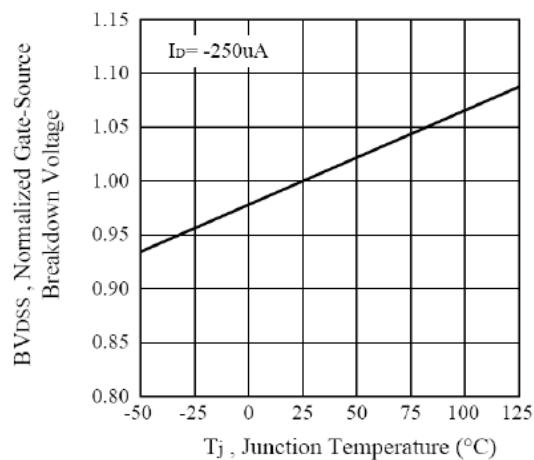


Figure 3. Breakdown Voltage Variation with Temperature

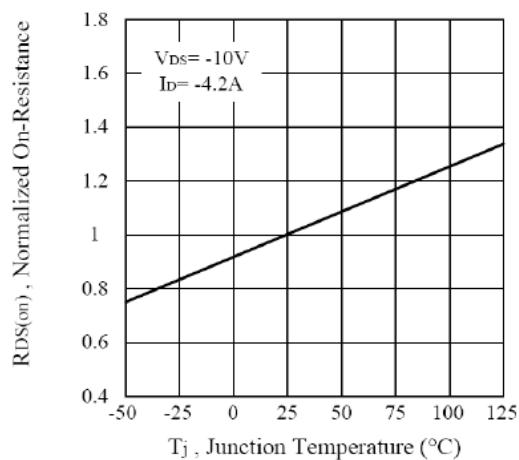


Figure 4. On-Resistance Variation with Temperature

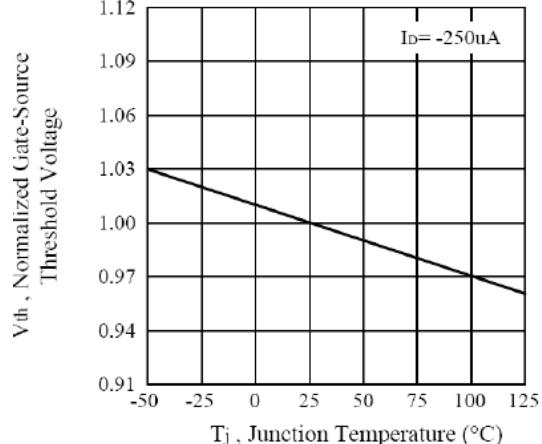


Figure 5. Gate Threshold Variation with Temperature

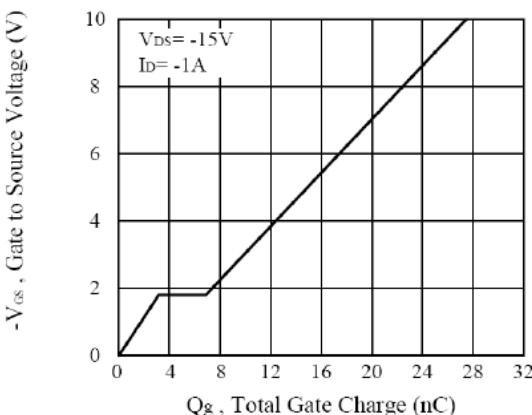
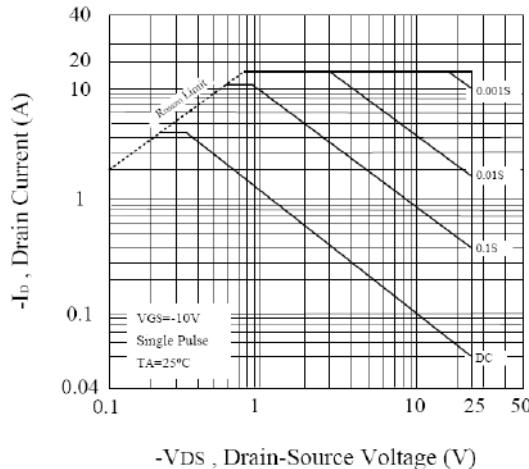


Figure 6. Gate Charge

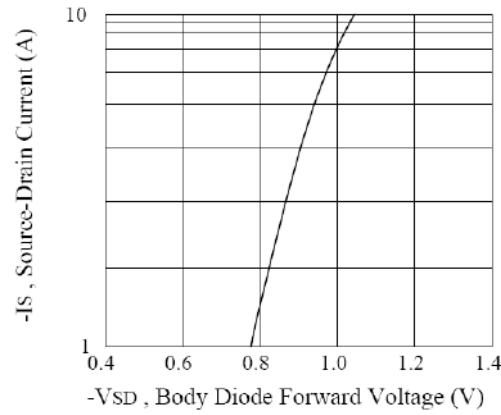
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- $I_D$ , Drain Current (A)

- $V_{DS}$ , Drain-Source Voltage (V)

Figure 9. Maximum Safe Operating Area



- $I_S$ , Source-Drain Current (A)

- $V_{SD}$ , Body Diode Forward Voltage (V)

Figure 8. Body Diode Forward Voltage Variation with Source Current

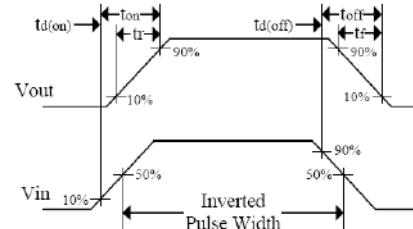
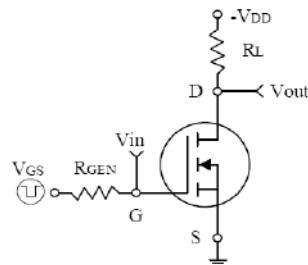


Figure 10. Switching Test Circuit and Switching Waveforms

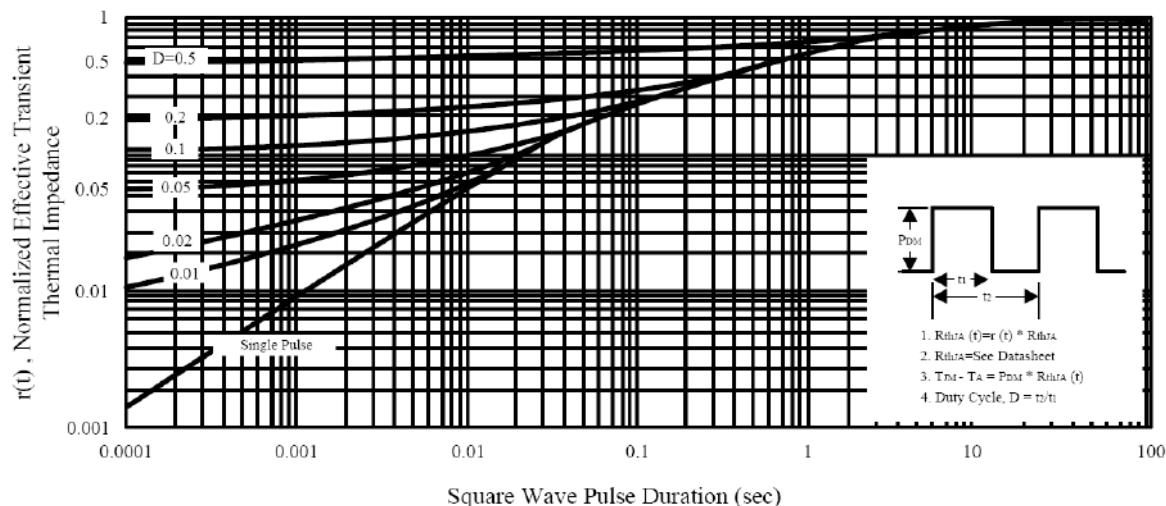


Figure 11. Normalized Thermal Transient Impedance Curve



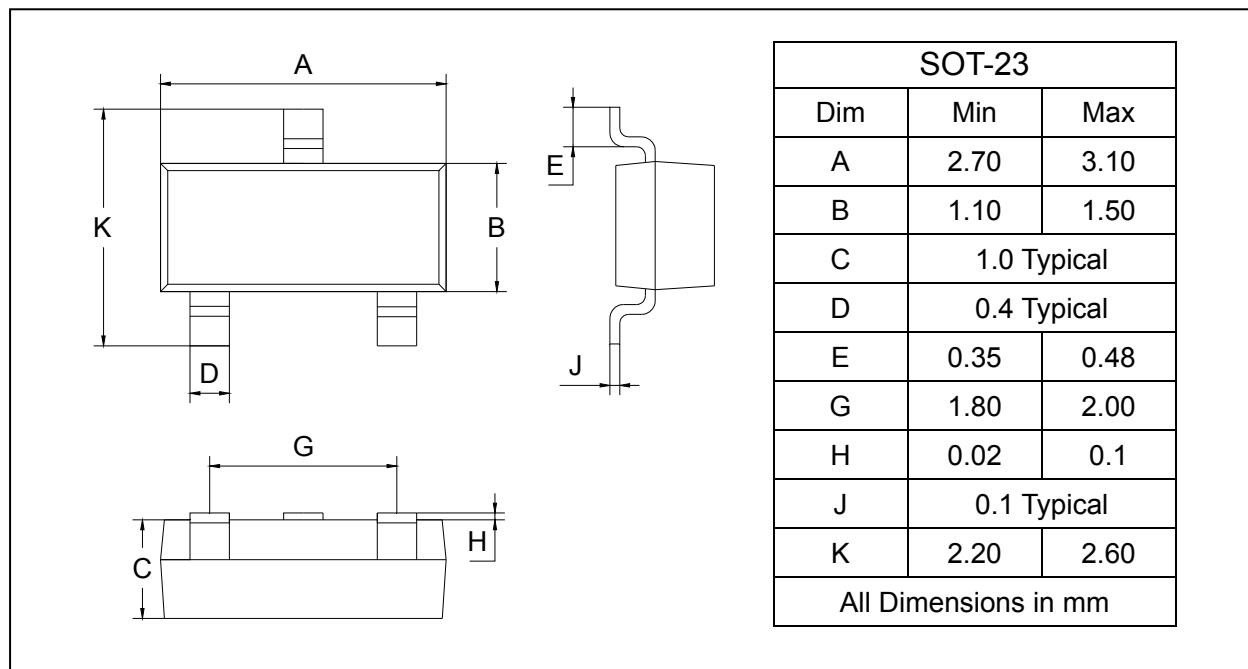
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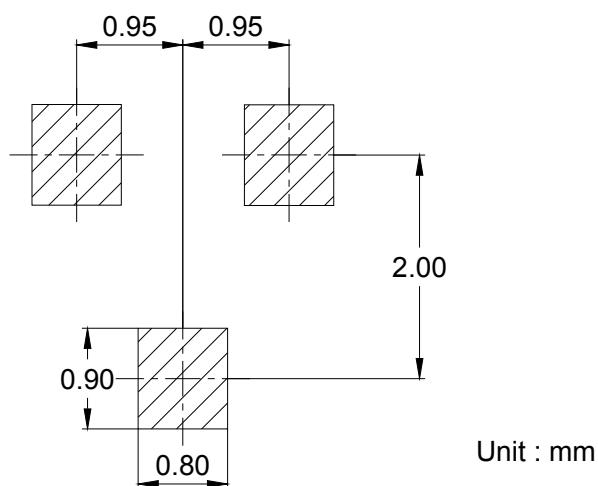
### PACKAGE OUTLINE

Plastic surface mounted package

SOT-23



### SOLDERING FOOTPRINT



### PACKAGE INFORMATION

Device	Package	Shipping
BL3401	SOT-23	3000/Tape&Reel