BLF2425M7L100; BLF2425M7LS100 Power LDMOS transistor

Rev. 2 — 1 September 2015

AMPLEON Product data sheet

Product profile 1.

1.1 General description

100 W LDMOS power transistor for industrial applications at frequencies from 2300 MHz to 2400 MHz.

Table 1. **Typical performance**

Typical RF performance at $T_{case} = 25 \ ^{\circ}C$ in a common source class-AB production test circuit.

Test signal	f	I _{Dq}	V_{DS}	P _{L(AV)}	Gp	η_D	ACPR _{885k}	ACPR _{5M}
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
IS-95	2300 to 2400	900	28	20	18	27	-46 <mark>[1]</mark>	-
1 carrier W-CDMA	2300 to 2400	900	28	30	18.7	33	-	-40 <mark>[2]</mark>

[1] Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

[2] 3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R_{th} providing excellent thermal stability
- Designed for low memory effects providing excellent digital pre-distortion capability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

RF power amplifiers for industrial and multi carrier applications in the 2300 MHz to 2400 MHz frequency range

2. Pinning information

Pin	Description		Simplified outline	Graphic symbol
BLF2425	M7L100 (SOT502A)			
1	drain			
2	gate		$ \int \int \int \partial C_3 $	1 لــــا
3	source	<u>[1]</u>		
				3 sym112
BLF2425	M7LS100 (SOT502B)			
1	drain			
2	gate			1 لــــا
3	source	<u>[1]</u>		
				2 1
				sym112

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information						
Type number	Packag	je				
	Name	Description	Version			
BLF2425M7L100	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT502A			
BLF2425M7LS100	-	earless flanged ceramic package; 2 leads	SOT502B			

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-c)}	thermal resistance from junction to case	T_{case} = 80 °C; P _L = 100 W	0.3	K/W

6. Characteristics

Table 6.DC characteristics

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified.

• •	D			-		
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V_{GS} = 0 V; I _D = 1 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 150 mA	1.5	1.8	2.3	V
I _{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	5	μA
I _{DSX}	drain cut-off current	V _{GS} = V _{GS(th)} + 3.75 V; V _{DS} = 10 V	25.1	29	-	A
I _{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	500	nA
g fs	forward transconductance	V _{DS} = 10 V; I _D = 5.35 A	-	10.5	-	S
R _{DS(on)}	drain-source on-state resistance	V _{GS} = V _{GS(th)} + 3.75 V; I _D = 5.25 A	-	0.1	-	Ω

Table 7. RF characteristics

Test signal: single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz; f_1 = 2300 MHz; f_2 = 2400 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 900 mA; T_{case} = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
G _p	power gain	P _{L(AV)} = 20 W	17.3	18	-	dB
RL _{in}	input return loss	P _{L(AV)} = 20 W	-	-14	-	dB
η_D	drain efficiency	P _{L(AV)} = 20 W	22	27	-	%
ACPR _{885k}	adjacent channel power ratio (885 kHz)	P _{L(AV)} = 20 W	-	-46	-40	dBc

7. Test information

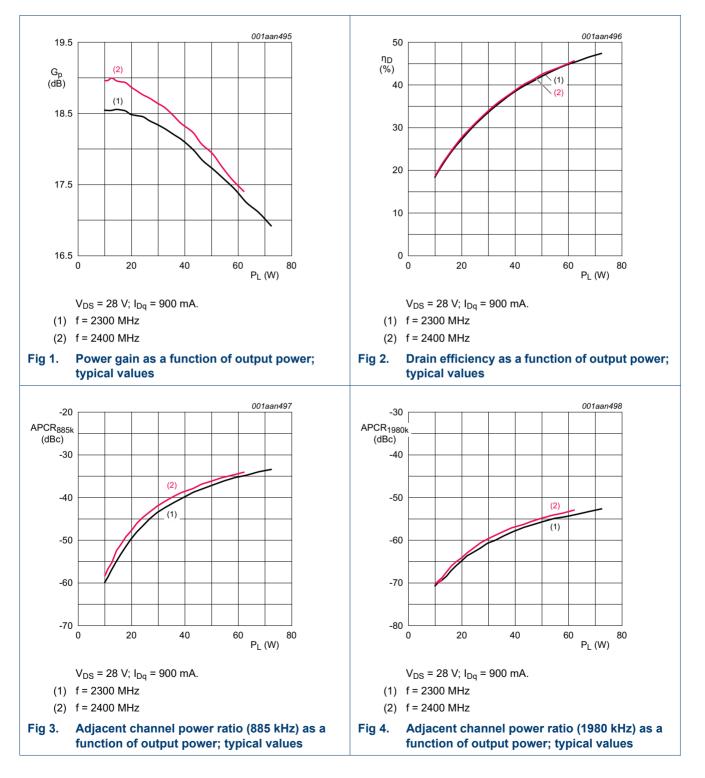
7.1 Ruggedness in class-AB operation

The BLF2425M7L100 and BLF2425M7LS100 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 900 mA; P_L = 100 W (CW); f = 2300 MHz.

7.2 Graphical data

7.2.1 Single carrier IS-95

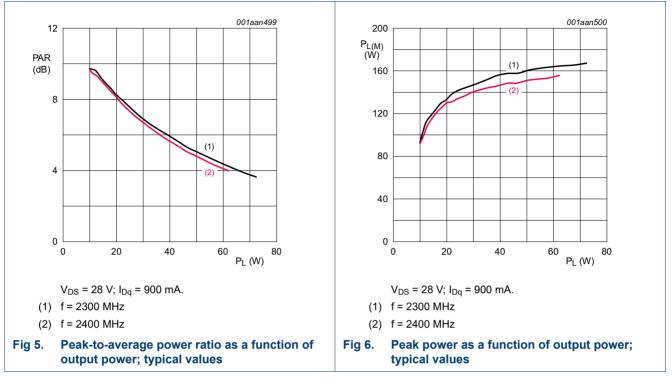
Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.



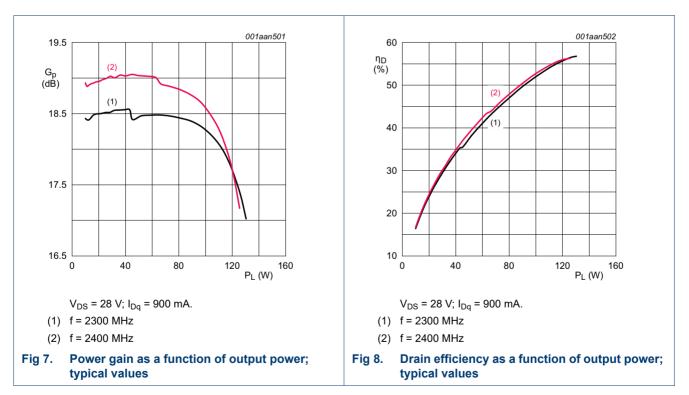
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BLF2425M7L(S)100

Power LDMOS transistor

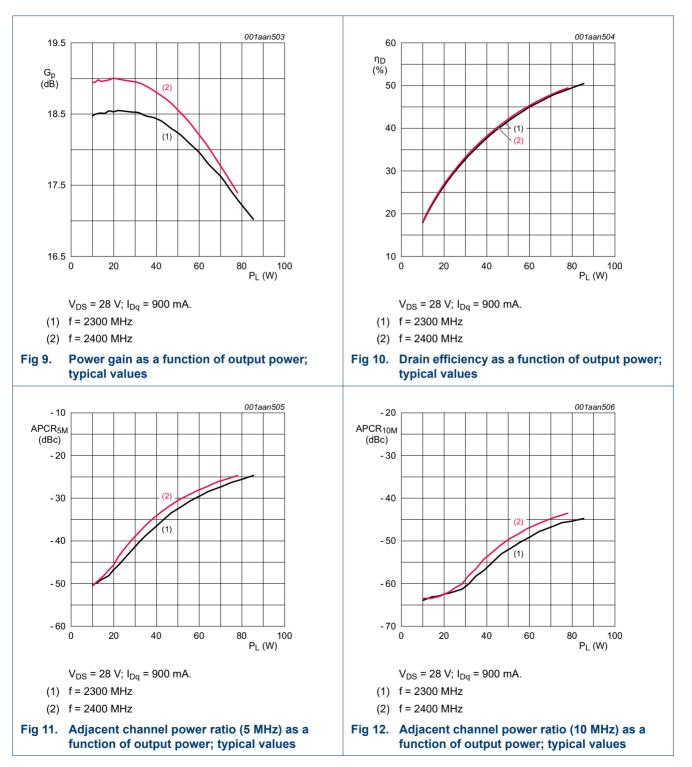


7.2.2 Pulsed CW



7.2.3 Single carrier W-CDMA

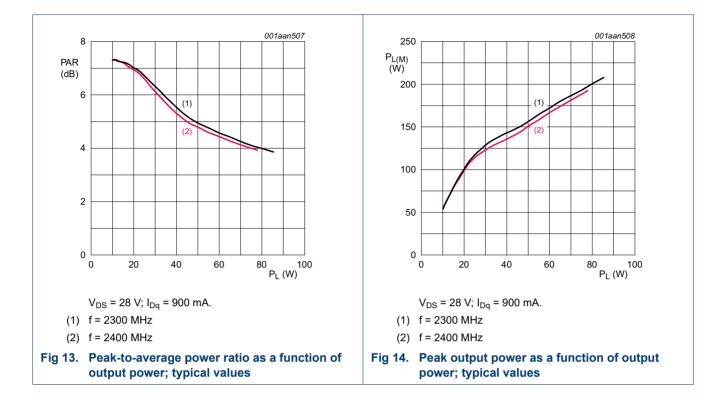
3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.



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BLF2425M7L(S)100

Power LDMOS transistor



BLF2425M7L100_2425M7LS100#2

Product data sheet

BLF2425M7L(S)100

Power LDMOS transistor

8. Package outline

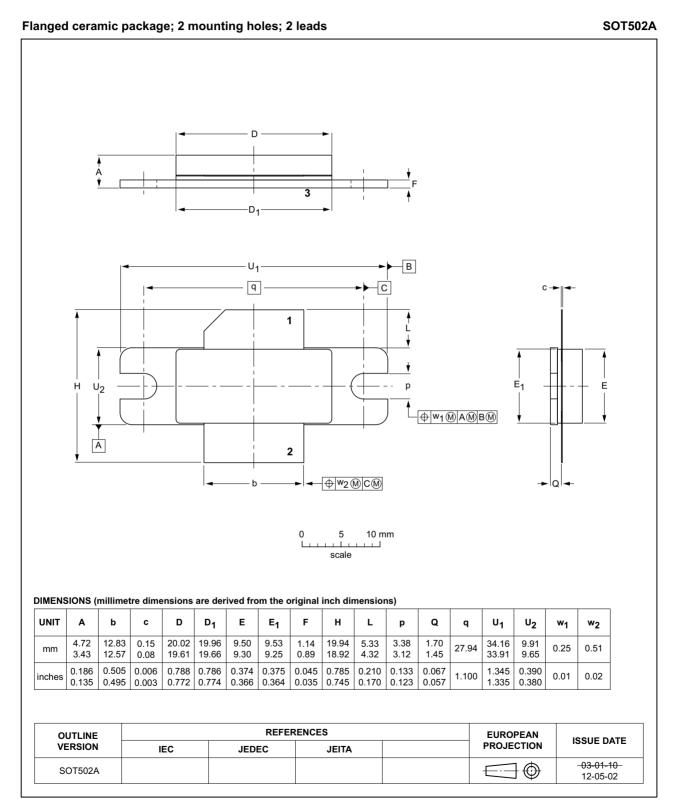


Fig 15. Package outline SOT502A

BLF2425M7L(S)100 Power LDMOS transistor

SOT502B

Earless flanged ceramic package; 2 leads

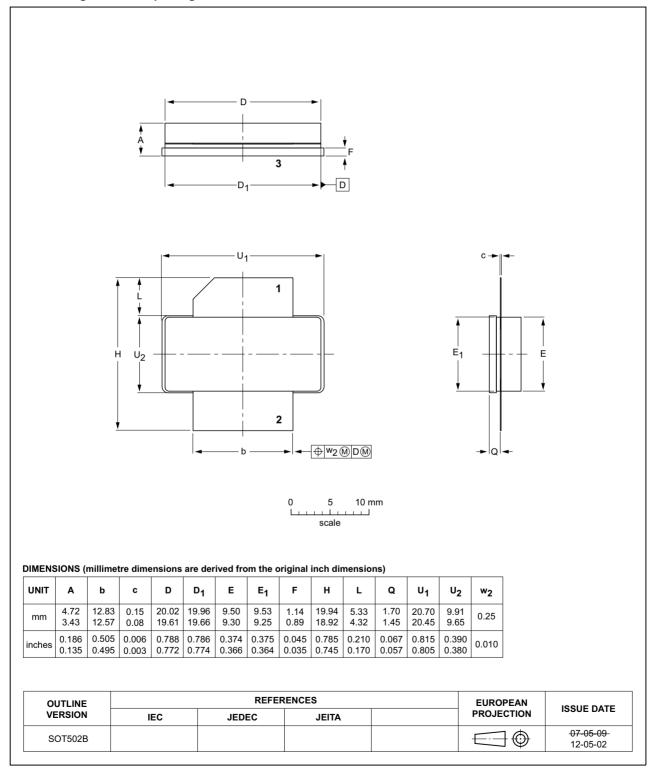


Fig 16. Package outline SOT502B

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Product data sheet

9. Abbreviations

Table 8.	Abbreviations
Acronym	Description
3GPP	3rd Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
ESD	ElectroStatic Discharge
IS-95	Interim Standard 95
LDMOS	Laterally Diffused Metal Oxide Semiconductor
PAR	Peak-to-Average Ratio
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

10. Revision history

Table 9.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BLF2425M7L100_2425M7LS100#2	20150901	Product data sheet	-	BLF2425M7L100_2425M7LS1 00#1		
Modifications:	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.					
	 Legal texts have been adapted to the new company name where appropriate. 					
BLF2425M7L100_2425M7LS100#1	20131206	Product data sheet	-	-		

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11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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