# BLF7G20L-200; BLF7G20LS-200 Power LDMOS transistor Rev. 5 — 1 September 2015

**AMMPLEON** 

Product data sheet

# **Product profile**

## 1.1 General description

200 W LDMOS power transistor for base station applications at frequencies from 1805 MHz to 1990 MHz.

**Typical performance** Table 1.

Typical RF performance at  $T_{case} = 25$  °C in a common source class-AB production test circuit.

Mode of operation	f	$I_{Dq}$	V <sub>DS</sub>	$P_{L(AV)}$	Gp	$\eta_D$	ACPR
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)
2-carrier W-CDMA	1805 to 1880	1620	28	55	18	33	-29 <u>[1]</u>

<sup>[1]</sup> Test signal: 3GPP; test model 1; 64 PDPCH; PAR = 8.4 dB at 0.01 % probability on CCDF.

#### 1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R<sub>th</sub> providing excellent thermal stability
- Designed for broadband operation (1805 MHz to 1990 MHz)
- Lower output capacitance for improved performance in Doherty applications
- Designed for low-memory effects providing excellent digital pre-distortion capability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Restriction of Hazardous Substances (RoHS) Directive 2002/95/EC

### 1.3 Applications

 RF power amplifiers for W-CDMA base stations and multi-carrier applications in the 1805 MHz to 1990 MHz frequency range

# 2. Pinning information

Table 2. Pinning

Di-	Pililing	Oi	0
Pin	Description	Simplified outline	Graphic symbol
BLF7G20	DL-200 (SOT502A)		
1	drain		
2	gate	5 1 3	ئے.
3	source		2
			3
			sym112
BLF7G20	)LS-200 (SOT502B)		
1	drain		
2	gate		ائے
3	source	[1]	2
			3
			sym112

<sup>[1]</sup> Connected to flange

# 3. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
BLF7G20L-200	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A				
BLF7G20LS-200	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B				

# 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
$V_{GS}$	gate-source voltage		-0.5	+13	V
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	225	°C

# 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{\text{th(j-c)}}$	thermal resistance from junction to case	$T_{case}$ = 80 °C; $P_{L}$ = 55 W; $V_{DS}$ = 28 V; $I_{Dq}$ = 1620 mA	0.27	K/W

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## 6. Characteristics

Table 6. Characteristics

 $T_i = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 1.5 \text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS}$ = 10 V; $I_{D}$ = 150 mA	1.5	1.9	2.3	V
I <sub>DSS</sub>	drain leakage current	$V_{GS}$ = 0 V; $V_{DS}$ = 28 V	-	-	4.2	μА
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	42	50.6	-	Α
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 11 V; $V_{DS}$ = 0 V	-420	2.44	420	nA
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_{D}$ = 7.5 A	-	18.6	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 5.25 \text{ A}$	-	0.093	-	Ω

# 7. Test information

#### Table 7. Functional test information

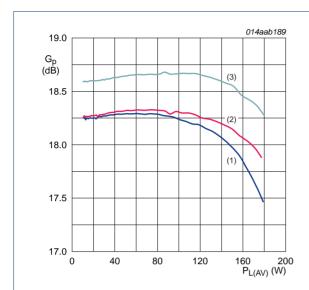
Mode of operation: 2-carrier W-CDMA; PAR = 8.4 dB at 0.01 % probability on the CCDF; 3GPP test model 1; 64 PDPCH;  $f_1$  = 1807.5 MHz;  $f_2$  = 1812.5 MHz;  $f_3$  = 1872.5 MHz;  $f_4$  = 1877.5 MHz; RF performance at  $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA;  $T_{case}$  = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$P_{L(AV)}$	average output power		-	55	-	W
$G_p$	power gain	$P_{L(AV)} = 55 W$	17	18	-	dB
RLin	input return loss	$P_{L(AV)} = 55 W$	-	-	-10	dB
$\eta_{D}$	drain efficiency	$P_{L(AV)} = 55 W$	30	33	-	%
ACPR	adjacent channel power ratio	$P_{L(AV)} = 55 W$	-	-29	-	dBc

### 7.1 Ruggedness in class-AB operation

The BLF7G20L-200 and BLF7G20LS-200 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 30 V;  $I_{Dq}$  = 1620 mA;  $P_L$  = 185 W (CW); f = 1805 MHz to 1880 MHz.

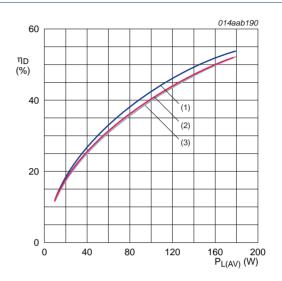
### **7.2 1 Tone CW**



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA.

- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

Fig 1. Power gain as a function of average output power; typical values

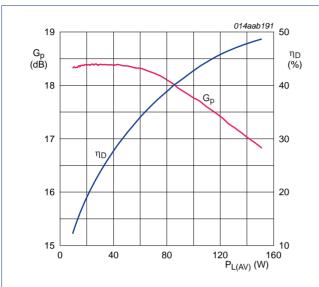


 $V_{DS} = 28 \text{ V}; I_{Dq} = 1620 \text{ mA}.$ 

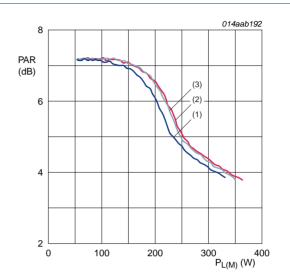
- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

Fig 2. Drain efficiency as a function of average output power; typical values

#### 7.3 1-carrier W-CDMA



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA; f = 1845 MHz; PAR = 7.2 dB at 0.01 % probability on the CCDF.

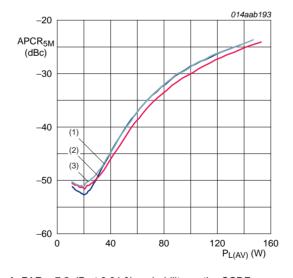


 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA; PAR = 7.2 dB at 0.01 % probability on the CCDF.

- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

Fig 3. Power gain and drain efficiency as functions of average output power; typical values



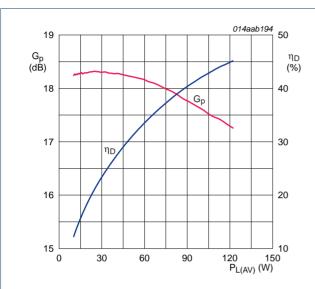


 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA; PAR = 7.2 dB at 0.01 % probability on the CCDF.

- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

Fig 5. Adjacent power channel ratio (5 MHz) as function of average output power; typical values

#### 7.4 2-carrier W-CDMA



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA; channel spacing = 5 MHz; PAR = 8.4 dB at 0.01 % probability on the CCDF.

014aah195 19.0 G<sub>p</sub> (dB) 18.5 (3) 18.0 (2) 17.5 (1) 17.0 30 60 90 120 150 P<sub>L(AV)</sub> (W)

 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA; channel spacing = 5 MHz; PAR = 8.4 dB at 0.01 % probability on the CCDF.

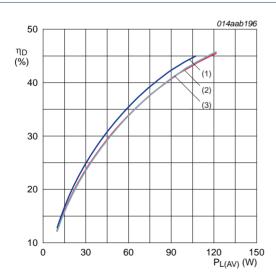
Power gain as a function of average output

- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

power; typical values

Fig 7.

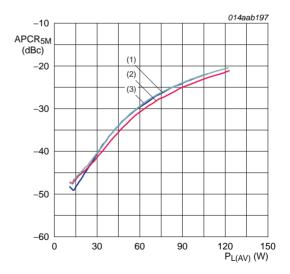
Fig 6. Power gain and drain efficiency as functions of average output power; typical values



 $V_{DS}=28~V;\,I_{Dq}=1620~mA;$  channel spacing = 5 MHz; PAR = 8.4 dB at 0.01 % probability on the CCDF.

- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

Fig 8. Drain efficiency as a function of average output power; typical values



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 1620 mA; channel spacing = 5 MHz; PAR = 8.4 dB at 0.01 % probability on the CCDF.

- (1) f = 1805 MHz
- (2) f = 1845 MHz
- (3) f = 1880 MHz

Fig 9. Adjacent power channel ratio (5 MHz) as a function of average output power; typical values

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### 7.5 Test circuit

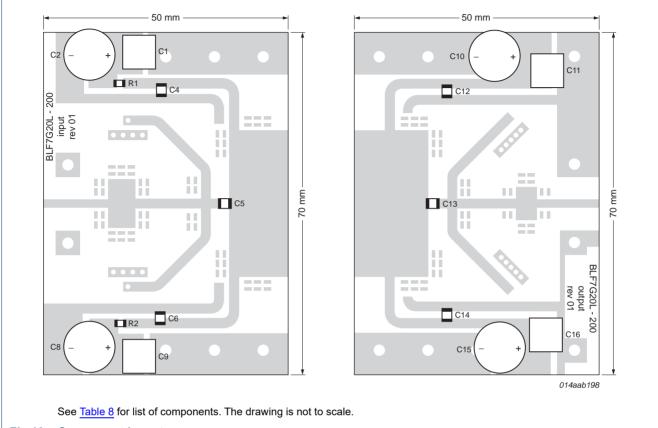


Fig 10. Component layout

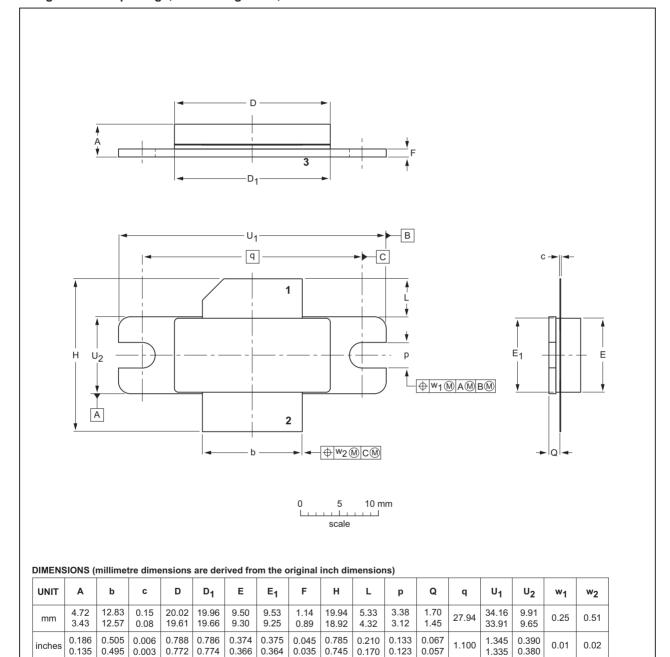
**Table 8.** List of components
See Figure 10 for component layout.

Component	Description	Value	Remarks
C1, C9, C11, C16	multilayer ceramic chip capacitor	10 μF	TDK
C4, C6	multilayer ceramic chip capacitor	68 pF	ATC800B
C5	multilayer ceramic chip capacitor	2.0 pF	ATC800B
C12, C14	multilayer ceramic chip capacitor	100 pF	ATC800B
C13	multilayer ceramic chip capacitor	3.3 pF	ATC800B
C2, C8, C10, C15	electrolytic capacitor	470 μF; 63 V	
R1, R2	chip resistor	10 Ω	Philips 0603

# 8. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT502A

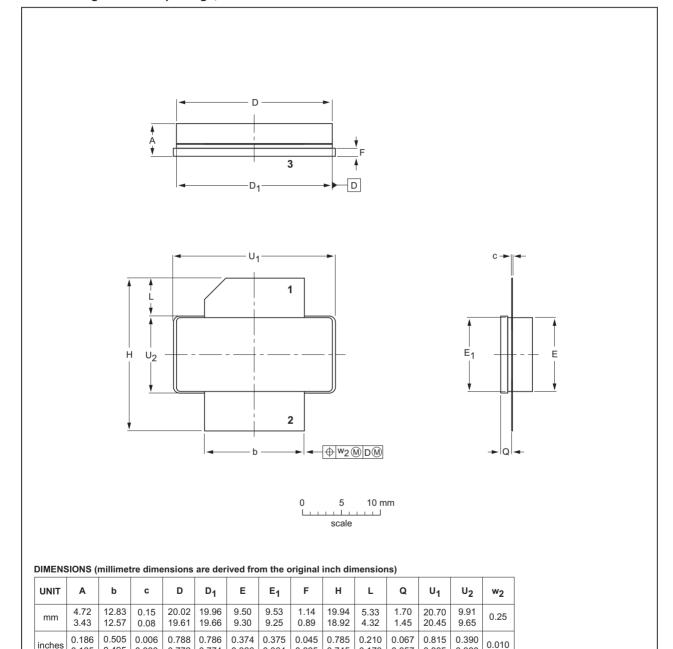


OUTLINE	REFERENCES			EUROPEAN LOGUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT502A						<del>-03-01-10 -</del> 12-05-02	

Fig 11. Package outline SOT502A

#### Earless flanged ceramic package; 2 leads

SOT502B



	OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
	VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
I	SOT502B					<del>07-05-09</del> 12-05-02	

0.170

0.057

0.805

0.380

0.035 0.745

Fig 12. Package outline SOT502B

0.135

0.495

0.003

0.772 | 0.774

0.366 0.364

# 9. Abbreviations

Table 9. Abbreviations

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
PAR	Peak-to-Average power Ratio
PDPCH	transmission Power of the Dedicated Physical CHannel
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

# 10. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BLF7G20L-200_7G20LS-200#5	20150901	Product data sheet	-	BLF7G20L-200_7G20LS-200 v.4		
Modifications:	The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.					
	<ul> <li>Legal tex</li> </ul>	ts have been adapted to	the new compa	ny name where appropriate.		
BLF7G20L-200_7G20LS-200 v.4	20110722	Product data sheet	-	BLF7G20L-200_7G20LS-200 v.3		
BLF7G20L-200_7G20LS-200 v.3	20110301	Preliminary data sheet	-	BLF7G20L-200_7G20LS-200 v.2		
BLF7G20L-200_7G20LS-200 v.2	20100827	Preliminary data sheet	-	BLF7G20L-200_7G20LS-200 v.1		
BLF7G20L-200_7G20LS-200 v.1	20100603	Objective data sheet	-	-		

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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# BLF7G20L-200; BLF7G20LS-200

**Power LDMOS transistor** 

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# BLF7G20L-200; BLF7G20LS-200

# **AMPLEON**

**Power LDMOS transistor** 

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