BLF8G10LS-300P

Power LDMOS transistor

AMPLEON

Rev. 3 — 1 September 2015

Product data sheet

1. Product profile

1.1 General description

300 W LDMOS power transistor for base station applications at frequencies from 700 MHz to 1000 MHz.

Table 1. Typical performance

Typical RF performance at $T_{case} = 25$ °C in a common source class-AB production test circuit.

Test signal	f	V _{DS}	P _{L(AV)}	Gp	η _D	ACPR
	(MHz)	(V)	(W)	(dB)	(%)	(dBc)
2-carrier W-CDMA	758 to 803	28	65	20.5	32	-35 <u>[1]</u>

^[1] Test signal: 3GPP test model 1; 1 to 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF per carrier; carrier spacing 5 MHz.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low thermal resistance providing excellent thermal stability
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Restriction of Hazardous Substances (RoHS) Directive 2002/95/EC

1.3 Applications

RF power amplifier for multi standards and multi carrier applications in the 700 MHz to 1000 MHz frequency range

2. Pinning information

Table 2. Pinning

	9		
Pin	Description	Simplified outlin	e Graphic symbol
1	drain1		,
2	drain2	1 2	1
3	gate1		3
4	gate2	3 4	5
5	source	[1]	4
			' <u></u>
			2 sym117

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Packag	Package			
	Name	Description	Version		
BLF8G10LS-300P	-	earless flanged balanced ceramic package; 4 leads	SOT539B		

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		<u>[1]</u> _	225	°C

^[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{\text{th(j-case)}}$	thermal resistance from junction to case	T_{case} = 80 °C; P_L = 65 W	0.29	K/W

6. Characteristics

Table 6. DC characteristics

 $T_i = 25$ °C; values per section unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 2.2 \text{ mA}$	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V_{DS} = 20 V; I_{D} = 220 mA	1.5	1.9	2.3	V
V_{GSq}	gate-source quiescent voltage	V _{DS} = 28 V; I _D = 1000 mA	-	2.0	-	V
I_{DSS}	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$	-	-	2.4	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 20 \text{ V}$	-	38.1	-	Α
I _{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	240	nΑ
9 _{fs}	forward transconductance	V_{DS} = 20 V; I_{D} = 11 A	-	15.0	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 7.7 A$	-	0.086	-	Ω

Table 7. RF characteristics

Test signal: 2-carrier W-CDMA; PAR = 7.2 dB at 0.01 % probability on CCDF; 3GPP test model 1; 1 to 64 DPCH; f_1 = 760.5 MHz; f_2 = 765.5 MHz; f_3 = 795.5 MHz; f_4 = 800.5 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 2000 mA; T_{case} = 25 °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	$P_{L(AV)} = 65 \text{ W}$	19.5	20.5	-	dB
RLin	input return loss	P _{L(AV)} = 65 W	-	-12	-8	dB
η_{D}	drain efficiency	P _{L(AV)} = 65 W	28	32	-	%
ACPR	adjacent channel power ratio	P _{L(AV)} = 65 W	-	-35	-32	dBc

7. Test information

7.1 Ruggedness in class-AB operation

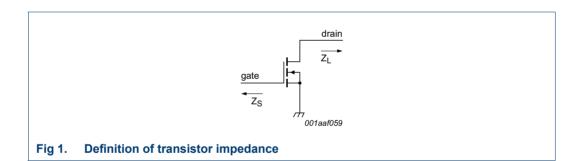
The BLF8G10LS-300P is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 2000 mA; P_{L} = 65 W (2-carrier W-CDMA); f = 758 MHz.

7.2 Impedance information

Table 8. Typical impedance Measured load-pull data per section; $I_{Dq} = 1000 \text{ mA}$; $V_{DS} = 28 \text{ V}$

f	Z _S [1]	Z _L [1]	P _{L(3dB)}
(MHz)	(Ω)	(Ω)	(W)
720	2.3 – j2.8	1.6 – j2.7	204.4
746	2.5 – j3.2	1.7 – j2.6	220.0
757	2.3 – j3.6	1.6 – j2.5	225.2
769	2.6 – j3.6	1.7 – j2.4	227.9
791	2.6 – j3.9	1.5 – j2.8	214.8
805	2.6 – j3.9	1.8 – j2.3	207.2
820	2.7 – j4.2	1.6 – j2.1	228.5
869	2.8 – j4.1	1.2 – j2.1	217.2
881	2.9 – j4.4	1.2 – j2.1	219.9
894	3.3 – j4.7	1.1 – j2.1	215.4
925	3.6 – j5.2	1.2 – j2.1	223.5
942	4.1– j 5.7	1.1 – j2.2	220.5
960	4.7 – j5.9	1.1 – j2.2	218.8

[1] Z_S and Z_L defined in Figure 1.



7.3 Test circuit

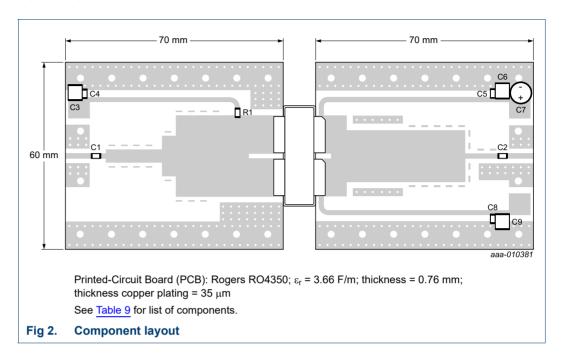


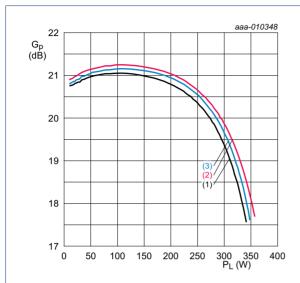
Table 9. List of components See Figure 2 for component layout.

Component **Description** Value Remarks C1, C2 multilayer ceramic chip capacitor 82 pF ATC 800B C3, C6, C9 multilayer ceramic chip capacitor $10 \mu F$, 50 VMurata C4, C5, C8 multilayer ceramic chip capacitor 82 pF ATC 100B C7 electrolytic capacitor 470 μF, 63 V R1 chip resistor 4.7 Ω SMD 1206

7.4 Graphical data

Following are typical RF measurements of the BLF8G10LS-300P in its class-AB test circuit.

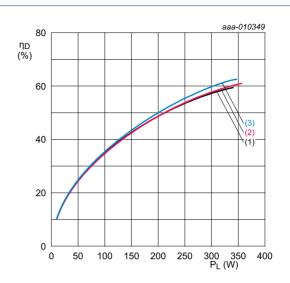
7.4.1 CW pulsed



 V_{DS} = 28 V; I_{Dq} = 2000 mA; t_p = 100 μ s: δ = 10 %.

- (1) f = 758 MHz
- (2) f = 780.5 MHz
- (3) f = 803 MHz

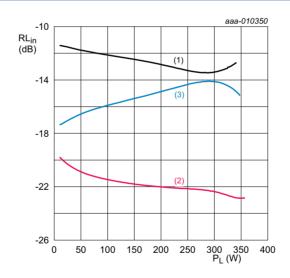
Fig 3. Power gain as a function of output power; typical values



 V_{DS} = 28 V; I_{Dq} = 2000 mA; t_p = 100 μ s: δ = 10 %.

- (1) f = 758 MHz
- (2) f = 780.5 MHz
- (3) f = 803 MHz

Fig 4. Drain efficiency as a function of output power; typical values

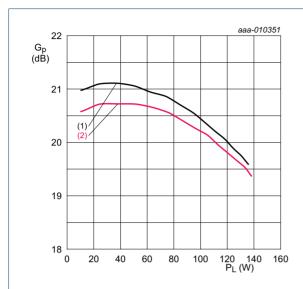


 V_{DS} = 28 V; I_{Dq} = 2000 mA; t_p = 100 μ s: δ = 10 %.

- (1) f = 758 MHz
- (2) f = 780.5 MHz
- (3) f = 803 MHz

Fig 5. Input return loss as a function of output power; typical values

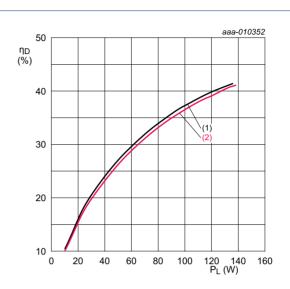
7.4.2 1-Carrier W-CDMA



 $V_{DS} = 28 \text{ V}; I_{Dq} = 2000 \text{ mA}.$

- (1) f = 758 MHz
- (2) f = 803 MHz

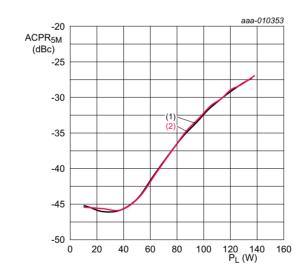
Fig 6. Power gain as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 2000 \text{ mA}.$

- (1) f = 758 MHz
- (2) f = 803 MHz

Fig 7. Drain efficiency as a function of output power; typical values

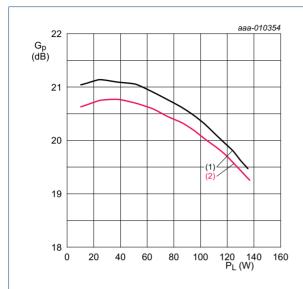


 $V_{DS} = 28 \text{ V}; I_{Dq} = 2000 \text{ mA}.$

- (1) f = 758 MHz
- (2) f = 803 MHz

Fig 8. Adjacent channel power ratio (5 MHz) as a function of output power; typical values

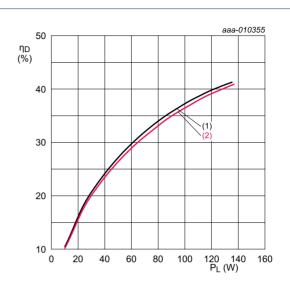
7.4.3 2-Carrier W-CDMA



 $V_{DS} = 28 \text{ V}; I_{Dq} = 2000 \text{ mA}.$

- (1) f = 758 MHz
- (2) f = 803 MHz

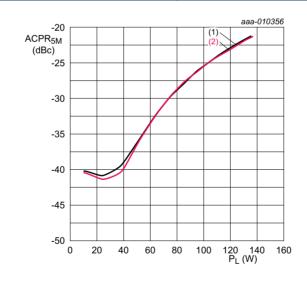
Fig 9. Power gain as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 2000 \text{ mA}.$

- (1) f = 758 MHz
- (2) f = 803 MHz

Fig 10. Drain efficiency as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 2000 \text{ mA}.$

- (1) f = 758 MHz
- (2) f = 803 MHz

Fig 11. Adjacent channel power ratio (5 MHz) as a function of output power; typical values

8. Package outline

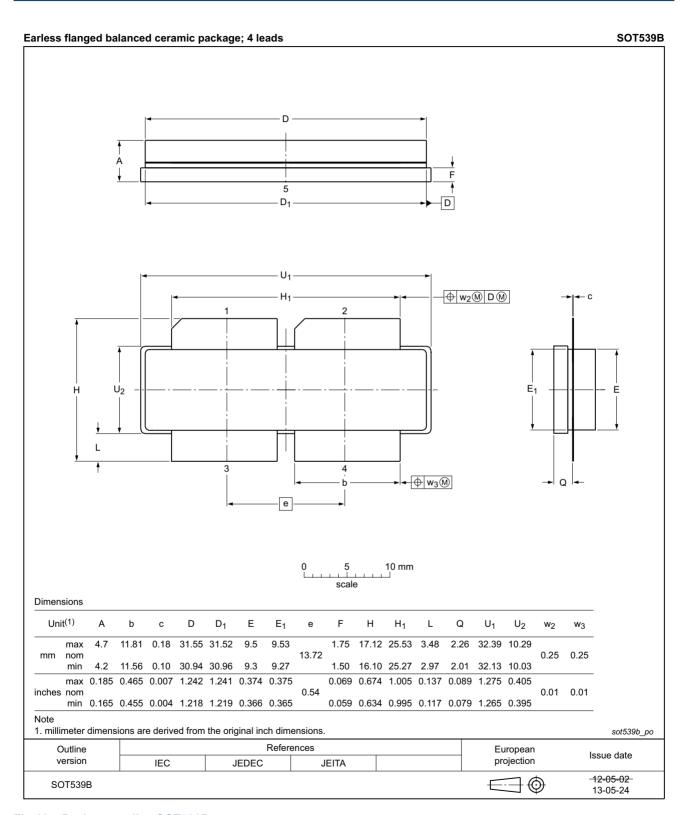


Fig 12. Package outline SOT539B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Table 10. Abbreviations

Acronym	Description
3GPP	3rd Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
MTF	Median Time to Failure
PAR	Peak-to-Average Ratio
SMD	Surface Mounted Device
VSWR	Voltage Standing-Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF8G10LS-300P#3	20150901	Product data sheet	-	BLF8G10LS-300P v.2
Modifications:	 The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. 			with the new identity
	 Legal texts h 	nave been adapted to the n	ew company name w	here appropriate.
BLF8G10LS-300P v.2	20131217	Product data sheet	-	BLF8G10LS-300P v.1
BLF8G10LS-300P v.1	20131118	Objective data sheet	-	-

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12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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14. Contents

1	Product profile 1
1.1	General description 1
1.2	Features and benefits
1.3	Applications
2	Pinning information 2
3	Ordering information 2
4	Limiting values 2
5	Thermal characteristics 2
6	Characteristics 3
7	Test information
7.1	Ruggedness in class-AB operation 3
7.2	Impedance information 4
7.3	Test circuit
7.4	Graphical data 5
7.4.1	CW pulsed 6
7.4.2	1-Carrier W-CDMA
7.4.3	2-Carrier W-CDMA 8
8	Package outline 9
9	Handling information 10
10	Abbreviations
11	Revision history
12	Legal information
12.1	Data sheet status
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks 12
13	Contact information 12
4.4	Contents 12

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