BLF8G22LS-205V

Power LDMOS transistor

Rev. 2 — 1 September 2015

1. Product profile

1.1 General description

205 W LDMOS power transistor with improved video bandwidth for base station applications at frequencies from 2100 MHz to 2200 MHz.

Table 1. Typical performance

Typical RF performance at T_{case} = 25 °C in a common source class-AB production test circuit.

Test signal	f	I _{Dq}	V _{DS}	P _{L(AV)}	Gp	η _D	ACPR _{5M}
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)
1-carrier W-CDMA	2110 to 2170	1500	28	50	18.3	32.5	-32 [1]

[1] Test signal: 3GPP test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low thermal resistance providing excellent thermal stability
- Designed for broadband operation
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

 RF power amplifiers for base stations and multi carrier applications in the 2100 MHz to 2200 MHz frequency range

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	drain		
2	gate		6 7 → 1 → 4,5
3	source		
4	decoupling lead		2
5	decoupling lead	2	aaa-003619
6	n.c.	6 7	
7	n.c.		

[1] Connected to flange.

3. Ordering information

Table 3.Ordering information

Type number	Packag	Package		
	Name	Description	Version	
BLF8G22LS-205V	-	earless flanged LDMOST ceramic package; 6 leads	SOT1239B	

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature	[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator.

5. Thermal characteristics

Table 5.Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-c)}	thermal resistance from junction to case	T _{case} = 80 °C; P _L = 56 W; V _{DS} = 28 V; I _{Dq} = 1200 mA	0.26	K/W

6. Characteristics

Table 6.DC characteristics

 T_i = 25 °C, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V _{GS} = 0 V; I _D = 3.3 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 330 mA	1.5	1.9	2.3	V
V _{GSq}	gate-source quiescent voltage	V _{DS} = 28 V; I _D = 1650 mA	1.7	2.1	2.5	V
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 28 V	-	-	3.6	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	60	-	A
I _{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	360	nA
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 330 mA	-	2.9	-	S
R _{DS(on)}	drain-source on-state resistance	V _{GS} = V _{GS(th)} + 3.75 V; I _D = 11.6 A	-	0.04	-	Ω

Table 7. RF characteristics

Test signal: 1-carrier W-CDMA; PAR = 7.2 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 DPCH; f_1 = 2110 MHz; f_2 = 2170 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 1200 mA; T_{case} = 25 °C; unless otherwise specified; in a production circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _p	power gain	P _{L(AV)} = 50.1 W	17.1	18.3	-	dB
η _D	drain efficiency	P _{L(AV)} = 50.1 W	27.5	32.5	-	%
RL _{in}	input return loss	P _{L(AV)} = 50.1 W	-	-10	-6	dB
ACPR	adjacent channel power ratio	P _{L(AV)} = 50.1 W	-	-30	-25	dBc

7. Test information

7.1 Ruggedness in Doherty operation

The BLF8G22LS-205V is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dg} = 1200 mA; P_L = 140 W (W-CDMA); f = 2110 MHz.

7.2 Impedance information

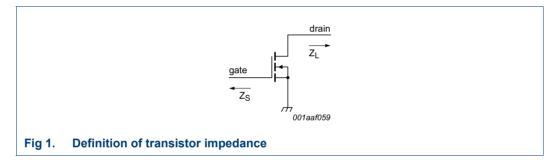
Table 8. Typical impedance

Measured load-pull data; I_{Dq} = 1800 mA; V_{DS} = 28 V; typical values unless otherwise specified.

f	Z _S [1]	Z _L [1]	P _L [2]	η _D [2]	G _p ^[2]
(MHz)	(Ω)	(Ω)	(W)	(%)	(dB)
Maximum po	wer load				·
2110	1.80 – j4.05	1.2 – j2.75	56.00	56.61	15.57
2140	2.24 - j5.00	1.2 – j2.75	55.95	55.85	15.71
2170	2.90 - j4.50	1.2 – j2.75	55.88	56.05	16.03
Maximum dra	ain efficiency load	·			
2110	1.80 – j4.05	1.60 – j1.34	54.08	65.84	18.12
2140	2.24 – j5.00	1.52 – j1.57	54.38	64.88	18.06
2170	2.90 – j4.50	1.41 – j1.77	54.58	64.24	18.08

[1] Z_S and Z_L defined in Figure 1.

[2] at 3 dB gain compression



7.3 VBW in a class-AB operation

The BLF8G22LS-205V shows 110 MHz (typical) video bandwidth in class-AB test circuit in 2140 MHz at V_{DS} = 28 V; I_{Dq} = 1500 mA.

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7.4 Test circuit

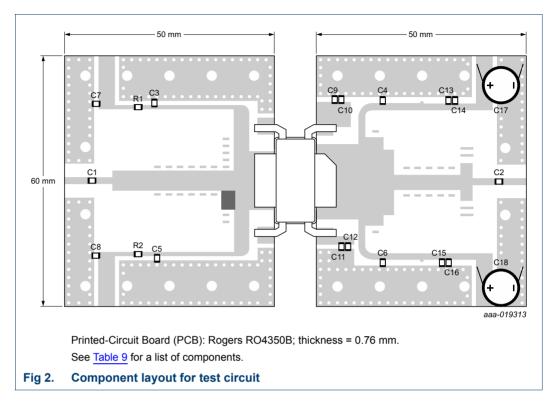


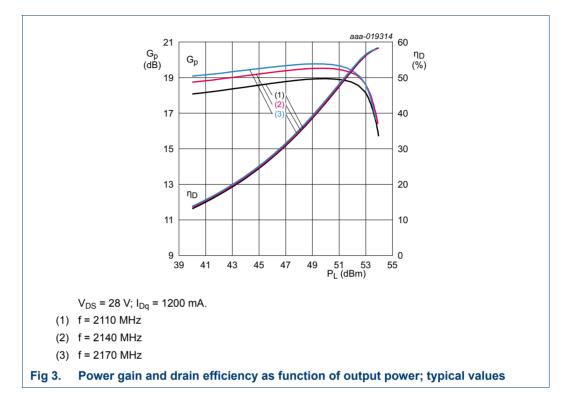
Table 9. List of components

See <u>Figure 2</u> for component layout.

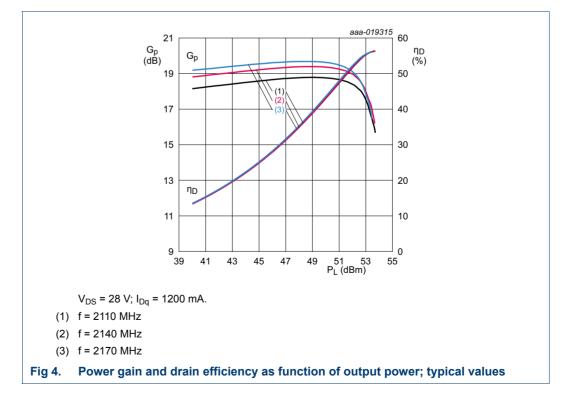
Component	Description	Value	Remarks
C1, C2, C3, C4, C5, C6	multilayer ceramic chip capacitor	20 pF	
C7, C8, C9, C10, C11, C12, C14, C16	multilayer ceramic chip capacitor	10 μF, 50 V	
C13, C15	multilayer ceramic chip capacitor	1 μF, 50 v	
C17, C18	electrolytic capacitor	2200 μF, 63 V	
R1, R2	chip resistor	5.1 Ω	

7.5 Graphical data

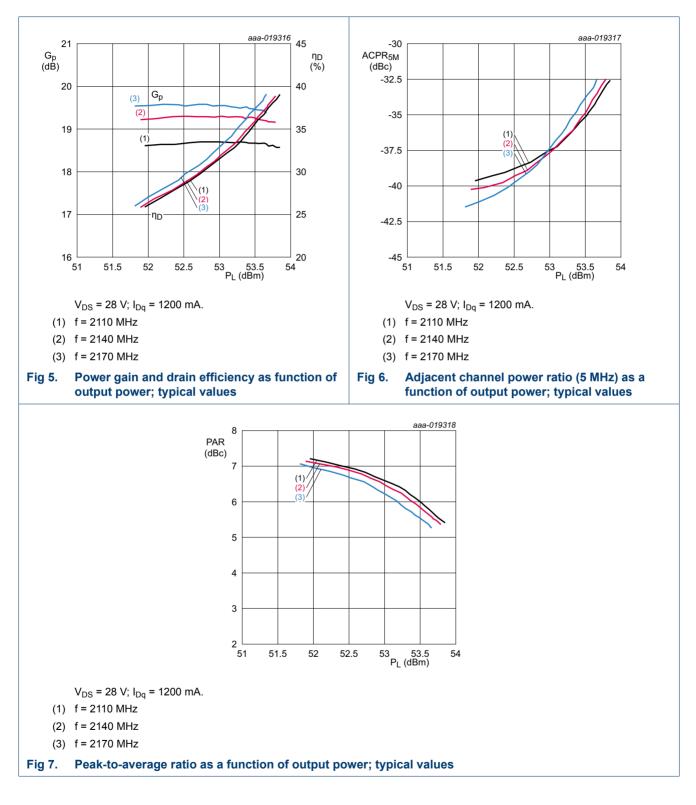
7.5.1 Pulsed CW







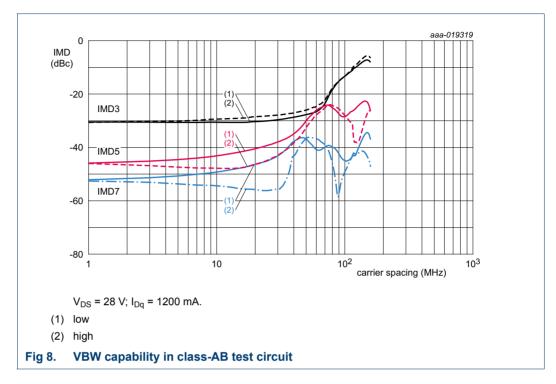
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7.5.3 1-Carrier W-CDMA

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7.5.4 2-Tone VBW



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8. Package outline

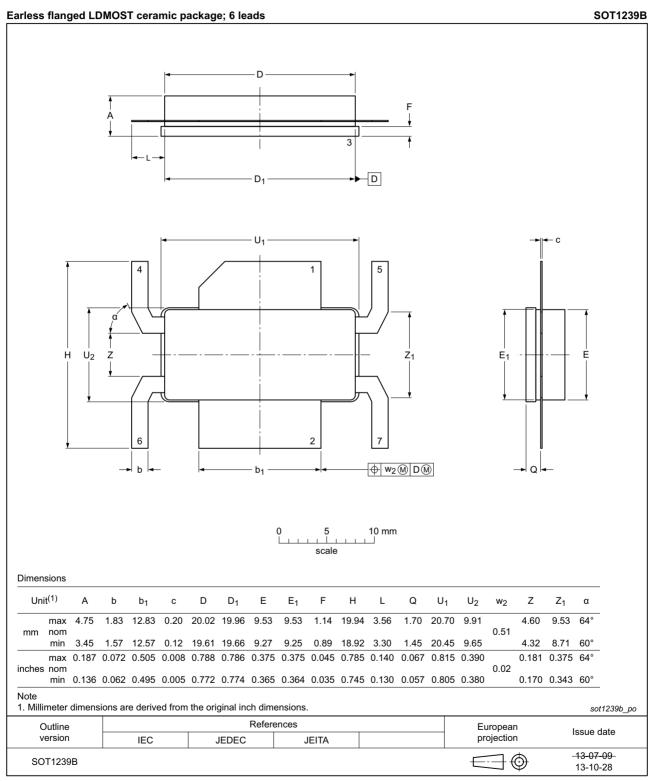


Fig 9. Package outline SOT1239B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Table 10. Abbre	able 10. Abbreviations		
Acronym	Description		
3GPP	3rd Partnership Project		
CW	Continuous Wave		
CCDF	Complementary Cumulative Distribution Function		
DPCH Dedicated Physical CHannel			
ESD	ElectroStatic Discharge		
LDMOS Laterally Diffused Metal-Oxide Semiconductor			
LDMOST	Laterally Diffused Metal-Oxide Semiconductor Transistor		
MTF	Median Time to Failure		
PAR	Peak-to-Average Ratio		
VBW	Video BandWidth		
VSWR	Voltage Standing Wave Ratio		
W-CDMA	Wideband Code Division Multiple Access		

11. Revision history

Table 11.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF8G22LS-205V v.2	20150901	Product data sheet	-	BLF8G22LS-205V v.1
Modifications:	The format of this document has been redesigned to comply with the new identity guidelines of Ampleon			
	Legal texts have been adapted to the new company name where appropriate			
BLF8G22LS-205V v.1	20150901	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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