# BLF8G22LS-270V; BLF8G22LS-270GV

**Power LDMOS transistor**

**Rev. 3 — 1 September 2015**

AMPLEON

**Product data sheet**

## 1. Product profile

### 1.1 General description

270 W LDMOS power transistor with improved video bandwidth for base station applications at frequencies from 2110 MHz to 2170 MHz.

**Table 1. Typical performance**

*Typical RF performance at $T_{case}$ = 25 °C in a common source class-AB production test circuit, tested on straight lead device.*

| Test signal | f (MHz) | $I_{Dq}$ (mA) | $V_{DS}$ (V) | $P_{L(AV)}$ (W) | $G_p$ (dB) | $\eta_D$ (%) | $ACPR_{5M}$ (dBc) |
|---|---|---|---|---|---|---|---|
| 2-carrier W-CDMA | 2110 to 2170 | 2400 | 28 | 80 | 17.3 | 29 | −29 [1] |

[1] 3GPP test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF; 5 MHz carrier spacing.

### 1.2 Features and benefits

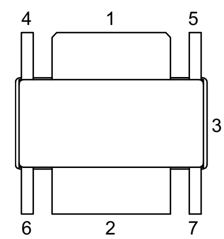
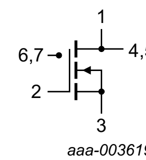
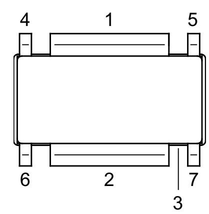
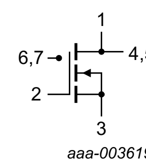
- Excellent ruggedness
- High efficiency
- Low $R_{th}$ providing excellent thermal stability
- Designed for broadband operation
- Decoupling leads to enable improved video bandwidth (80 MHz typical)
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### 1.3 Applications

- RF power amplifiers for base stations and multi carrier applications in the 2110 MHz to 2170 MHz frequency range

## 2. Pinning information

**Table 2. Pinning**

| Pin | Description | Simplified outline | Graphic symbol |
|---|---|---|---|
| **BLF8G22LS-270V (SOT1244B)** | | | |
| 1 | drain |   |   |
| 2 | gate | | |
| 3 | source [1] | | |
| 4 | video lead | | |
| 5 | video lead | | |
| 6 | n.c. | | |
| 7 | n.c. | | |
| **BLF8G22LS-270GV (SOT1244C)** | | | |
| 1 | drain |   |   |
| 2 | gate | | |
| 3 | source [1] | | |
| 4 | video lead | | |
| 5 | video lead | | |
| 6 | n.c. | | |
| 7 | n.c. | | |

[1] Connected to flange.

## 3. Ordering information

**Table 3. Ordering information**

| Type number | Package | | |
|---|---|---|---|
| | Name | Description | Version |
| BLF8G22LS-270V | - | earless flanged ceramic package; 6 leads | SOT1244B |
| BLF8G22LS-270GV | - | earless flanged ceramic package; 6 leads | SOT1244C |

## 4. Limiting values

**Table 4. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

| Symbol | Parameter | Conditions | Min | Max | Unit |
|---|---|---|---|---|---|
| $V_{DS}$ | drain-source voltage | | - | 65 | V |
| $V_{GS}$ | gate-source voltage | | –0.5 | +13 | V |
| $T_{stg}$ | storage temperature | | –65 | +150 | °C |
| $T_j$ | junction temperature | | - | 225 | °C |

# 5. Thermal characteristics

**Table 5. Thermal characteristics**

| Symbol | Parameter | Conditions | Typ | Unit |
|---|---|---|---|---|
| $R_{th(j-c)}$ | thermal resistance from junction to case | $T_{case}$ = 80 °C; $P_L$ = 50 W | 0.26 | K/W |

# 6. Characteristics

**Table 6. DC characteristics**

*$T_j$ = 25 °C; per section unless otherwise specified.*

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| $V_{(BR)DSS}$ | drain-source breakdown voltage | $V_{GS}$ = 0 V; $I_D$ = 4.5 mA | 65 | - | - | V |
| $V_{GS(th)}$ | gate-source threshold voltage | $V_{DS}$ = 10 V; $I_D$ = 450 mA | 1.5 | 1.8 | 2.3 | V |
| $I_{DSS}$ | drain leakage current | $V_{GS}$ = 0 V; $V_{DS}$ = 28 V | - | - | 4.2 | μA |
| $I_{DSX}$ | drain cut-off current | $V_{GS}$ = $V_{GS(th)}$ + 3.75 V; $V_{DS}$ = 10 V | - | 80 | - | A |
| $I_{GSS}$ | gate leakage current | $V_{GS}$ = 11 V; $V_{DS}$ = 0 V | - | - | 420 | nA |
| $g_{fs}$ | forward transconductance | $V_{DS}$ = 10 V; $I_D$ = 450 mA | - | 3.8 | - | S |
| $R_{DS(on)}$ | drain-source on-state resistance | $V_{GS}$ = $V_{GS(th)}$ + 3.75 V; $I_D$ = 15.75 A | - | 0.04 | - | Ω |

**Table 7. RF characteristics**

*Test signal: 2-carrier W-CDMA; PAR = 8.4 dB at 0.01 % probability on the CCDF; 3GPP test model 1; 1-64 DPCH; $f_1$ = 2112.5 MHz; $f_2$ = 2117.5 MHz; $f_3$ = 2162.5 MHz; $f_4$ = 2167.5 MHz; RF performance at $V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; $T_{case}$ = 25 °C; unless otherwise specified; in a class-AB production test circuit, tested on straight lead device.*

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| $G_p$ | power gain | $P_{L(AV)}$ = 80 W | 16.3 | 17.3 | - | dB |
| $RL_{in}$ | input return loss | $P_{L(AV)}$ = 80 W | - | −17 | −7 | dB |
| $\eta_D$ | drain efficiency | $P_{L(AV)}$ = 80 W | 26 | 29 | - | % |
| $ACPR_{5M}$ | adjacent channel power ratio (5 MHz) | $P_{L(AV)}$ = 80 W | - | −29 | −26.5 | dBc |

# 7. Test information

## 7.1 Ruggedness in class-AB operation

The BLF8G22LS-270V and BLF8G22LS-270GV are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: $V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; $P_L$ = 270 W (CW); f = 2110 MHz.

## 7.2 Impedance information

**Table 8. Typical impedance information**

*$I_{Dq}$ = 2400 mA; main transistor $V_{DS}$ = 28 V.*
*$Z_S$ and $Z_L$ defined in Figure 1.*

| f (MHz) | $Z_S$ (Ω) | $Z_L$ (Ω) |
|---|---|---|
| **BLF8G22LS-270V** | | |
| 2110 | 0.68 – j4.73 | 2.42 – j2.08 |
| 2140 | 0.80 – j4.94 | 2.67 – j2.24 |
| 2170 | 0.96 – j5.37 | 2.68 – j2.24 |
| **BLF8G22LS-270GV** | | |
| 2110 | 1.23 – j6.94 | 2.39 – j4.22 |
| 2140 | 1.43 – j7.42 | 2.68 – j4.22 |
| 2170 | 1.44 – j7.50 | 2.90 – j4.30 |

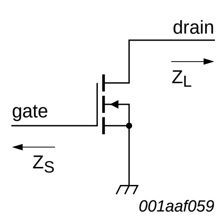
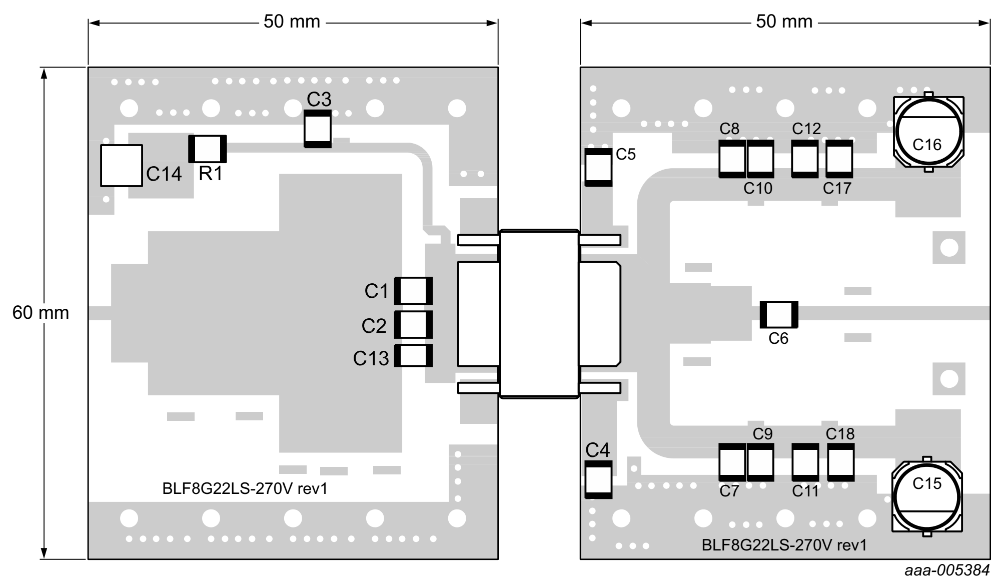



**Fig 1. Definition of transistor impedance**

## 7.3 Test circuit




Printed-Circuit Board (PCB): Taconic RF-35; $\varepsilon_r$ = 3.5; thickness = 0.76 mm.

See Table 9 for list of components.

**Fig 2. Component layout for test circuit**

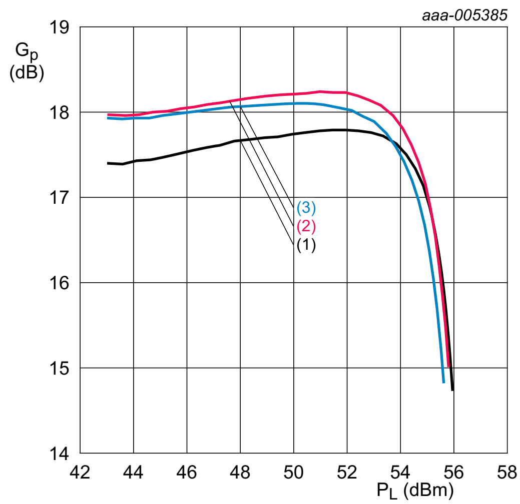
**Table 9. List of components**

*For test circuit, see Figure 2.*

| Component | Description | Value | Remarks |
|---|---|---|---|
| C1, C2 | multilayer ceramic chip capacitor | 0.7 pF | ATC100B |
| C3 | multilayer ceramic chip capacitor | 47 pF | ATC100B |
| C4, C5, C17, C18 | multilayer ceramic chip capacitor | 4.7 µF, 50 V | Murata |
| C6 | multilayer ceramic chip capacitor | 33 pF | ATC100B |
| C7, C8 | multilayer ceramic chip capacitor | 12 pF | ATC100B |
| C9, C10, C11, C12 | multilayer ceramic chip capacitor | 100 pF | ATC100B |
| C13 | multilayer ceramic chip capacitor | 0.2 pF | ATC100B |
| C14 | multilayer ceramic chip capacitor | 10 µF, 50 V | Murata; SMD 2220 |
| C15, C16 | electrolytic capacitor | 470 µF, 63 V | |
| R1 | resistor | 5.1 Ω | SMD 1206; tolerance = 1 % |

### 7.4 Graphs

#### 7.4.1 Pulsed CW



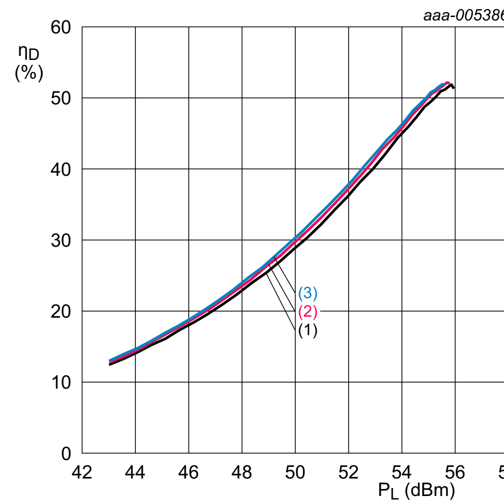

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; $t_p$ = 100 μs; δ = 10 %.

(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 3. Power gain as a function of output power; typical values**




$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; $t_p$ = 100 μs; δ = 10 %.

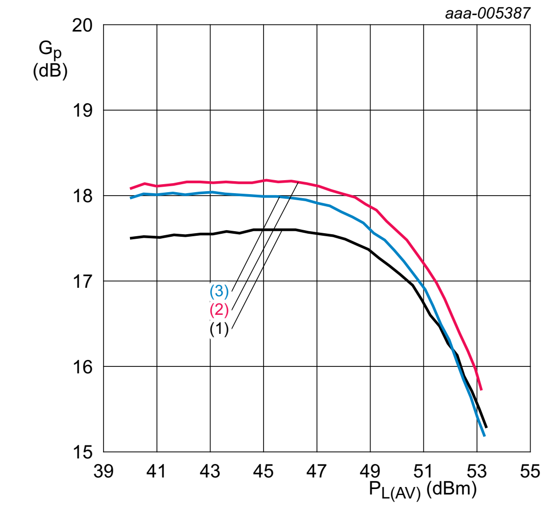
(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 4. Drain efficiency as a function of output power; typical values**

### 7.4.2 IS-95



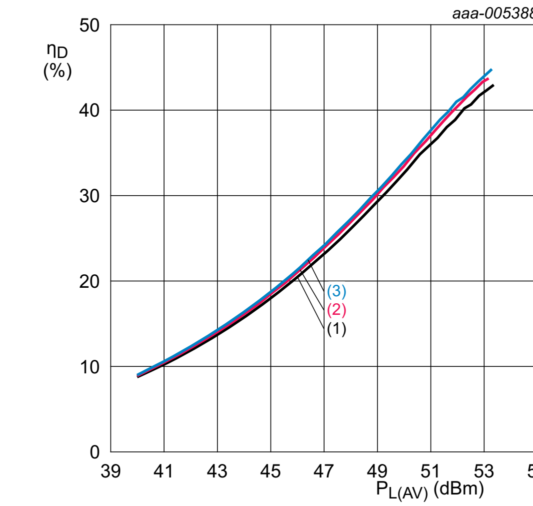

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 5. Power gain as a function of average output power; typical values**



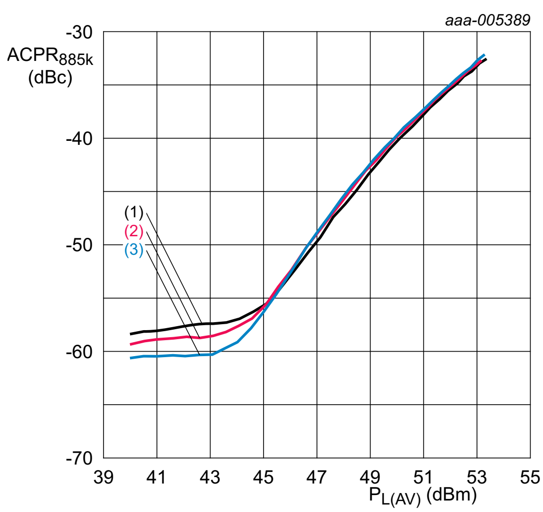

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 6. Drain efficiency as a function of average output power; typical values**



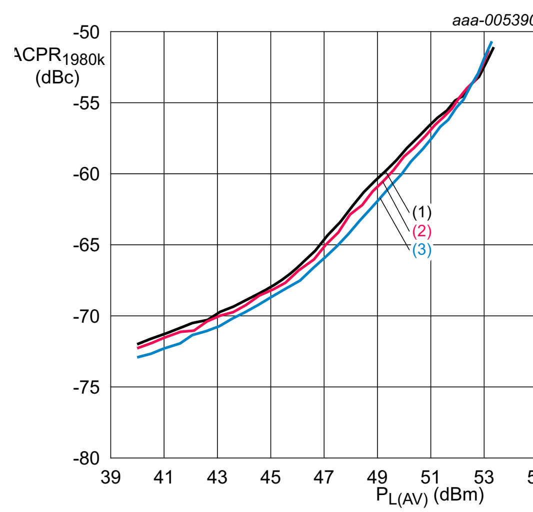

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 7. Adjacent channel power ratio (885 kHz) as a function of average output power; typical values**



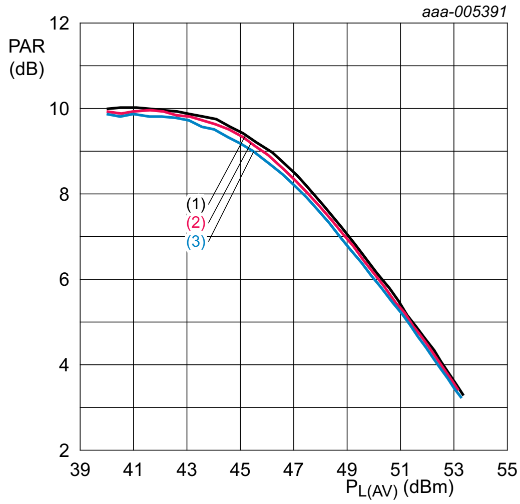

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 8. Adjacent channel power ratio (1980 kHz) as a function of average output power; typical values**



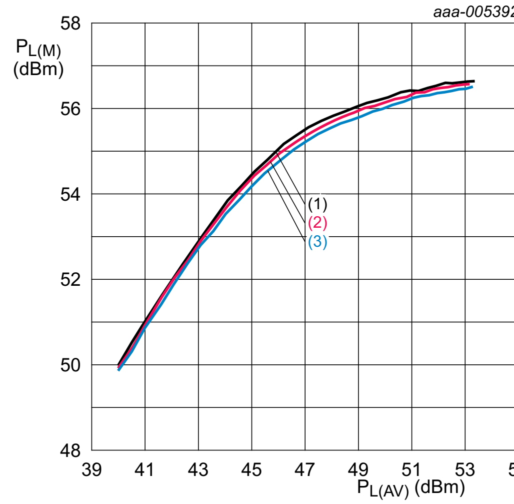

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 9. Peak-to-average power ratio as a function of average output power; typical values**




$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

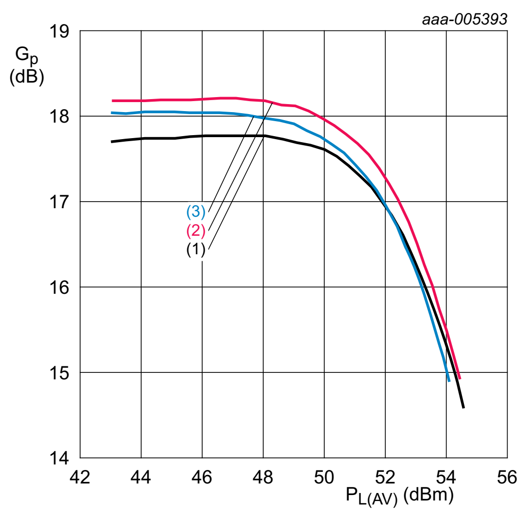
(1) f = 2110 MHz

(2) f = 2140 MHz

(3) f = 2170 MHz

**Fig 10. Peak output power ratio as a function of average output power; typical values**

### 7.4.3 1-carrier W-CDMA



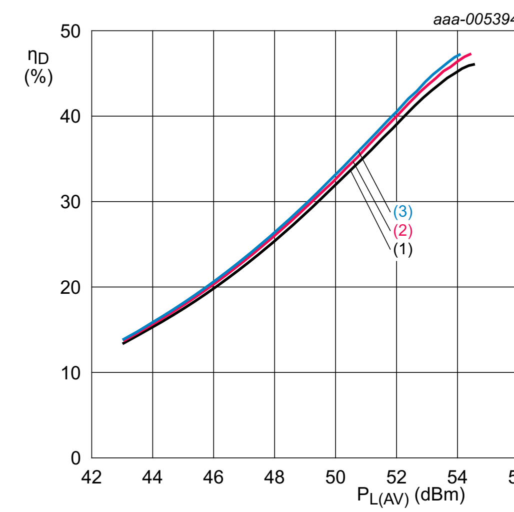

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2112.5 MHz

(2) f = 2140 MHz

(3) f = 2167.5 MHz

**Fig 11. Power gain as a function of average output power; typical values**



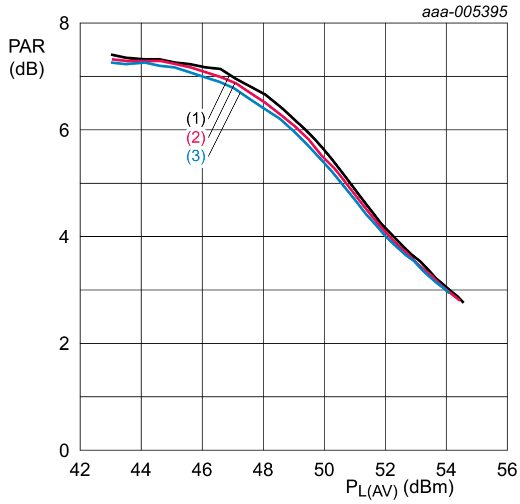

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2112.5 MHz

(2) f = 2140 MHz

(3) f = 2167.5 MHz

**Fig 12. Drain efficiency as a function of average output power; typical values**



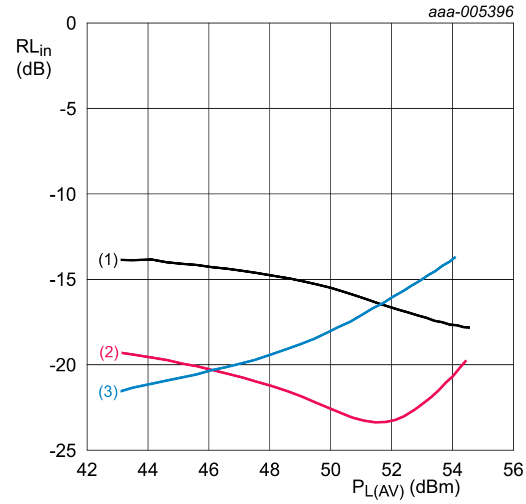

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

(1) f = 2112.5 MHz

(2) f = 2140 MHz

(3) f = 2167.5 MHz

**Fig 13. Peak-to-average power ratio as a function of average output power; typical values**




$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA.

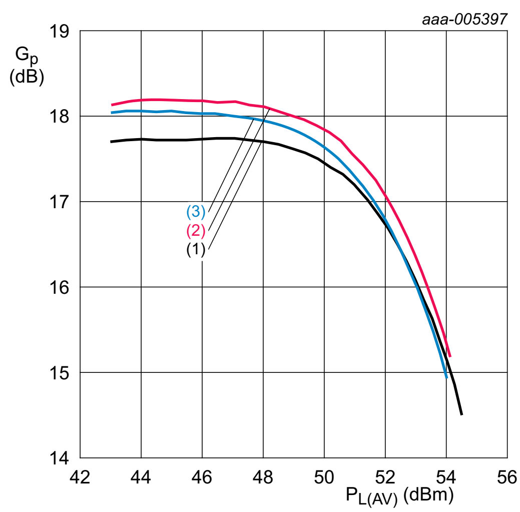
(1) f = 2112.5 MHz

(2) f = 2140 MHz

(3) f = 2167.5 MHz

**Fig 14. Input return loss as a function of average output power; typical values**

### 7.4.4 2-carrier W-CDMA



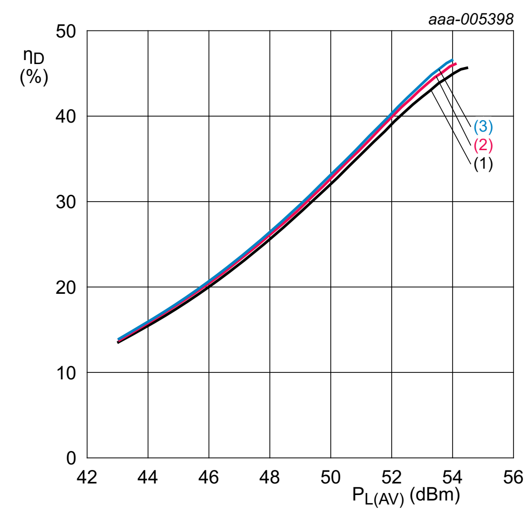

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; 5 MHz carrier spacing.

(1) f = 2115 MHz

(2) f = 2140 MHz

(3) f = 2165 MHz

**Fig 15. Power gain as a function of average output power; typical values**



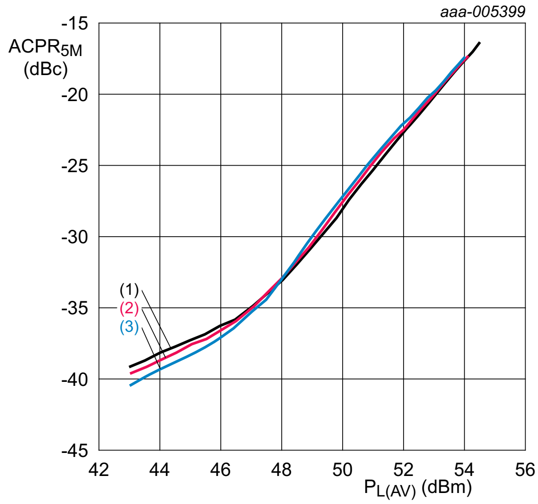

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; 5 MHz carrier spacing.

(1) f = 2115 MHz

(2) f = 2140 MHz

(3) f = 2165 MHz

**Fig 16. Drain efficiency as a function of average output power; typical values**



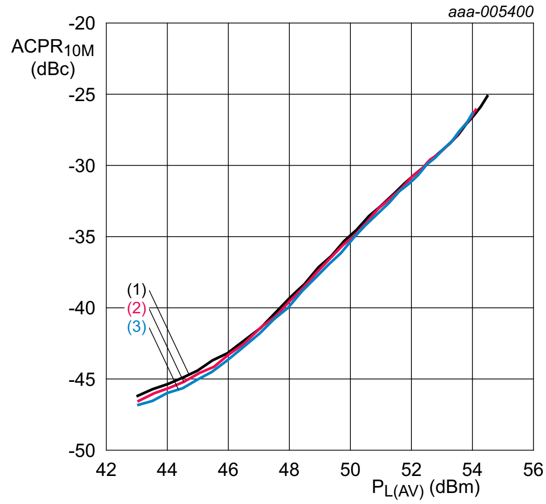

$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; 5 MHz carrier spacing.

(1) f = 2115 MHz

(2) f = 2140 MHz

(3) f = 2165 MHz

**Fig 17. Adjacent channel power ratio (5 MHz) as a function of average output power; typical values**




$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; 5 MHz carrier spacing.

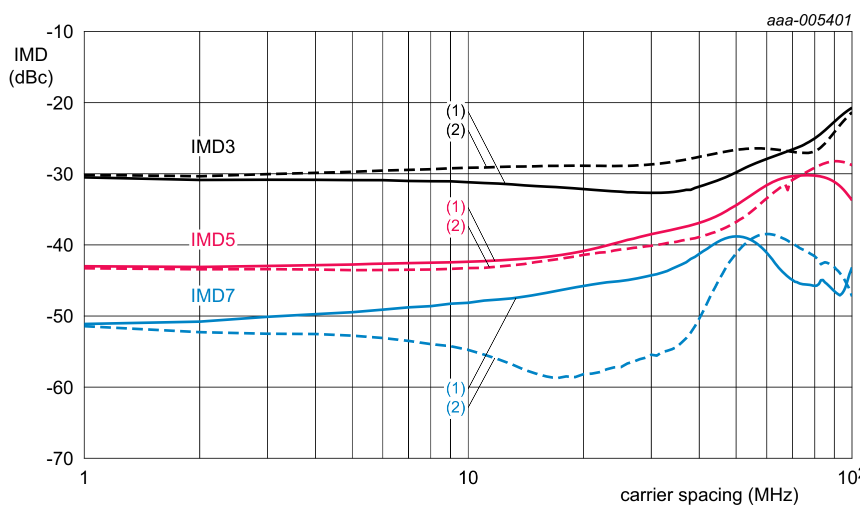
(1) f = 2115 MHz

(2) f = 2140 MHz

(3) f = 2165 MHz

**Fig 18. Adjacent channel power ratio (10 MHz) as a function of average output power; typical values**

### 7.4.5 2-Tone VBW




$V_{DS}$ = 28 V; $I_{Dq}$ = 2400 mA; f = 2140 MHz.

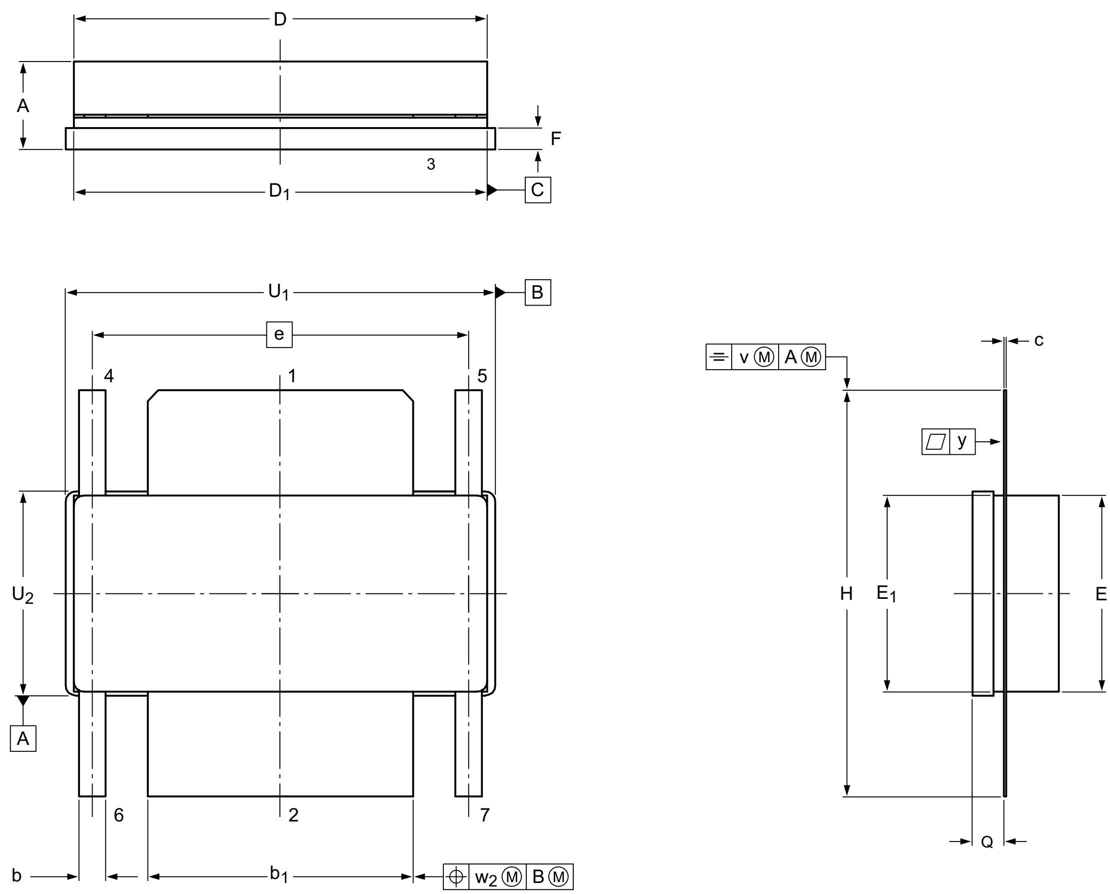
(1) IMD low

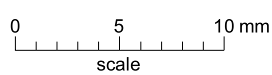
(2) IMD high

**Fig 19. VBW capability in class-AB test circuit**

# 8. Package outline

**Earless flanged ceramic package; 6 leads** **SOT1244B**




Dimensions

| Unit[1] | | A | b | $b_1$ | c | D | $D_1$ | e | E | $E_1$ | F | H | Q[2] | $U_1$ | $U_2$ | v | $w_2$ | y |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| mm | max | 4.75 | 1.41 | 12.83 | 0.18 | 20.02 | 19.96 | | 9.53 | 9.53 | 1.14 | 19.94 | 1.70 | 20.70 | 9.91 | 0.25 | 0.25 | 0.25 |
| | nom | | | | | | | 18.03 | | | | | | | | | | |
| | min | 3.45 | 1.14 | 12.57 | 0.10 | 19.61 | 19.66 | | 9.27 | 9.27 | 0.89 | 18.92 | 1.45 | 20.45 | 9.65 | | | |
| inches | max | 0.187 | 0.055 | 0.505 | 0.007 | 0.788 | 0.786 | | 0.375 | 0.375 | 0.045 | 0.785 | 0.067 | 0.815 | 0.39 | 0.01 | 0.01 | 0.01 |
| | nom | | | | | | | 0.710 | | | | | | | | | | |
| | min | 0.136 | 0.045 | 0.495 | 0.004 | 0.772 | 0.774 | | 0.365 | 0.365 | 0.035 | 0.745 | 0.057 | 0.805 | 0.38 | | | |

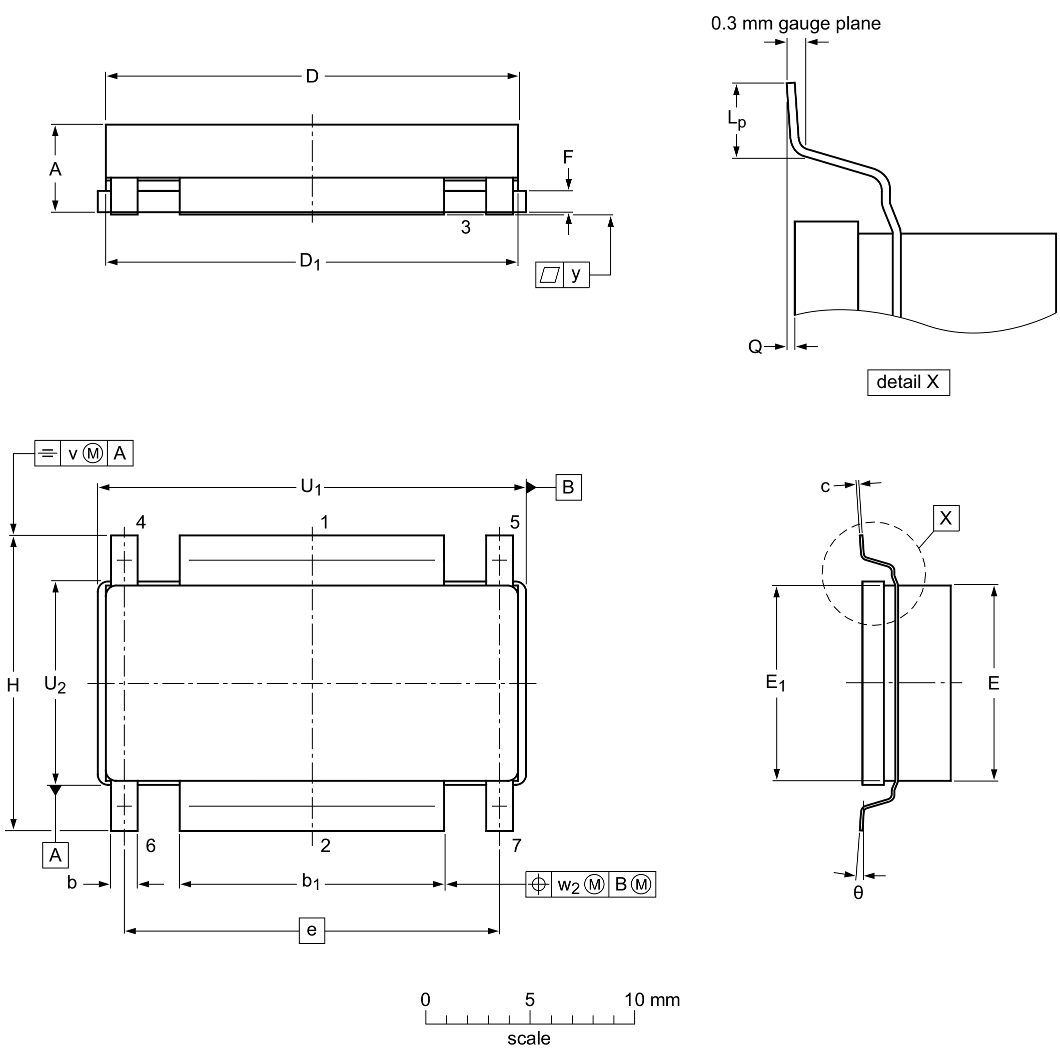
Note
1. Millimeter dimensions are derived from the original inch dimensions.
2. Dimension is measured 0.030 inch (0.76 mm) from body.

*sot1244b_po*

| Outline version | References | | | | European projection | Issue date |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | JEITA | | | |
| SOT1244B | | | | | | ~~12-04-18~~ 12-05-07 |

**Fig 20. Package outline SOT1244B**

**Earless flanged ceramic package; 6 leads** — **SOT1244C**




Dimensions

| Unit[1] | | A | b | $b_1$ | c | D | $D_1$ | e | E | $E_1$ | F | H | $L_p$ | Q | $U_1$ | $U_2$ | v | $w_2$ | y | θ |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| mm | max | 4.75 | 1.41 | 12.83 | 0.18 | 20.02 | 19.96 | | 9.53 | 9.53 | 1.14 | 14.3 | 1.38 | 0.195 | 20.70 | 9.91 | 0.25 | 0.25 | 0.15 | 7° |
| | nom | | | | | | | 18.03 | | | | | | | | | | | | |
| | min | 3.45 | 1.14 | 12.57 | 0.10 | 19.61 | 19.66 | | 9.27 | 9.27 | 0.89 | 14.1 | 0.98 | 0.055 | 20.45 | 9.65 | | | | 0° |
| inches | max | 0.187 | 0.055 | 0.505 | 0.007 | 0.788 | 0.786 | | 0.375 | 0.375 | 0.045 | 0.563 | 0.055 | 0.008 | 0.815 | 0.39 | 0.01 | 0.01 | 0.006 | 7° |
| | nom | | | | | | | 0.710 | | | | | | | | | | | | |
| | min | 0.136 | 0.045 | 0.495 | 0.004 | 0.772 | 0.774 | | 0.365 | 0.365 | 0.035 | 0.555 | 0.039 | 0.002 | 0.805 | 0.38 | | | | 0° |

Note

1. Millimeter dimensions are derived from the original inch dimensions.

*sot1244c_po*

| Outline version | References | | | | European projection | Issue date |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | JEITA | | | |
| SOT1244C | | | | | | ~~12-05-10~~ 12-05-30 |

**Fig 21. Package outline SOT1244C**

## 9. Handling information

**CAUTION**

This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

## 10. Abbreviations

**Table 10. Abbreviations**

| Acronym | Description |
|---|---|
| 3GPP | 3rd Generation Partnership Project |
| CCDF | Complementary Cumulative Distribution Function |
| CW | Continuous Wave |
| DPCH | Dedicated Physical Channel |
| ESD | ElectroStatic Discharge |
| IS-95 | Interim Standard 95 |
| LDMOS | Laterally Diffused Metal Oxide Semiconductor |
| PAR | Peak-to-Average Ratio |
| SMD | Surface Mounted Device |
| VBW | Video BandWidth |
| VSWR | Voltage Standing Wave Ratio |
| W-CDMA | Wideband Code Division Multiple Access |

## 11. Revision history

**Table 11. Revision history**

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|---|---|---|---|---|
| BLF8G22LS-270V_8G22LS-270GV#3 | 20150901 | Product data sheet | | BLF8G22LS-270V_8G22LS-270GV v.2 |
| Modifications: | • The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.<br>• Legal texts have been adapted to the new company name where appropriate. | | | |
| BLF8G22LS-270V_8G22LS-270GV v.2 | 20121203 | Product data sheet | - | BLF8G22LS-270V_8G22LS-270GV v.1 |
| BLF8G22LS-270V_8G22LS-270GV v.1 | 20120613 | Objective data sheet | - | - |

# 12. Legal information

## 12.1 Data sheet status

| Document status[1][2] | Product status[3] | Definition |
|---|---|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.ampleon.com.

## 12.2 Definitions

**Draft —** The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Ampleon does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet —** A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Ampleon sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification —** The information and data provided in a Product data sheet shall define the specification of the product as agreed between Ampleon and its customer, unless Ampleon and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Ampleon product is deemed to offer functions and qualities beyond those described in the Product data sheet.

## 12.3 Disclaimers

**Limited warranty and liability —** Information in this document is believed to be accurate and reliable. However, Ampleon does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Ampleon takes no responsibility for the content in this document if provided by an information source outside of Ampleon.

In no event shall Ampleon be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Ampleon' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Ampleon.

**Right to make changes —** Ampleon reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use —** Ampleon products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Ampleon product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Ampleon and its suppliers accept no liability for inclusion and/or use of Ampleon products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications —** Applications that are described herein for any of these products are for illustrative purposes only. Ampleon makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Ampleon products, and Ampleon accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Ampleon product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Ampleon does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Ampleon products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Ampleon does not accept any liability in this respect.

**Limiting values —** Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale —** Ampleon products are sold subject to the general terms and conditions of commercial sale, as published at http://www.ampleon.com/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Ampleon hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Ampleon products by customer.

**No offer to sell or license —** Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control —** This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products —** Unless this data sheet expressly states that this specific Ampleon product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Ampleon accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Ampleon' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Ampleon' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Ampleon for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Ampleon' standard warranty and Ampleon' product specifications.

**Translations —** A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

### 12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Any reference or use of any 'NXP' trademark in this document or in or on the surface of Ampleon products does not result in any claim, liability or entitlement vis-à-vis the owner of this trademark. Ampleon is no longer part of the NXP group of companies and any reference to or use of the 'NXP' trademarks will be replaced by reference to or use of Ampleon's own Any reference or use of any 'NXP' trademark in this document or in or on the surface of Ampleon products does not result in any claim, liability or entitlement vis-à-vis the owner of this trademark. Ampleon is no longer part of the NXP group of companies and any reference to or use of the 'NXP' trademarks will be replaced by reference to or use of Ampleon's own trademarks.

## 13. Contact information

For more information, please visit: http://www.ampleon.com

For sales office addresses, please visit: http://www.ampleon.com/sales

## 14. Contents





For more information, please visit: http://www.ampleon.com
For sales office addresses, please visit: http://www.ampleon.com/sales

**Date of release: 1 September 2015**
**Document identifier: BLF8G22LS-270V_8G22LS-270GV#3**