BPC2425M9X250

Power LDMOS module Rev. 1 — 29 March 2018

AMMPLEON

Product data sheet

Product profile 1.

1.1 General description

250 W LDMOS power module for Industrial, Scientific and Medical (ISM) applications at frequencies from 2400 MHz to 2500 MHz. The module is designed for high-power CW applications.

Table 1. **Test information**

Typical RF performance at V_{DS} = 32 V; T_{mb} = 25 °C; I_{Dq} = 50 mA.

Test signal	f	V _{DS}	P _L	G _p	η _D
	(MHz)	(V)	(W)	(dB)	(%)
CW	2450	32	300	17	61
CW pulsed [1]	2450	32	300	17.5	63

^[1] Pulse width is 300 μ s; duty cycle is 50 %.

1.2 Features and benefits

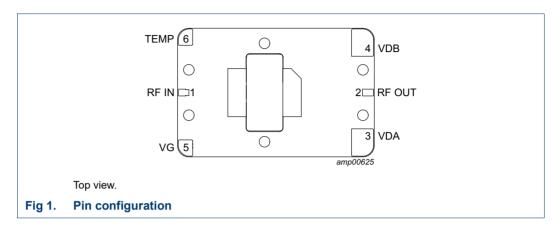
- High efficiency
- Small size: 52 × 34 mm
- Input/output 50 Ω matched
- Designed for broadband operation (2400 MHz to 2500 MHZ)
- Built-in temperature sensor
- Built-in temperature compensation networks
- 100 % RF testing in production
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

RF power amplifiers for CW applications in the 2400 MHz to 2500 MHz frequency range such as industrial heating and drying, scientific, medical, plasma lighting and solid state cooking

2. Pinning information

2.1 Pinning



2.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
RF IN	1	RF input
RF OUT	2	RF output
VDA	3	drain-source voltage, pin A 🗓
VDB	4	drain-source voltage, pin B [1]
VG	5	gate-source voltage
TEMP	6	temperature sensor

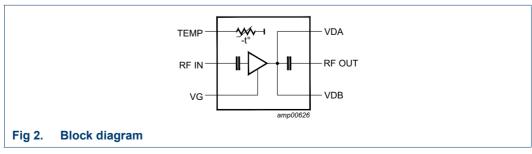
^[1] Drain voltage must be applied for both pins VDA and VDB

3. Ordering information

Table 3. Ordering information

Type number	Packag	Package			
	Name	Description	Version		
BPC2425M9X250	-	pallet; 6 mounting holes; 6 terminations	-		

4. Block diagram



BPC2425M9X250

5. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Mi	in Max	Unit
V_{DS}	drain-source voltage	non operating	0	65	V
V_{GS}	gate-source voltage	non operating	-6	+13	V
T _{stg}	storage temperature		-6	5 +85	°C
T _{mb}	mounting base temperature		[1] 0	60	°C

^[1] Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

6. Characteristics

Table 5. DC characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 2.7 \text{ mA}$	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	$V_{DS} = 32 \text{ V}; I_D = 50 \text{ mA}$	-	1.75	-	V
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 32 V	-	-	4.20	μΑ
R _{GS}	gate-source resistance		300	1500	5000	Ω
C _{iss}	input capacitance	VG pin	-	0.01	-	μF
		VD pin	-	1	-	μF

Table 6. RF Characteristics

Test signal: CW; RF performance at T_{mb} = 25 °C; V_{DS} = 32 V; I_{Dq} = 50 mA; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	P _L = 280 W; f = 2450 MHz	17	18	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	f = 2450 MHz	-	280	-	W
P _{L(3dB)}	output power at 3 dB gain compression	f = 2450 MHz	-	310	-	W
f	frequency	P _L = 250 W	2400	-	2500	MHz
G _{flat}	gain flatness	P _L = 250 W; f = 2400 MHz to f = 2500 MHz	-	1.5	-	dB
RLin	input return loss	P _L = 60 W; f = 2400 MHz to f = 2500 MHz	-	-15	-5	dB
η_{D}	drain efficiency	P _L = 300 W; f = 2450 MHz	56	61	-	%
$\alpha_{\text{sup}(H)}$	harmonic suppression	P _L = 300 W; f = 2450 MHz	-	30	-	dBc

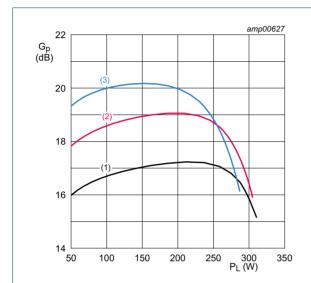
6.1 Ruggedness in class-AB operation

The BPC2425M9X250 is capable of withstanding a load mismatch corresponding to VSWR = 4 : 1 through all phases with a time rate of 15 ms/degree under the following conditions: V_{DS} = 32 V; I_{Dq} = 50 mA; P_L = 250 W (CW); f = 2450 MHz; T_{mb} = 25 °C.

7. Test information

7.1 Graphical data

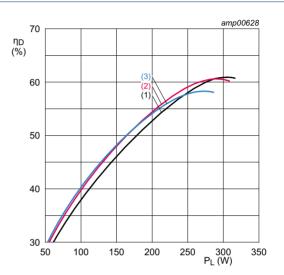
7.1.1 CW



 I_{Dq} = 50 mA; V_{DS} = 32 V; T_{mb} = 25 °C.

- (1) f = 2400 MHz
- (2) f = 2450 MHz
- (3) f = 2500 MHz

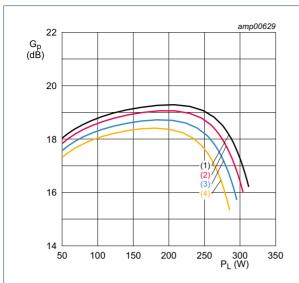
Fig 3. Power gain as a function of output power; typical values



 I_{Dq} = 50 mA; V_{DS} = 32 V; T_{mb} = 25 °C.

- (1) f = 2400 MHz
- (2) f = 2450 MHz
- (3) f = 2500 MHz

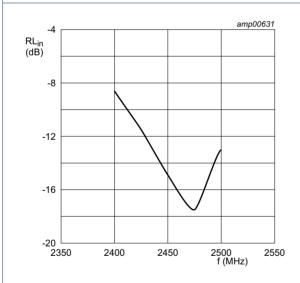
Fig 4. Drain efficiency as a function of output power; typical values



$$I_{Dq}$$
 = 50 mA; V_{DS} = 32 V; f = 2450 MHz.

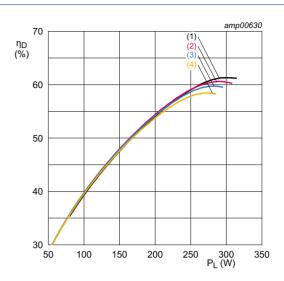
- (1) $T_{mb} = 5 \,^{\circ}C$
- (2) T_{mb} = 25 °C
- (3) $T_{mb} = 40 \, ^{\circ}C$
- (4) $T_{mb} = 60 \, ^{\circ}C$

Fig 5. Power gain as a function of output power; typical values



 I_{Dq} = 50 mA; V_{DS} = 32 V; P_{L} = 280 W.

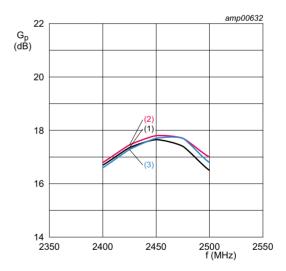
Fig 7. Input return loss as a function of frequency; typical values



 I_{Dq} = 50 mA; V_{DS} = 32 V; f = 2450 MHz.

- (1) $T_{mb} = 5 \,^{\circ}C$
- (2) $T_{mb} = 25 \, ^{\circ}C$
- (3) $T_{mb} = 40 \, ^{\circ}C$
- (4) $T_{mb} = 60 \, ^{\circ}C$

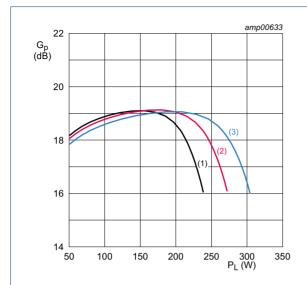
Fig 6. Drain efficiency as a function of output power; typical values



 $I_{Dq} = 50 \text{ mA}.$

- (1) $V_{DS} = 28 \text{ V}$; $P_L = 220 \text{ W}$
- (2) $V_{DS} = 30 \text{ V}$; $P_L = 250 \text{ W}$
- (3) $V_{DS} = 32 \text{ V}$; $P_L = 280 \text{ W}$

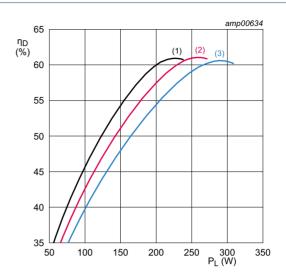
Fig 8. Power gain as a function of frequency; typical values



 I_{Dq} = 50 mA; T_{mb} = 25 °C; f = 2450 MHz.

- (1) $V_{DS} = 28 \text{ V}$
- (2) $V_{DS} = 30 \text{ V}$
- (3) $V_{DS} = 32 \text{ V}$

Fig 9. Power gain as a function of output power; typical values

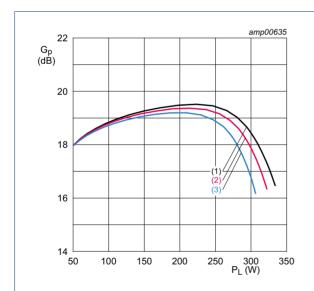


 I_{Dq} = 50 mA; T_{mb} = 25 °C; f = 2450 MHz.

- (1) $V_{DS} = 28 \text{ V}$
- (2) $V_{DS} = 30 \text{ V}$
- (3) $V_{DS} = 32 \text{ V}$:

Fig 10. Drain efficiency as a function of output power; typical values

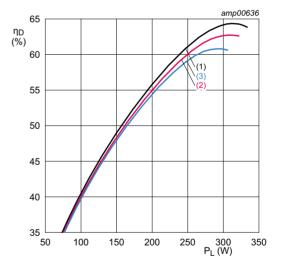
7.1.2 CW pulsed



 I_{Dq} = 50 mA; V_{DS} = 32 V; T_{mb} = 25 °C; f = 2450 MHz.

- (1) $t_p = 300 \ \mu s; \ \delta = 10 \ \%$
- (2) $t_p = 300 \, \mu s; \, \delta = 50 \, \%$
- (3) $t_p = 300 \ \mu s; \ \delta = 90 \ \%$

Fig 11. Power gain as a function of output power; typical values



 I_{Dq} = 50 mA; V_{DS} = 32 V; T_{mb} = 25 °C; f = 2450 MHz.

- (1) $t_p = 300 \,\mu\text{s}; \, \delta = 10 \,\%$
- (2) $t_p = 300 \, \mu s; \, \delta = 50 \, \%$
- (3) $t_p = 300 \ \mu s; \ \delta = 90 \ \%$

Fig 12. Drain efficiency as a function of output power; typical values

8. Package outline

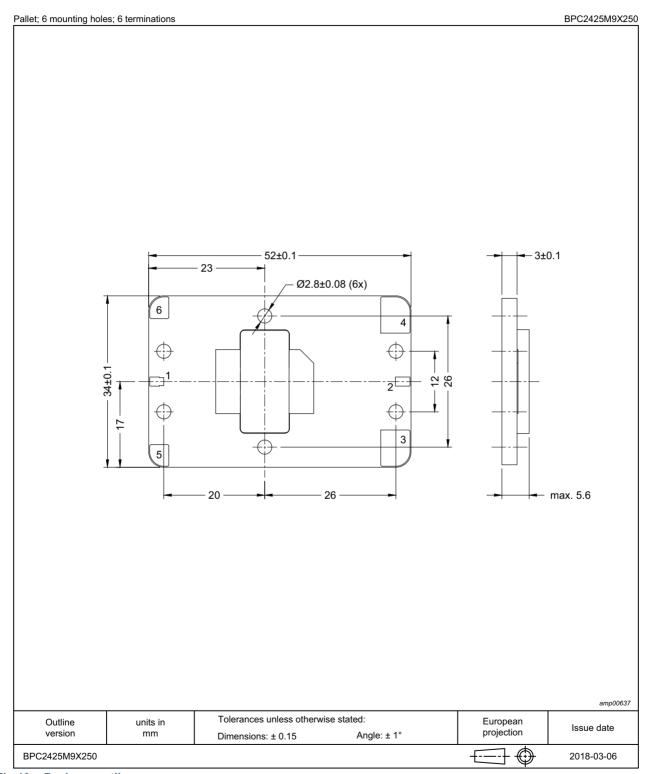


Fig 13. Package outline

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 7. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C1 [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	1C 2

- [1] CDM classification C1 is granted to any part that passes after exposure to an ESD pulse of 250 V, but fails after exposure to an ESD pulse of 500 V.
- [2] HBM classification 1C is granted to any part that passes after exposure to an ESD pulse of 1000 V, but fails after exposure to an ESD pulse of 2000 V.

10. Abbreviations

Table 8. Abbreviations

Acronym	Description
CW	Continuous Wave
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
MTF	Median Time to Failure
RoHS	Restriction of Hazardous Substances
VSWR	Voltage Standing Wave Ratio

11. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BPC2425M9X250 v.1	20180329	Product data sheet	-	-

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Power LDMOS module

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