

Features

- Low voltage operation
- Low current consumption
- Miniature SMD package size
- I²C communication protocol
- Established reliability
- Capacitive technology

Applications

Industrial:

- HVAC systems
- Process monitoring
- Packaging automation

** Medical Devices (low/medium risk):

- Diagnostic equipment
- Analysis equipment

BPS230 Series - 2 mm Humidity Sensor

Absolute Maximum Ratings

Supply Voltage (V_{CC})	-0.3 to 7.0 V
Input Voltage (V_I)	
CE	-0.3 to $V_{CC} + 0.3$ V
SCL/SDA	-0.3 to 7.0 V
Output Voltage (V_O)	-0.3 to $V_{CC} + 0.3$ V
Hi-level Output Current (I_{OH})	
1 Terminal	-5 mA
All Terminals Total	-20 mA
Low-level Output Current (I_{OL})	
1 Terminal	5 mA
All Terminals Total	20 mA
Operating Temperature (T_a)	-40°C to +105°C (-40°F to +221°F)
Storage Temperature (T_{stg})	-50°C to +125°C (-58°F to +257°F)

Recommended Operating Conditions

Power Supply Voltage (V_{CC})	1.62 to 5.5 VDC
Capacitance between V_{CC} and V_{SS} (C_P)	0.1 μ F typical
Pull Up Resistor Value on SDA ¹ (R1)	5 k Ω typical
Pull Up Resistor Value on SCL ¹ (R2)	5 k Ω typical

¹ Select the resistance value to meet AC characteristics.

Electrical Characteristics

Humidity Detection	
Measurement Range	0 to 100 % RH
Resolution (10-bit)	0.1 % RH typical
Humidity Accuracy	
@ 25 °C (20 to 80 % RH)	±3 % RH
@ 5 °C to 45 °C (0 to 100 % RH)	±5 % RH
Hysteresis @ 5 °C to 45 °C (0 to 100 % RH)	± 1 % RH typical
Response Time	
Reach (τ 63 % @ 25 °C, wind velocity @ 1.0 m/s)	8 seconds

Unless otherwise specified: $V_{CC} = 1.62$ to 5.5 V, $V_{SS} = 0$ V, $T_a = -20$ °C to 100 °C

Temperature Detection	
Measurement Range	-30 °C to +100 °C (-22 °F to +212 °F)
Resolution (11 bit)	
-10 °C to +70 °C	0.1 °C (32.18 °F)
All other temperatures	0.4 °C (32.72 °F)
Temperature Accuracy	
@ 5 °C to 60 °C	± 0.4 °C (±32.72 °F)
@ -20 °C to 85 °C	± 1.0 °C (±33.8 °F)
Reproducibility @ -30 °C to 100 °C	± 0.1 °C (±32.18 °F)
Response Time	
Reach (τ 63 % (dependent on surrounding heat conduction))	30 seconds

Unless otherwise specified: $V_{CC} = 1.62$ to 5.5 V, $V_{SS} = 0$ V, $T_a = -30$ °C to 100 °C

Current Consumption

Sleep Current (CE=0, Sleep Mode)	10 nA typical, 400 nA maximum
Average Operating Current	13 μ A typical, 35 μ A maximum

Unless otherwise specified: $V_{CC} = 1.62$ to 5.5 V, $V_{SS} = 0$ V, $T_a = 0$ °C to 60 °C



WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

* RoHS3 Directive 2015/863 Amendments of Annex II on March 31, 2015

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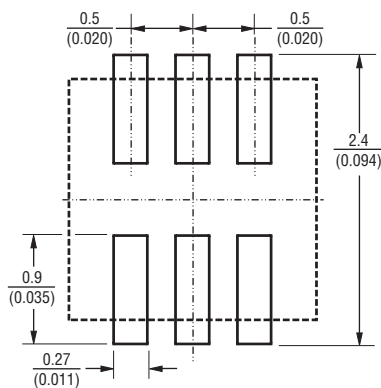
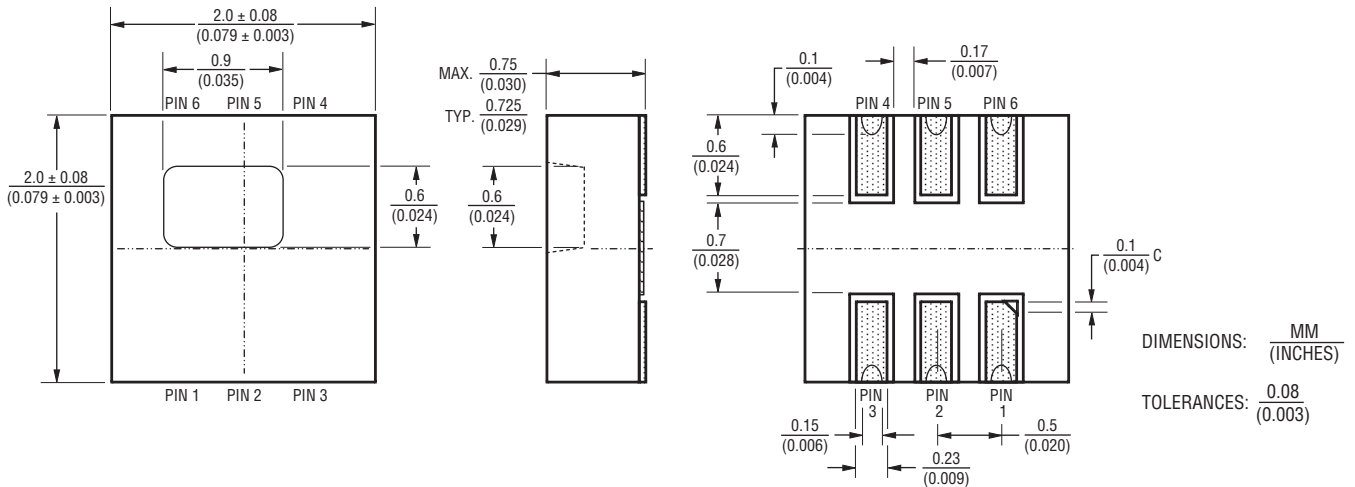
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High Level Input Voltage 1 (VIH1) [Target Terminal: SCL, SDA]	0.7 V _{CC} minimum, V _{CC} maximum
High Level Input Voltage 2 (VIH2) [Target Terminal: CE]	0.8 V _{CC} minimum, V _{CC} maximum
Low Level Input Voltage 1 (VIL1) [Target Terminal: SCL, SDA]	V _{SS} minimum, 0.3 V _{CC} maximum
Low Level Input Voltage 2 (VIL2) [Target Terminal: CE]	V _{SS} minimum, 0.2 V _{CC} maximum
Low Level Output Current (IOL) [VOL = 0.1 V _{CC} , Target Terminal: SCL, SDA]	0.5 mA minimum
Terminal Leak Current 1 (IL1) [Terminal voltage = V _{CC} , Target Terminal: SCL, SDA]	± 1 µA
Terminal Leak Current 2 (IL2) [Terminal voltage = 0 V, Target Terminal: SCL, SDA, CE]	± 1 µA
Input Pull-Down Resistance (RPD) [Terminal voltage = V _{CC} , Target Terminal: CE]	60 kΩ minimum, 150 kΩ typical, 450 kΩ maximum

Moisture Sensitivity Level.....	1
ESD Classification (HBM).....	1 kV
Marking.....	B , Date Code
Standard Packaging.....	3,000 pcs. per 13-inch reel
Weight.....	0.059 grams (0.002 oz)



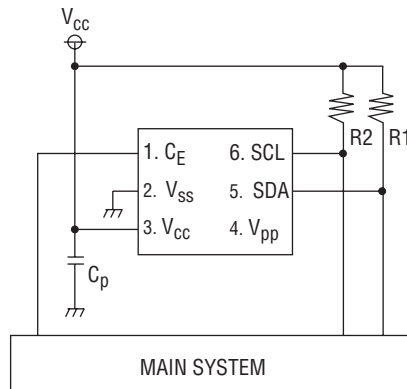
No.	Terminal Name	Function
1	CE	Chip enable terminal
2	V _{SS}	Power supply terminal (-)
3	V _{CC}	Power supply terminal (+)
4	NC	No connection
5	SDA	I ² C serial data
6	SCL	I ² C serial clock

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BPS230 Series - 2 mm Humidity Sensor

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Basic Circuit Schematic



C_p 0.1 μ F

R1 5k Ω

R2 5k Ω

NOTE: R1 and R2 are reference values. Resistor values should be selected to meet the AC characteristics.

Operation Mode

Operation Mode	Terminal Setup	Operation State of Each Functional Block						
	CE	V _{pp}	Power Supply	Oscillation	Temp. Detection	Capacitance Detection	OTP Memory	I ² C-Bus
Sleep *1	0	NC	Stop	Stop	Stop	Stop	Stop	Stop
Standby	1	NC	Operation	Operation	Stop	Stop	Read-out Possible	Operation

*1 In case of power control mode, there is no sleep operation.
I²C slave address (SADR) is defined as "111 1111" (7Fh).

Control Register Map

Address	Bit	Bit Name	Function	Value	Read--Out	Write-In	R/W	Init.
00h	D7-1	-	Reserved	-			R	0
	D0	RESET	Reset	0	Normal Operation	None	R/W	0
				1	-	Reset Action		
01h	D7-6	MANMODE	Manual Detection Mode	00	Normal Operation Mode		R/W	0
	D5-3	HAVE[2:0]	Humidity Detection Value Avg. Mode	000	No Averaging Process			
				001	2 Times Average Mode			
				01x	4 Times Average Mode			
				1xx	8 Times Average Mode			
	D2	TAVE	Temperature Detection Value Avg. Mode	0	8 Times Average Mode		R/W	0
				1	16 Times Average Mode			

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Control Register Map (Continued)

Address	Bit	Bit Name	Function	Value	Read-Out	Write-In	R/W	Init.
01h	D1	-	Reserved	-			R	0
	D0	MAN	Manual Detection Mode	0	Standby State	Detection Operation Stop	R/W	0
				1	Under Detection Operation	Detection Operation Start		
03h	D7-1	-	Reserved	-			R	0
	D0	ERR	Manual Detection Error Flag	0	No Error	Nothing is Done	R/W	0
				1	Error Occurred	Error Flag Reset		
04h	D7-0	HC[7:0]	Humidity Detection Result (After Correction Operation)	000h-3FFh			R	X
05h	D7-2	-	Reserved	-			R	0
	D1-0	HC[9:8]	Humidity Detection Result (After Correction Operation)				R	X
06h	D7-0	TC[7:0]	Temperature Detection Result (After Correction Operation)	000h-7FFh			R	X
07h	D7-3	-	Reserved	-			R	0
	D2-0	TC[10:8]	Temperature Detection Result (After Correction Operation)				R	X
0Ah	D7-0	K[7:0]	Capacity Detection Result (Before Correction Operation)	000h-FFFFh			R	0

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Control Register Map (Continued)

Address	Bit	Bit Name	Function	Value	Read-Out	Write-In	R/W	Init.
0Bh	D7-0	K[15:8]	Capacity Detection Result <i>(Before Correction Operation)</i>				R	0
2Ch	D7-5	-	Reserved	-	-		R	0
	D4	SCR_ON_R	Standard Capacity Connection Control	0	Outside Capacity Cutting		R/W	0
				1	Outside Capacity Connection			
	D3-0	SCI_ON_R[3:0]	Internal Capacity Connection Control	0h~Fh x 0.6 pF Example: At the time of 8 hours, access to internal capacity of 4.8 pF			R/W	X
03h	D7-1	-	Reserved	-			R	0
	D0	ERR	Manual Detection Error Flag	0	No Error	Nothing is Done	R/W	0
				1	Error Occurred	Error Flag Reset		

Transfer Function Formula

Humidity

$$RH = \frac{100}{2^{10}} \times RH_{IC} \quad (0 \sim 100 \% RH)$$

RH_{IC} : IC Humidity Output Data (10 bit)

Refer to Register Map:

RH_{IC} = Data of the addresses 04H and 05H (000h ~ 3FFh)
It changes into a decimal and is operation.

Temperature

$$T = [T_{IC} - (2^{10} - \frac{25}{0.1})] \times 0.1 \quad (-30 \sim 100 ^\circ C)$$

T_{IC} : IC Temperature Output Data (11 bit)

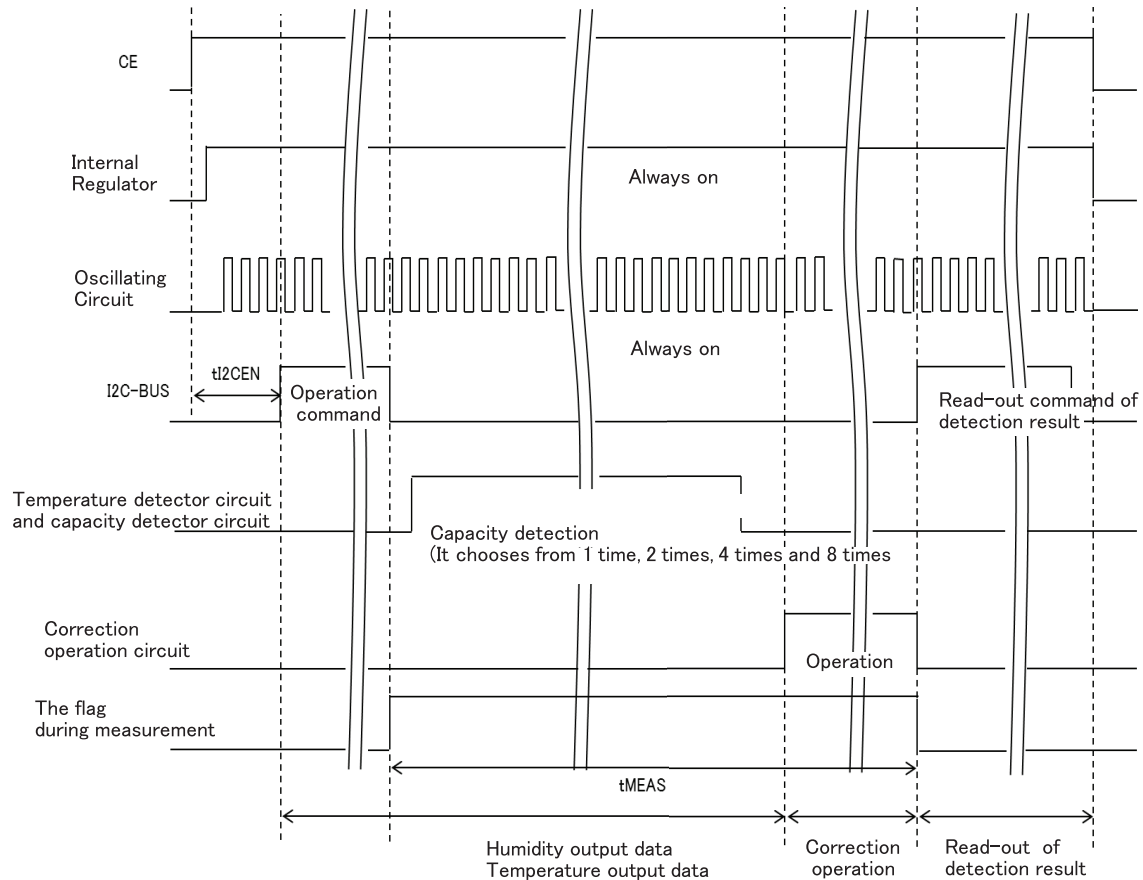
Refer to Register Map:

T_{IC} = Data of the addresses 06H and 07H (000h ~ 7FFh)
It changes into a decimal and is operation.

BPS230 Series - 2 mm Humidity Sensor

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Capacitance/Temperature Detection Sequence



How To Order

BPS230 - D 3P0 - S 10 E

Model Series _____
Humidity-Temperature Sensor

Output Type _____
D = Digital

Accuracy (% RH) _____
3P0 = ± 3.0

Moisture Sensitivity _____
S = Standard

Resolution _____
10 = 10-bit

Packaging Designator _____
E = 3000 pcs. per 7-inch Reel

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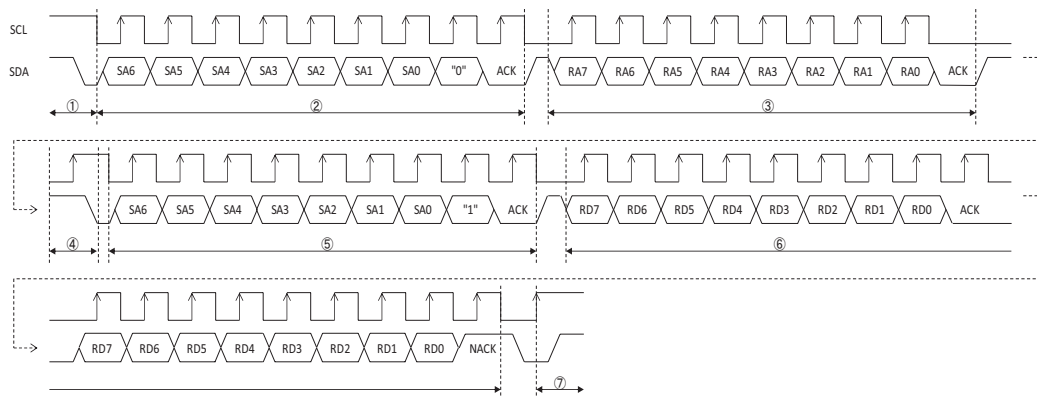
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Output Type Waveform and Data Read/Write Procedure

I²C-BUS Data Read-out Procedure

- ① I²C master device releases START condition.
- ② I²C master device transmits slave address and WRITE mode selection.
- ③ I²C master device transmits register address of this IC.
- ④ I²C master device releases repeated START condition. (Release method is same as START condition.)
- ⑤ I²C master device again transmits slave address and READ mode selection. (Read mode can be selected by transmitting "1" in 8th bit.)
- ⑥ I²C master device reads-out data from register address designated at ③.
It is possible to read-out data while register address increments one, by reading-out multiple data continuously. However, during continuous read-out, please return ACK to this IC as a reply of master.
- ⑦ After the completion of all read-out, I²C master device releases STOP condition.



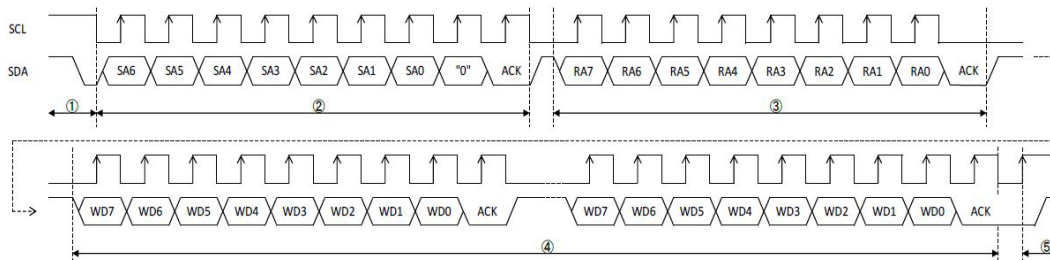
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Output Type Waveform and Data Read/Write Procedure (Continued)

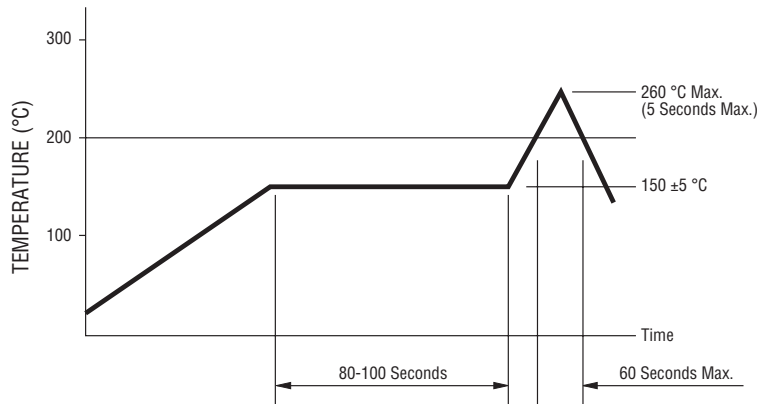
I²C-BUS Data Write-in Procedure

- ① I²C master device releases START condition. (Start condition can be released by changing SDA from "H" to "L" while SCL is in "H" state.)
- ② I²C master device transmits slave address and WRITE mode selection. (Write mode can be selected by transmitting "0" in 8th bit while 1~7th bits are slave address.)
- ③ I²C master device transmits register address of this IC.
- ④ I²C master device transmits write-in data.
- ⑤ It is possible to write-in data while register address increments one, by transmitting multiple write-in data continuously.

After the completion of transmitting all write-in data, I²C master device releases stop condition. (Stop condition can be released by changing SDA from "L" to "H" while SCL is in "H" state.)



Solder Profile



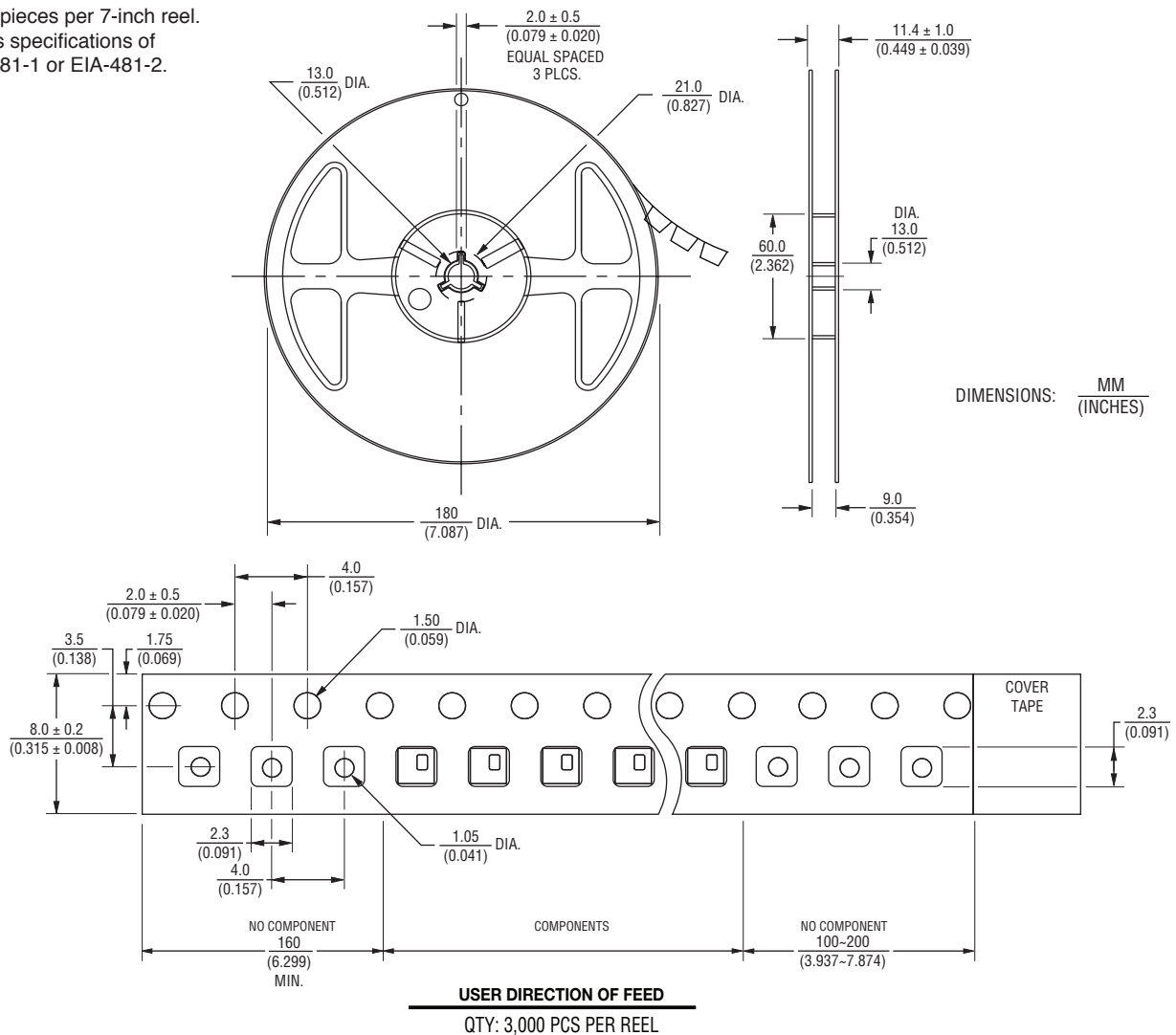
Processing Method: Reflow soldering with infrared heat or forced air convection (only once).

Notes:

1. No clean solder paste is recommended.
2. Aqueous wash is not recommended.
3. Use of water soluble soldering flux should be avoided due to possible corrosion.
4. Multiple passes through the soldering process is not recommended.
5. Other SMD processes and profiles should be verified by the customer.

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3000 pieces per 7-inch reel.
Meets specifications of
EIA-481-1 or EIA-481-2.

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