



# **Product Specification**

# SPECIFICATION FOR APPROVAL

| Title | 55.0" WUXGA TFT LCD |
|-------|---------------------|
|-------|---------------------|

| BUYER  |          |
|--------|----------|
| MODEL  | BR550D15 |
| SUFFIX | AU       |





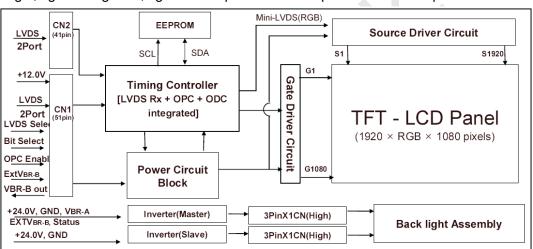
### **Product Specification**

#### 1. General Description

The BR550D15 is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element.

It is a transmissive type display operating in the normally black mode. This TFT-LCD has a 54.64 inch diagonal measured active display area with WUXGA resolution(1080 vertical by 1920 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes.

Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot, thus, presenting a palette of more than 1.06Bilion colors. It is intended to support Public Display where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



#### **General Features**

| General Features       | Fileral Features   |   |  |  |  |
|------------------------|--|---|--|--|--|
| Active screen size     | 54.64 inch (1387.80mm) diagonal                          |   |  |  |  |
| Outline Dimension      | 1286.0(H) x 745.0(V) x 60.0(D) mm(Typ.)                  |   |  |  |  |
| Active Area            | 1209.6(H) x 680.4(V) mm                                  |   |  |  |  |
| Pixel Pitch            | 0.630 mm x 0.630 mm                                      |   |  |  |  |
| Pixel Format           | 1920 horiz. by 1080 vert. Pixels. RGB stripe arrangement |   |  |  |  |
| Display Colors         | 10bit 1.06Bilion colors                                  |   |  |  |  |
| Luminance, white       | 1500 cd/m2(  | Typ. Center 1 point)                              |  |  |  |
| Power Consumption      | 387.56 W Watts(Typ.) (PDD=7.56W PBL=380W)                |   |  |  |  |
| Weight                 | 21Kg (Typ.)  |   |  |  |  |
| Display operating mode | Transmissive mode, normally black                        |   |  |  |  |
| Surface treatments     | Hard coating   | (3H), Anti-glare treatment of the front polarizer |  |  |  |





## **Product Specification**

#### 2. Absolute maximum ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. Absolute Maximum Ratings

| Parameter             | Symbol | Values |       | Units | Notes   |
|-----------------------|--------|--------|-------|-------|---------|
|                       |        | Min    | Max   |       |         |
| Power Supply Input    | Vcc    | -0.3   | +14.0 | Vdc   | At 25 ℃ |
| Voltage               |        |        |       |       |         |
| Operating Temperature | Тор    | 0      | +50   | °C    | 1       |
| Storage Temperature   | Тѕт    | -20    | +60   | °C    | 1       |
| Operating Ambient     | Нор    | 10     | +90   | %RH   | 1       |
| Humidity              |        |        |       |       |         |
| Storage Humidity      | Нѕт    | 10     | +90   | %RH   | 1       |

Note : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39  $\,^\circ\text{C}$  Max, and no condensation of water.

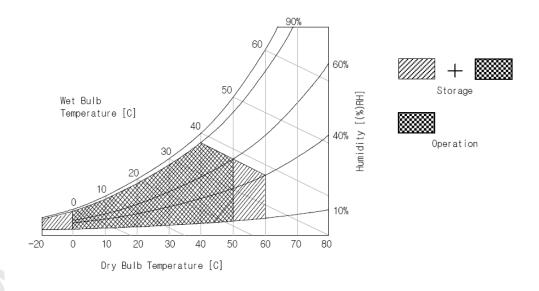


Figure 2. Temperature and relative humidity





## **Product Specification**

#### 3. Electrical specifications

#### 3-1. Electrical characteristics

The BR550D15 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. Another which powers the LED Backlight .LED Driver is an internal unit to the LCD.

 Table 2. Electrical Characteristics

|                                  | i abic z.         | Littli ita | . Onalact | CHISTICS |       |         |
|----------------------------------|-------------------|------------|-----------|----------|-------|---------|
| Parameter                        | Symbol            |            | Values    |          | Units | Notes   |
|                                  |                   | Min        | Туре      | Max      |       |         |
| Power Supply Input<br>Voltage    | Vcc               | 10.8       | 12.0      | 13.2     | V     |         |
| Permissive Power Input<br>Ripple | V <sub>RF</sub>   | -          | -         | 0.1      | V     |         |
| Power Supply Input<br>Current    | Icc               | -          | 0.875     | 1.14     | А     | 1       |
| Power Consumption                | Pc                | -          | 7.56      | 10.8     | Watts |         |
| In Rush Current                  | Irush             | -          | -         | 5        | Α     | 2       |
| LED Power supply<br>Voltage      | V <sub>B</sub> L  | 22.8       | 24        | -        | V     | 3       |
| LED Power Supply current         | Івь               | -(         | 15.83     | -        | А     |         |
| LED BL Power<br>Consumption      | P <sub>BL</sub>   | -          | 380       | -        | Watts | 4       |
| Brightness Adjust                | V <sub>BR-B</sub> | 10         | -         | 100      | %     | On Duty |
| Linear Brightness Adjust         | V <sub>BR-A</sub> | 0          | -         | 3.3      | V     |         |
| Life Time                        |                   | 50,000     |           |          | Hrs   | 5       |

**Note:** Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape TFT-LCD Module have a low luminance.

- 1.The specified current and power consumption are under the VCC=12.0V, 25°C, fv (frame frequency)=120Hz condition.
- 2. The duration of rush current is about 2ms. And Vcc rise time is 500us ± 20%.
- 3. Operating voltage is measured under 25°C.
- 4.The LED Backlight power consumption shown above include LED Diver Module under 25°C.
- 5. The life time is determined as the time at which brightness of lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25°C.





## **Product Specification**

#### 3-2. Interface Connections

LED BackLight Driver 14pin connectors is shown in the table 4.

#### Table 3. Module connector pin configuration

|    |             |                                     |   | <u> </u> |            |                                      |
|----|-------------|-------------------------------------|---|----------|------------|--------------------------------------|
| No | Symbol      | Description                         | Ш | No       | Symbol     | Description                          |
| 1  | GND         | Ground                              | П | 27       | Bit Select | 'H' or NC= 10bit(D), 'L' = 8bit      |
| 2  | NC          | No Connection                       | П | 28       | R2AN       | SECOND LVDS Receiver Signal (A-)     |
| 3  | NC          | No Connection                       | П | 29       | R2AP       | SECOND LVDS Receiver Signal (A+)     |
| 4  | NC          | No Connection                       | П | 30       | R2BN       | SECOND LVDS Receiver Signal (B-)     |
| 5  | NC          | No Connection                       | П | 31       | R2BP       | SECOND LVDS Receiver Signal (B+)     |
| 6  | NC          | No Connection                       | П | 32       | R2CN       | SECOND LVDS Receiver Signal (C-)     |
| 7  | LVDS Select | 'H' =JEIDA , 'L' or NC = VESA       | П | 33       | R2CP       | SECOND LVDS Receiver Signal (C+)     |
| 8  | VBR EXT     | External VBR (From System)          | П | 34       | GND        | Ground                               |
| 9  | OPC OUT     | OPC output (From LCM)               | П | 35       | R2CLKN     | SECOND LVDS Receiver Clock Signal(-) |
| 10 | OPC Enable  | 'H' = Enable , 'L' or NC = Disable  | П | 36       | R2CLKP     | SECOND LVDS Receiver Clock Signal(+) |
| 11 | GND         | Ground                              | П | 37       | GND        | Ground                               |
| 12 | R1AN        | FIRST LVDS Receiver Signal (A-)     | П | 38       | R2DN       | SECOND LVDS Receiver Signal (D-)     |
| 13 | R1AP        | FIRST LVDS Receiver Signal (A+)     | П | 39       | R2DP       | SECOND LVDS Receiver Signal (D+)     |
| 14 | R1BN        | FIRST LVDS Receiver Signal (B-)     | Π | 40       | R2EN       | SECOND LVDS Receiver Signal (E-)     |
| 15 | R1BP        | FIRST LVDS Receiver Signal (B+)     | П | 41       | R2EP       | SECOND LVDS Receiver Signal (E+)     |
| 16 | R1CN        | FIRST LVDS Receiver Signal (C-)     | П | 42       | Reserved   | No connection or GND                 |
| 17 | R1CP        | FIRST LVDS Receiver Signal (C+)     | П | 43       | Reserved   | No connection or GND                 |
| 18 | GND         | Ground                              | Ц | 44       | GND        | Ground                               |
| 19 | R1CLKN      | FIRST LVDS Receiver Clock Signal(-) | Ц | 45       | GND        | Ground                               |
| 20 | R1CLKP      | FIRST LVDS Receiver Clock Signal(+) | П | 46       | GND        | Ground                               |
| 21 | GND         | Ground                              | П | 47       | NC         | No connection                        |
| 22 | R1DN        | FIRST LVDS Receiver Signal (D-)     | Ц | 48       | VLCD       | Power Supply +12.0V                  |
| 23 | R1DP        | FIRST LVDS Receiver Signal (D+)     | П | 49       | VLCD       | Power Supply +12.0V                  |
| 24 | R1EN        | FIRST LVDS Receiver Signal (E-)     | П | 50       | VLCD       | Power Supply +12.0V                  |
| 25 | R1EP        | FIRST LVDS Receiver Signal (E+)     | Ц | 51       | VLCD       | Power Supply +12.0V                  |
| 26 | Reserved    | No connection or GND                | П | -        | -          | -                                    |

#### Notes:

- 1.All GND(ground) pins should be connected together and should also be connected to the LCD's metal frame.
- 2.All VCC(power input) pins should be connected together.
- 3.All NC pins should be separated from other signal or power.

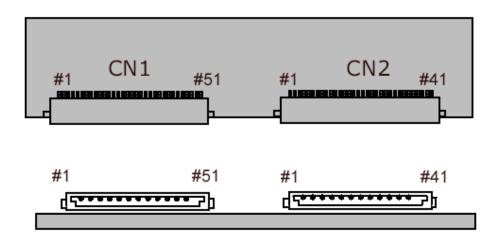




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LCD Connector(CN2): FI-RE41S-HF, Refer to below table

| No | Symbol | Description                         |   | No | Symbol | Description                          |
|----|--------|-------------------------------------|---|----|--------|--------------------------------------|
| 1  | NC     | No connection(Reserved)             | П | 22 | RE3N   | THIRD LVDS Receiver Signal (E-)      |
| 2  | NC     | No connection                       | Ц | 23 | RE3P   | THIRD LVDS Receiver Signal (E+)      |
| 3  | NC     | No connection                       | Ц | 24 | GND    | Ground                               |
| 4  | NC     | No connection                       | Ц | 25 | GND    | Ground                               |
| 5  | NC     | No connection                       | П | 26 | RA4N   | FOURTH LVDS Receiver Signal (A-)     |
| 6  | NC     | No connection                       | П | 27 | RA4P   | FOURTH LVDS Receiver Signal (A+)     |
| 7  | NC     | No connection                       | П | 28 | RB4N   | FOURTH LVDS Receiver Signal (B-)     |
| 8  | NC     | No connection                       | П | 29 | RB4P   | FOURTH LVDS Receiver Signal (B+)     |
| 9  | GND    | Ground                              | П | 30 | RC4N   | FOURTH LVDS Receiver Signal (C-)     |
| 10 | RA3N   | THIRD LVDS Receiver Signal (A-)     | П | 31 | RC4P   | FOURTH LVDS Receiver Signal (C+)     |
| 11 | RA3P   | THIRD LVDS Receiver Signal (A+)     | П | 32 | GND    | Ground                               |
| 12 | RB3N   | THIRD LVDS Receiver Signal (B-)     | П | 33 | RCLK4N | FOURTH LVDS Receiver Clock Signal(-) |
| 13 | RB3P   | THIRD LVDS Receiver Signal (B+)     | П | 34 | RCLK4P | FOURTH LVDS Receiver Clock Signal(+) |
| 14 | RC3N   | THIRD LVDS Receiver Signal (C-)     | П | 35 | GND    | Ground                               |
| 15 | RC3P   | THIRD LVDS Receiver Signal (C+)     | П | 36 | RD4N   | FOURTH LVDS Receiver Signal (D-)     |
| 16 | GND    | Ground                              | П | 37 | RD4P   | FOURTH LVDS Receiver Signal (D+)     |
| 17 | RCLK3N | THIRD LVDS Receiver Clock Signal(-) | П | 38 | RE4N   | FOURTH LVDS Receiver Signal (E-)     |
| 18 | RCLK3P | THIRD LVDS Receiver Clock Signal(+) | П | 39 | RE4P   | FOURTH LVDS Receiver Signal (E+)     |
| 19 | GND    | Ground                              | П | 40 | GND    | Ground                               |
| 20 | RD3N   | THIRD LVDS Receiver Signal (D-)     | П | 41 | GND    | Ground                               |
| 21 | RD3P   | THIRD LVDS Receiver Signal (D+)     | П | -  |        |                                      |



Rear view of LCM





## **Product Specification**

#### **Table 4. LED Backlight Driver connector pin configuration**

|        |          |                          | Board A (CN1101) | Board A (CN1106) |      |
|--------|----------|--------------------------|------------------|------------------|------|
| Pin No | Symbol   | Description              | Board B (CN2101) | Board B (CN2106) | Note |
| 1      | VBL      | Power Supply +24.0V      | VBL              | VBL              |      |
| 2      | VBL      | Power Supply +24.0V      | VBL              | VBL              |      |
| 3      | VBL      | Power Supply +24.0V      | VBL              | VBL              |      |
| 4      | VBL      | Power Supply +24.0V      | VBL              | VBL              |      |
| 5      | VBL      | Power Supply +24.0V      | VBL              | VBL              |      |
| 6      | GND      | Backlight Ground         | GND              | GND              |      |
| 7      | GND      | Backlight Ground         | GND              | GND              |      |
| 8      | GND      | Backlight Ground         | GND              | GND              | 1    |
| 9      | GND      | Backlight Ground         | GND              | GND              |      |
| 10     | GND      | Backlight Ground         | GND              | GND              |      |
| 11     | NC       | No connection            | OPEN or GND      | Don't Care       |      |
| 12     | Von/off  | Backlight ON/OFF control | Von/off          | Don't Care       |      |
| 13     | EXTVBR-B | External PWM             | EXTVBR-B         |                  | 2    |
| 14     | GND      | Backlight Ground         | GND              |                  | 3    |

#### **Notes:**

- 1. The backlight ground should be common with LCD metal frame.
- 2. This Pin support Linear Dim Voltage control brightness.

| Voltage | Function           | Voltage | Function            |
|---------|--------------------|---------|---------------------|
| 0V      | Minimum Duty (10%) | 3.3V    | Maximum Duty (100%) |

3. #14 pin Must be Connected to GND.







### **Product Specification**

#### 3-3. Power Sequence

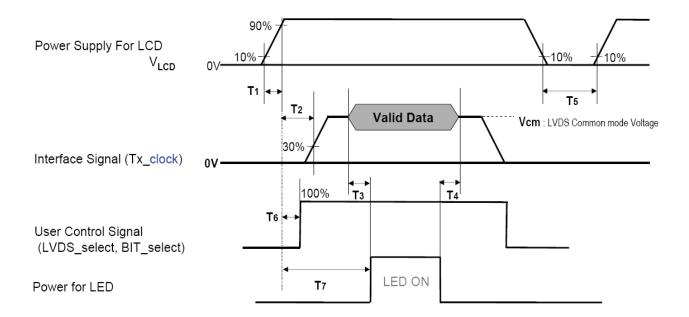


Table 5. Power sequence time delay

| Parameter      |     | Units |                |    |
|----------------|-----|-------|----------------|----|
|                | Min | Туре  | Max            |    |
| T1             | 0.5 | -     | 20             | ms |
| T <sub>2</sub> | 0.5 | -     | -              | ms |
| Тз             | 200 | -     | -              | ms |
| T4             | 200 | -     | -              | ms |
| T5             | 2.0 | -     | T <sub>2</sub> | s  |
| T <sub>6</sub> | -   | _     | _              | ms |
| T7             | 0.5 | -     |                | s  |

#### Notes:

- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signals are valid.

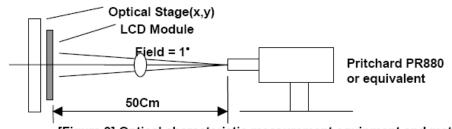




## **Product Specification**

#### 4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25  $\,^{\circ}$ C. The values specified are measured at an approximate distance 50cm from the LCD surface at a viewing angle 0  $\,^{\circ}$ .



[Figure 9] Optical characteristic measurement equipment and method

#### **Table 6. Optical characteristics**

| Parameter                  | Symbol                                  | Values |       | Units |        |
|----------------------------|---|--------|-------|-------|--------|
|                            |   | Min    | Туре  | Max   |        |
| Contrast ratio             | CR                                      | 1000   | 1400  | -     |        |
| Surface luminance, white   | LwH                                     | 1350   | 1500  | -     | cd/m2  |
| White luminance uniformity | ΔΥ                                      | 80     | 85    | -     | %      |
| Response time              | G to G                                  | -      | 5.5   | -     | ms     |
| Color Temperature          |   |        | 10000 |       | K      |
| Color Gamut(NTSC %)        |   |        | 72    |       | %      |
| Viewing angle (by CR >10)  | x axis, right( $\Phi$ =0°)              | -      | 89    | -     | degree |
|                            | x axis, left ( $\phi$ =180 $^{\circ}$ ) | -      | 89    | -     |        |
|                            | y axis, up ( $\Phi$ =90 $^{\circ}$ )    | -      | 89    | -     |        |
|                            | y axis, down ( $\phi$ =270 $^{\circ}$ ) | -      | 89    | -     |        |

#### Notes:

- 1.Contrast Ratio(CR) is defined mathematically as :
  - CR = Surface Luminance at all white pixels / Surface Luminance at all black pixels It is measured at center 1-point.
- 2.Surface luminance is determined after the unit has been 'ON' and 1Hourafterlighting the backlight in a dark environment at  $25\pm2^{\circ}$  C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying L255 white.
- 3. The White luminance uniformity on LCD surface is then expressed as :
  - $\Delta Y = (Minimum Luminance of 9points / Maximum Luminance of 9points) * 100$





## **Product Specification**

#### 5. Mechanical Characteristics

Table 7. provides general mechanical characteristics for the model BR550D15. Please refer to Figure 15,16 regarding the detailed mechanical drawing of the LCD.

**Table 7. Mechanical characteristics** 

|                     | Horizontal                               | 1286.0mm |  |  |  |
|---------------------|--|----------|--|--|--|
| Outside dimensions  | Vertical                                 | 745.0mm  |  |  |  |
|                     | Depth                                    | 60.0mm   |  |  |  |
| Active diapley area | Horizontal                               | 1209.6mm |  |  |  |
| Active display area | Vertical 680.4mm                         |          |  |  |  |
| Weight(approximate) | 19.5Kg(Typ.), 21Kg(Max.)                 |          |  |  |  |
| Surface Treatment   | Hard coating(3H) Anti-glare treatment of |          |  |  |  |
| Surface Treatment   | the front polarizer                      |          |  |  |  |