

CentralTM Semiconductor Corp.

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Manufacturers of World Class Discrete Semiconductors

BSS50
BSS51
BSS52

NPN SILICON
DARLINGTON TRANSISTORS

JEDEC TO-39 CASE

DESCRIPTION

The CENTRAL SEMICONDUCTOR BSS50 series types are NPN Silicon Darlington Transistors designed for industrial switching applications.

MAXIMUM RATINGS ($T_A=25^\circ\text{C}$)

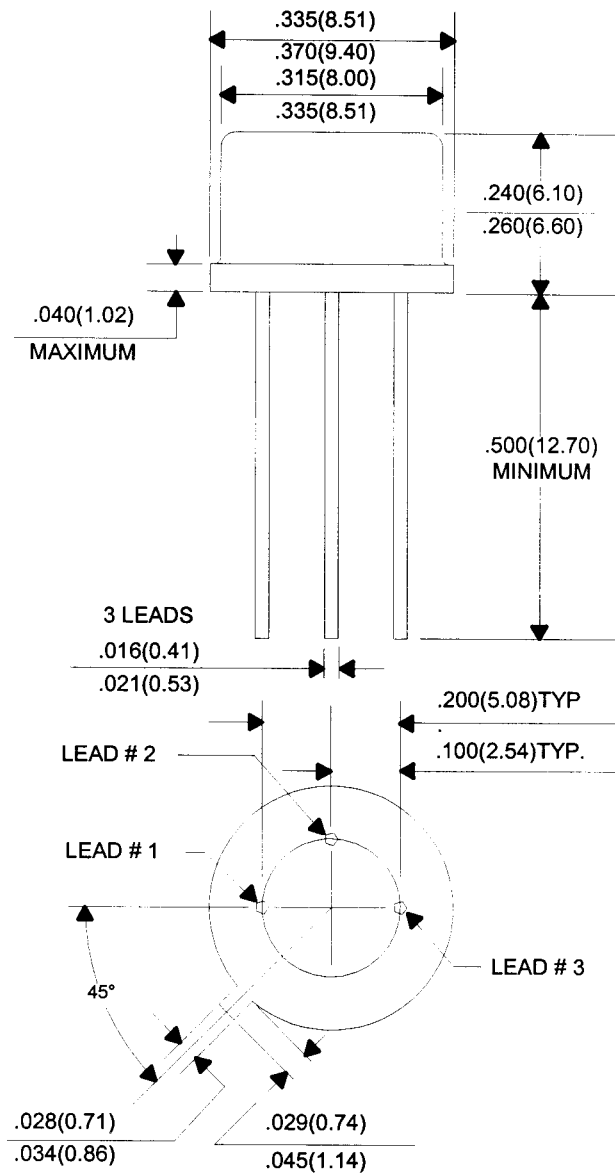
	SYMBOL	BSS50	BSS51	BSS52	UNITS
Collector-Base Voltage	V_{CBO}	60	80	90	V
Collector-Emitter Voltage	V_{CER}	45	60	80	V
Emitter-Base Voltage	V_{EBO}		5.0		V
Collector Current	I_C		1.0		A
Collector Current (Peak)	I_{CM}		2.0		A
Base Current (Peak)	I_{BM}		100		mA
Power Dissipation	P_D		0.8		W
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D		5.0		W
Operating and Storage					
Junction Temperature	T_J, T_{stg}		-65 to +200		$^\circ\text{C}$
Thermal Resistance	Θ_{JA}		219		$^\circ\text{C/W}$
Thermal Resistance	Θ_{JC}		35		$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	BSS50		BSS51		BSS52		UNITS
		MIN	MAX	MIN	MAX	MIN	MAX	
I_{CBO}	$V_{CB}=\text{Rated } V_{CER}$		50		50		50	nA
I_{EBO}	$V_{EB}=4.0\text{V}$		50		50		50	nA
$V_{CE}(\text{SAT})$	$I_C=500\text{mA}, I_B=500\mu\text{A}$		1.3		1.3		1.3	V
$V_{CE}(\text{SAT})$	$I_C=500\text{mA}, I_B=500\mu\text{A}, T_A=200^\circ\text{C}$		1.3		1.3		1.3	V
$V_{CE}(\text{SAT})$	$I_C=1.0\text{A}, I_B=1.0\text{mA}$		-		1.6		-	V
$V_{CE}(\text{SAT})$	$I_C=1.0\text{A}, I_B=1.0\text{mA}, T_A=200^\circ\text{C}$		-		2.3		-	V
$V_{CE}(\text{SAT})$	$I_C=1.0\text{A}, I_B=4.0\text{mA}$		1.6		-		1.6	V
$V_{CE}(\text{SAT})$	$I_C=1.0\text{A}, I_B=4.0\text{mA}, T_A=200^\circ\text{C}$		1.6		-		1.6	V
$V_{BE}(\text{SAT})$	$I_C=500\text{mA}, I_B=500\mu\text{A}$		1.9		1.9		1.9	V
$V_{BE}(\text{SAT})$	$I_C=1.0\text{A}, I_B=1.0\text{mA}$		-		2.2		-	V
$V_{BE}(\text{SAT})$	$I_C=1.0\text{A}, I_B=4.0\text{mA}$		2.2		-		2.2	V
$V_{BE}(\text{ON})$	$V_{CE}=10\text{V}, I_C=150\text{mA}$	1.3	1.65	1.3	1.65	1.3	1.65	V
$V_{BE}(\text{ON})$	$V_{CE}=10\text{V}, I_C=500\text{mA}$	1.4	1.75	1.4	1.75	1.4	1.75	V
h_{FE}	$V_{CE}=10\text{V}, I_C=150\text{mA}$	1K		1K		1K		
h_{FE}	$V_{CE}=10\text{V}, I_C=500\text{mA}$	2K		2K		2K		
h_{fe}	$V_{CE}=5.0\text{V}, I_C=500\text{mA}, f=35\text{MHz}$	10 TYP		10 TYP		10 TYP		
t_{on}	$I_C=500\text{mA}, I_{B1}=I_{B2}=0.5\text{mA}$	0.4 TYP		0.4 TYP		0.4 TYP		μs
t_{off}	$I_C=500\text{mA}, I_{B1}=I_{B2}=0.5\text{mA}$	1.5 TYP		1.5 TYP		1.5 TYP		μs
t_{on}	$I_C=1.0\text{A}, I_{B1}=I_{B2}=1.0\text{mA}$	0.4 TYP		0.4 TYP		0.4 TYP		μs
t_{off}	$I_C=1.0\text{A}, I_{B1}=I_{B2}=1.0\text{mA}$	1.5 TYP		1.5 TYP		1.5 TYP		μs

(See Reverse Side)

JEDEC TO-39 CASE - MECHANICAL OUTLINE



All Dimensions in Inches (mm).

Lead Code:

1. Emitter
2. Base
3. Collector

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