

High-voltage, high-current switch

The BSX 33 is an NPN silicon planar epitaxial transistor designed for high voltage and high current switching applications. It features a useful current gain range from 100 μ A to 500 mA and a low saturation voltage allowing switching operation at 1 A.

ELECTRICAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Characteristic and test conditions	Min.	Typ.	Max.	Unit
hFE	DC Current Gain (5)				
	$I_C = 100 \mu\text{A}$	20	50		
	$I_C = 5 \text{ mA}$	50	85		
	$I_C = 50 \text{ mA}$	50	95		
	$I_C = 150 \text{ mA}$	40	80		
VBEsat	Base Saturation Voltage (5)				
	$I_C = 50 \text{ mA}$	0.76			V
	$I_C = 150 \text{ mA}$	0.85	1.1		V
	$I_C = 1 \text{ A}$	1.2	1.6		V
VCEsat	Collector Saturation Voltage (5)				
	$I_C = 50 \text{ mA}$	0.08			V
	$I_C = 150 \text{ mA}$	0.15	0.30		V
	$I_C = 1 \text{ A}$	0.6	1		V
IEBO	Emitter Reverse Current $V_{EB} = 5 \text{ V}$	IC = 0	0.1	10	nA
ICBO	Collector Reverse Current $V_{CB} = 60 \text{ V}$	IE = 0	0.2	10	nA
	$(T = 150^\circ\text{C})$	IE = 0	0.2	10	μA
BVCEO	Collector to Base Breakdown Voltage $I_C = 100 \mu\text{A}$	IE = 0	85		V
BVEBO	Emitter to Base Breakdown Voltage $I_E = 100 \mu\text{A}$	IC = 0	7		V
LVCEO	Collector to Emitter Sustaining Voltage (4 and 5)	IE = 0	55		V
hfe	Small Signal Current Gain $I_C = 1 \text{ mA}$	$V_{CE} = 5 \text{ V}$	$f = 1 \text{ kHz}$	85	
	Input Resistance $I_C = 1 \text{ mA}$	$V_{CE} = 5 \text{ V}$	$f = 1 \text{ kHz}$	2	K Ω
hoe	Output Conductance $I_C = 1 \text{ mA}$	$V_{CE} = 5 \text{ V}$	$f = 1 \text{ kHz}$	8	μmho
hre	Voltage Feedback Ratio $I_C = 1 \text{ mA}$	$V_{CE} = 5 \text{ V}$	$f = 1 \text{ kHz}$	2.2	$\times 10^{-4}$
hfe	High Freq. Current Gain $I_C = 50 \text{ mA}$	$V_{CE} = 10 \text{ V}$	$f = 20 \text{ MHz}$	3	4.5
	Emitter Transition Capacitance $I_C = 0$	$V_{EB} = 0.5 \text{ V}$		50	80
Cobo	Base-Collector Capacitance $I_E = 0$	$V_{CB} = 10 \text{ V}$		12	20
	Turn On Time $I_C = 150 \text{ mA}$	$I_{B1} = 7.5 \text{ mA}$		120	200
toff	Turn Off Time $I_C = 150 \text{ mA}$	$I_{B1} = 7.5 \text{ mA}$		350	800

NOTES:

- These ratings are limiting values above which the serviceability of any individual semiconductor device may be impaired.
- These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- These ratings give a maximum junction temperature of 200°C and junction-to-case thermal resistance of 93°C/W (derating factor of $10.3 \text{ mW}/^\circ\text{C}$); junction-to-ambient thermal resistance of 350°C/W (derating factor of $2.85 \text{ mW}/^\circ\text{C}$).
- These ratings refer to a high-current point where collector-to-emitter voltage is lowest. For more information send for SGS AR 5.
- Measured under pulse conditions: pulse length $\approx 300 \mu\text{sec}$; duty cycle 1%.

ABSOLUTE MAXIMUM RATINGS (1)

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Voltages

Collector to Base	V_{CBO}	85 V
Collector to Emitter (4)	V_{CEO}	55 V
Emitter to Base	V_{EBO}	7 V

Temperatures

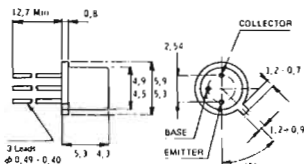
Storage Temperature Range	T_{STG}	-55°C to 200°C
Operating Junction Temperature	T_J	200°C
Lead Temperature (Soldering, 10 sec.)	T_L	260°C

Power (2-3)

Dissipation at 25°C Case Temperature	P_D	1.8 W
Dissipation at 25°C Ambient Temperature	P_D	0.5 W

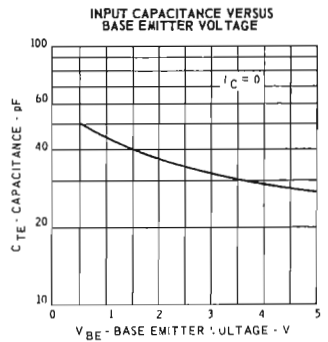
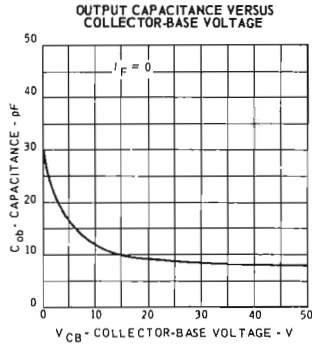
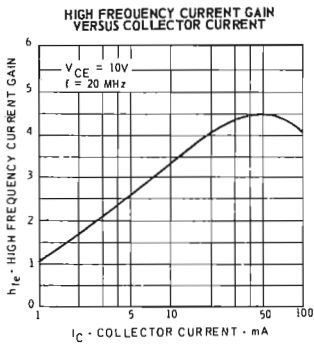
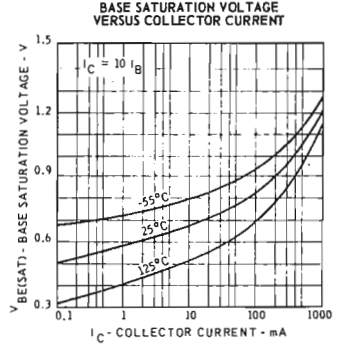
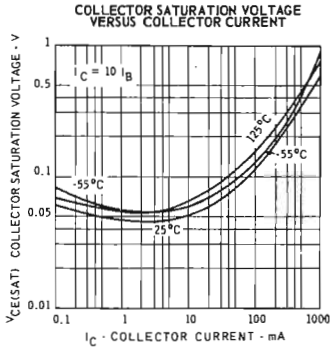
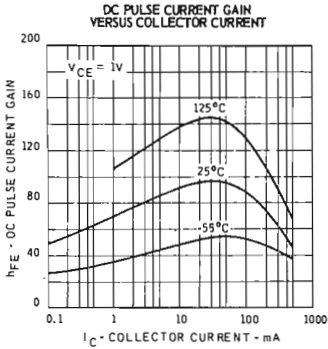
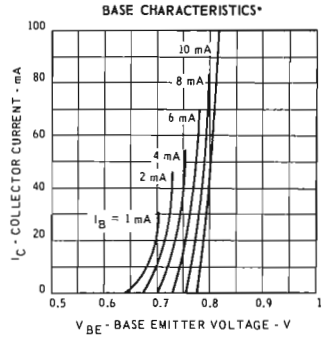
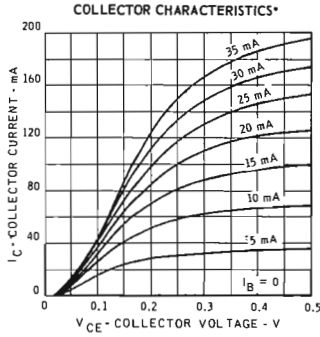
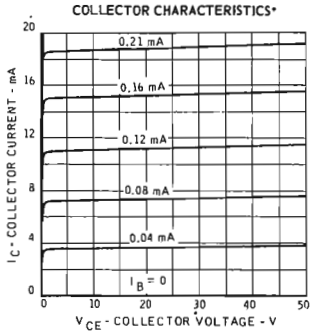
PHYSICAL DIMENSIONS

Similar to JEDEC TO-18



Note: All dimensions are in mm.

TYPICAL ELECTRICAL CHARACTERISTICS (25° C free air temperature unless otherwise noted)



* Single family characteristics on Transistor Curve Tracer.

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