

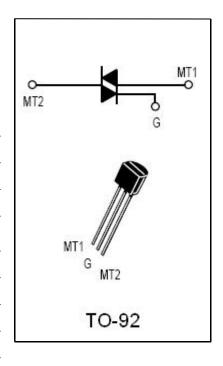
isc Triacs BT131-500

FEATURES

- With TO-92 package
- Glass passivated, sensitive gate triacs in a plastic envelope
- Intended for use in general purpose bidirectional switching and phase control applications.
- These devices are intended to be interfaced directly to microcontrollers,logic intergrated circuits and other low power gate trigger circuit.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	SYMBOL	PARAMETER	MIN	UNIT
	V _{DRM}	Repetitive peak off-state voltage	500	V
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	V _{RRM}	Repetitive peak off-state voltage	500	V
P _{GM} Peak gate power dissipation 5 W	I _{T(RMS)}		1	Α
	I _{TSM}	Non-repetitive peak on-state current	16	Α
P _{G(AV)} Average gate power dissipation 0.5 W	P _{GM}	Peak gate power dissipation	5	W
	P _{G(AV)}	Average gate power dissipation	0.5	W
T _j Operating junction temperature 110 °C	Tj	Operating junction temperature	110	°C
T _{stg} Storage temperature -40~150 °C	T _{stg}	Storage temperature	-40~150	$^{\circ}\!\mathbb{C}$



ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)

SYMBOL	PARAMETE	R	CONDITIONS	MIN	MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage		I _D =1mA	500		V
V_{RRM}	Repetitive peak reverse voltage		I _D =0.5mA	500		V
I_{D}	Off-state leakage current		$V_D = V_{DRM(max)}, T_j = 125^{\circ}C$		0.5	mA
I _{GT}	Gate trigger current	I	- - V _D =12V; I _T = 0.1A		3	
		II			3	mA
		III			3	IIIA
		IV			8	
V _{TM}	On-state voltage		I _T =2.0A		1.5	V
I _H	Holding current		I _{GT} =0.1A ,V _D = 12V		5	mA
V _{GT}	Gate trigger voltage		V_D =12V ; R_L =100 Ω all quadrant		1.5	V



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