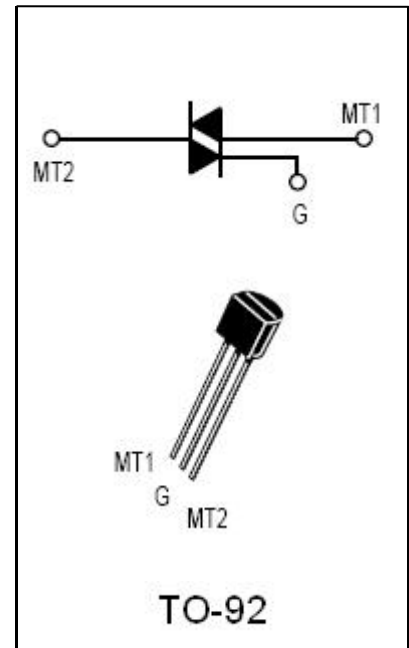


FEATURES

- With TO-92 package
- Glass passivated, sensitive gate triacs in a plastic envelope
- Intended for use in general purpose bidirectional switching and phase control applications.
- These devices are intended to be interfaced directly to microcontrollers, logic intergrated circuits and other low power gate trigger circuit.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	600	V
V _{RRM}	Repetitive peak off-state voltage	600	V
I _{T(RMS)}	RMS on-state current (full sine wave) T _{lead} ≤ 51°C	1	A
I _{TSM}	Non-repetitive peak on-state current	16	A
P _{GM}	Peak gate power dissipation	5	W
P _{G(AV)}	Average gate power dissipation	0.5	W
T _j	Operating junction temperature	110	°C
T _{stg}	Storage temperature	-45~150	°C

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{DRM}	Repetitive peak off-state voltage	I _D =0.1mA	600		V
V _{RRM}	Repetitive peak reverse voltage	I _D =0.5mA	600		V
I _D	Off-state leakage current	V _D = V _{DRM(max)} , T _j = 125°C		0.5	mA
I _{GT}	Gate trigger current	V _D =12V; I _T = 0.1A	I	10	mA
			II	10	
			III	10	
			IV	25	



V_{TM}	On-state voltage	$I_T=1.6A$		1.6	V
I_H	Holding current	$I_{GT}=0.1A, V_D=12V$		5	mA
V_{GT}	Gate trigger voltage	$V_D=12V; R_L=100\Omega$ all quadrant		1.5	V

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