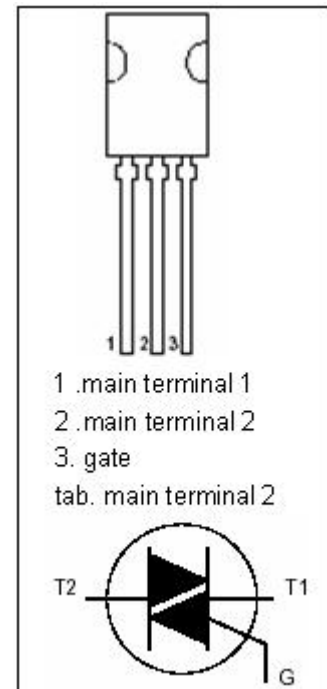


**FEATURES**

- With TO-126P package
- Designed for use in general purpose bidirectional switching and phase control applications , which are intended to be interfaced directly to microcontrollers , logic integrated circuits and other low power gate trigger circuits.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	600	V
V <sub>RRM</sub>	Repetitive peak off-state voltage	600	V
I <sub>T(RMS)</sub>	RMS on-state current (full sine wave)	4	A
I <sub>TSM</sub>	Non-repetitive peak on-state current	25	A
P <sub>GM</sub>	Peak gate power dissipation	5	W
P <sub>G(AV)</sub>	Average gate power dissipation	0.5	W
T <sub>j</sub>	Operating junction temperature	110	°C
T <sub>stg</sub>	Storage temperature	-45~150	°C


**ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> , V <sub>R</sub> =V <sub>RRM</sub> , T <sub>J</sub> =125°C		0.01 0.2	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>D</sub> =V <sub>DRM</sub> , V <sub>D</sub> =V <sub>DRM</sub> , T <sub>J</sub> =125°C		0.01 0.2	mA
I <sub>GT</sub>	Gate trigger current	V <sub>D</sub> =12V; I <sub>T</sub> = 0.1A	I	6	mA
			II	6	
			III	6	
			IV	10	
V <sub>TM</sub>	On-state voltage	I <sub>T</sub> = 4A		1.7	V
I <sub>H</sub>	Holding current	I <sub>GT</sub> = 0.1A, V <sub>D</sub> = 12V		8	mA
V <sub>GT</sub>	Gate trigger voltage	V <sub>D</sub> =12V; I <sub>T</sub> = 0.1A		1.5	V

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