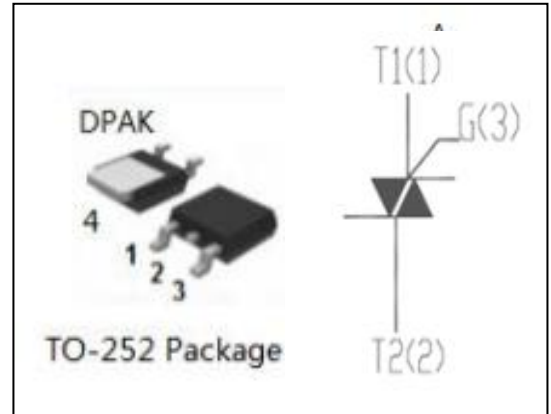


**isc Thyristors**
**BT136S-600E**
**DESCRIPTION**

- With TO-252( DPAK ) packaging
- Operating in 4 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Switching applications
- Phase control
- Static switching on inductive or resistive load


**ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )**

SYMBOL	PARAMETER	MAX	UNIT
$V_{\text{DRM}}$	Repetitive peak off-state voltage	600	V
$V_{\text{RRM}}$	Repetitive peak reverse voltage	600	V
$I_{\text{T(RSM)}}$	Average on-state current	4	A
$I_{\text{TSM}}$	Surge non-repetitive on-state current	25 27	A
$P_{\text{G(AV)}}$	Average gate power dissipation ( over any 20 ms period )	0.5	W
$T_j$	Operating junction temperature	-40~125	$^{\circ}\text{C}$
$T_{\text{stg}}$	Storage temperature	-40~150	$^{\circ}\text{C}$

**ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$  unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
$I_{RRM}$	Repetitive peak reverse current	$V_R=V_{RRM}$ Rated; $V_D=V_{DRM}$ Rated;	$T_j=125^\circ\text{C}$		0.5	mA
$I_{DRM}$	Repetitive peak off-state current					
$V_{TM}$	On-state voltage	$I_T=5\text{A}$			1.7	V
$I_{GT}$	Gate-trigger current	$V_D=12\text{V}; I_T=0.1\text{A};$	I		35	mA
			II		35	
			III		35	
			IV		70	
$V_{GT}$	Gate-trigger voltage	$V_D=12\text{V}; I_T=0.1\text{A};$			1.5	V
$R_{th(j-mb)}$	Junction to mounting base	Half cycle			3.7	$^\circ\text{C/W}$

**NOTICE:**

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