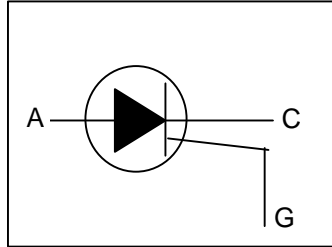
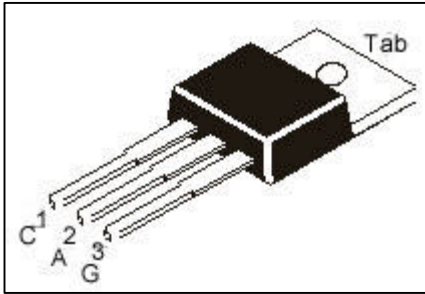


## THYRISTORS

**BT151**



**TO-220  
Plastic Package**

**For use in Applications Requiring high Bidirectional Blocking Voltage Capability and high Thermal Cycling Performance. Typical Applications include Motor Control, Industrial and Domestic Lighting, Heating and Static Switching**

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITION	VALUE		UNIT
			500	650	
		<b>BT151-</b>	<b>500</b>	<b>650</b>	
Repetitive Peak Off State Voltage	$V_{DRM}, V_{RRM}$		*500	*650	V
Average On State Current	$I_{T(AV)}$	half sine wave, $T_{mb} \leq 109^{\circ}C$	7.5		A
RMS On State Current	$I_{T(RMS)}$	all conduction angles	12		A
Non Repetitive Peak On State Current	$I_{TSM}$	half sine wave, $T_j=25^{\circ}C$ prior to surge $t=10ms$	100		A
		$t=8.3ms$	110		A
$I^2t$ for Fusing	$I^2t$	$t=10ms$	50		$A^2s$
Repetitive Rate of Rise of On State Current After Triggering	$di_T/dt$	$I_{TM}=20A, I_G=50mA,$ $di_G/dt=50mA/\mu s$	50		$A/\mu s$
Peak Gate Current	$I_{GM}$		2.0		A
Peak Gate Voltage	$V_{GM}$		5.0		V
Peak Reverse Gate Voltage	$V_{RGM}$		5.0		V
Peak Gate Power	$P_{GM}$		5.0		W
Average Gate Power	$P_{G(AV)}$	Over any 20ms period	0.5		W
Storage Temperature	$T_{stg}$		- 40 to +150		$^{\circ}C$
Operating Junction Temperature	$T_j$		125		$^{\circ}C$

### THERMAL RESISTANCE

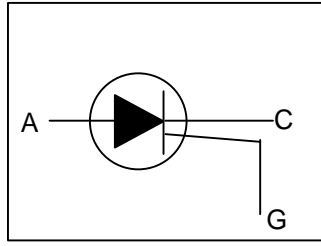
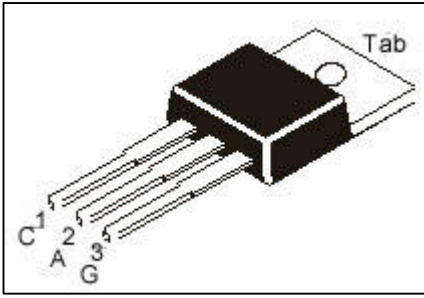
Junction to Mounting Base	$R_{th(j-mb)}$		1.3 max	K/W
Junction to Ambient	$R_{th(j-a)}$	in free air	60 typ	K/W

**\*Although not recommended, off state voltage upto 800V may be applied without damage, but the thyristor may switch to the on state. The rate of rise of current should not exceed 15A/ms**

# THYRISTORS

**BT151**

**TO-220  
Plastic Package**



## ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Gate Trigger Current	I <sub>GT</sub>	V <sub>D</sub> =12V, I <sub>T</sub> =0.1A		15	mA
Latching Current	I <sub>L</sub>	V <sub>D</sub> =12V, I <sub>GT</sub> =0.1A		40	mA
Holding Current	I <sub>H</sub>	V <sub>D</sub> =12V, I <sub>GT</sub> =0.1A		20	mA
On State Voltage	V <sub>T</sub>	I <sub>T</sub> =23A		1.75	V
Gate Trigger Voltage	V <sub>GT</sub>	V <sub>D</sub> =12V, I <sub>T</sub> =0.1A V <sub>D</sub> =V <sub>DRM</sub> (max), I <sub>T</sub> =0.1A, T <sub>J</sub> =125°C	0.25	1.5	V
Off State Leakage Current	I <sub>D</sub> , I <sub>R</sub>	V <sub>D</sub> = V <sub>DRM</sub> (max), V <sub>R</sub> =V <sub>RRM</sub> (max) T <sub>J</sub> =125°C		0.5	mA

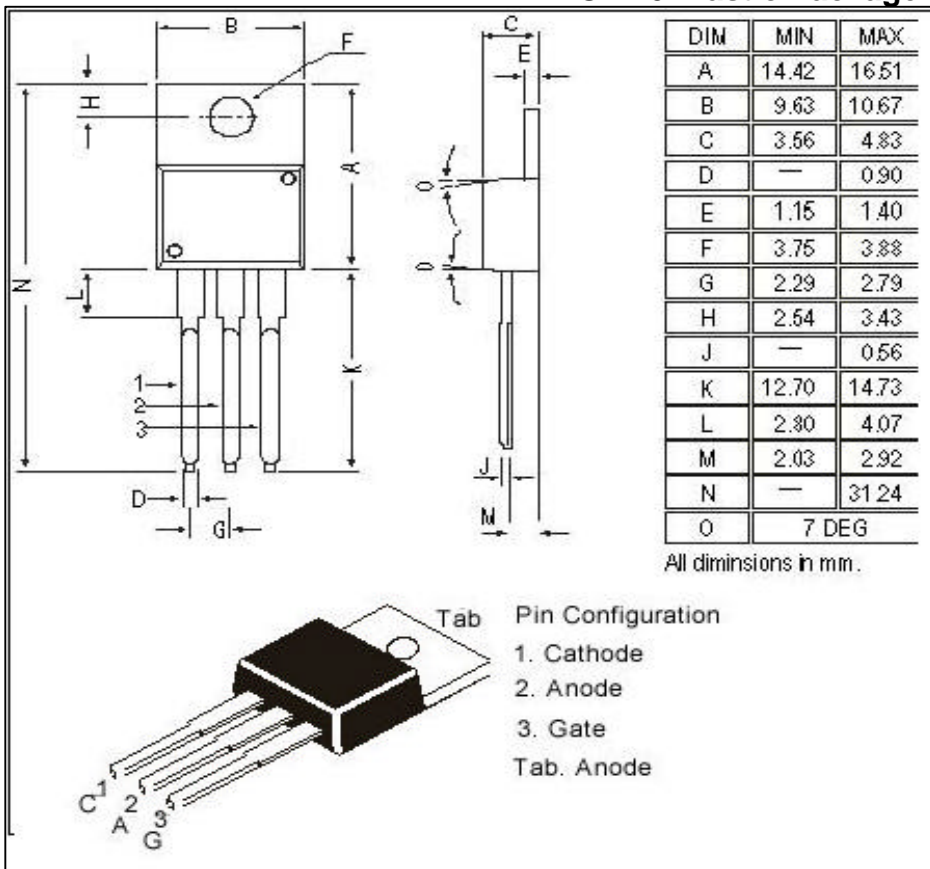
## DYNAMIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Critical Rate of Rise of Off State Voltage	dV <sub>D</sub> /dt	V <sub>DM</sub> =67% V <sub>DRM</sub> (max), T <sub>J</sub> =125°C, exponential waveform gate open circuit R <sub>GK</sub> =100Ω	50 200			V/μs V/μs
Gate Controlled Turn On time	t <sub>gt</sub>	I <sub>TM</sub> =40A, V <sub>D</sub> =V <sub>DRM</sub> (max), I <sub>G</sub> =0.1A, dI <sub>G</sub> /dt=5A/μs		2.0		μs
Circuit Commutated Turn Off time	t <sub>q</sub>	V <sub>D</sub> =67% V <sub>DRM</sub> (max), T <sub>J</sub> =125°C, I <sub>TM</sub> =20A, V <sub>R</sub> =25V, dI <sub>TM</sub> /dt=30A/μs, dV <sub>D</sub> /dt=50V/μs, R <sub>GK</sub> =100Ω		70		μs

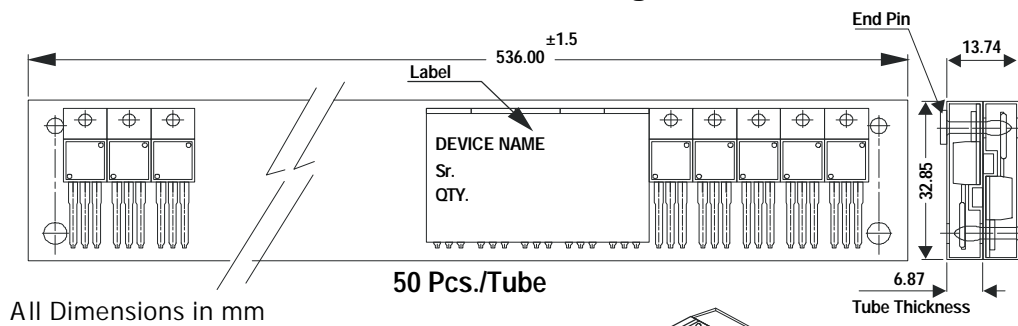
<b>Marking</b>	<b>BT151-500</b>	<b>BT151-650</b>
	<b>CDXX</b>	<b>CDXX</b>
	<b>BT151</b>	<b>BT151</b>
	<b>- 500</b>	<b>- 650</b>
<b>XX=Date Code</b>		

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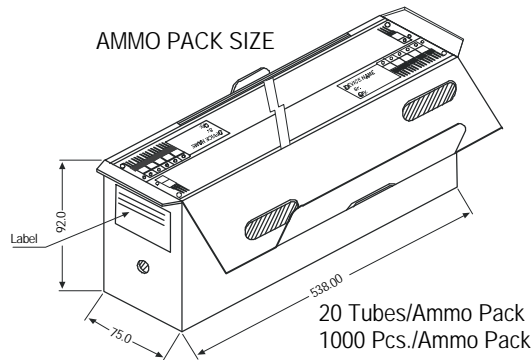
TO-220 Plastic Package



TO-220 Tube Packing



AMMO PACK SIZE



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220 /FP	200 pcs/polybag	396 gm/200 pcs	3" x 7.5" x 7.5"	1.0K	17" x 15" x 13.5"	16.0K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5" x 3.7" x 21.5"	1.0K	19" x 19" x 19"	10.0K	29 kgs

### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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