# **BT152 series**

### GENERAL DESCRIPTION

Glass passivated thyristors in a plastic envelope, intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

## **PINNING - TO220AB**

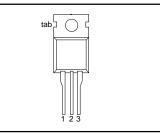
## QUICK REFERENCE DATA

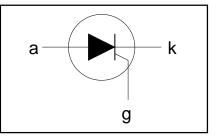
| SYMBOL                                 | PARAMETER                               | MAX. | MAX. | MAX. | UNIT |
|--|---|------|------|------|------|
|  | BT152-                                  | 500R | 600R | 800R |      |
| V <sub>DRM</sub> ,                     | Repetitive peak off-state<br>voltages   | 500  | 600  | 800  | V    |
| V <sub>RRM</sub><br>I <sub>T(AV)</sub> | Average on-state current                | 13   | 13   | 13   | Α    |
| I <sub>T(RMS)</sub>                    | RMS on-state current                    | 20   | 20   | 20   | Α    |
| I <sub>TSM</sub>                       | Non-repetitive peak on-state<br>current | 200  | 200  | 200  | A    |

#### **PIN CONFIGURATION**

#### SYMBOL

| PIN | DESCRIPTION |  |  |
|-----|-------------|--|--|
| 1   | cathode     |  |  |
| 2   | anode       |  |  |
| 3   | gate        |  |  |
| tab | anode       |  |  |





#### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

| SYMBOL  | PARAMETER   | CONDITIONS  | MIN.          | MAX.                             |                                  | UNIT                |                  |
|---|---|---|---------------|----------------------------------|----------------------------------|---------------------|------------------|
| V <sub>drm</sub>  | Repetitive peak off-state voltages  |   | -             | <b>-500R</b><br>500 <sup>1</sup> | <b>-600R</b><br>600 <sup>1</sup> | <b>-800R</b><br>800 | V                |
| I <sub>T(AV)</sub><br>I <sub>T(RMS)</sub><br>I <sub>TSM</sub> | Average on-state current<br>RMS on-state current<br>Non-repetitive peak<br>on-state current | half sine wave; $T_{mb} \le 103 \degree C$<br>all conduction angles<br>half sine wave; $T_j = 125 \degree C$ prior<br>to surge; with reapplied $V_{DRM(max)}$ | -             |                                  | 13<br>20                         |                     | A<br>A           |
|   |   | t = 10  ms<br>t = 8.3  ms   | -             |                                  | 200<br>220                       |                     | A<br>A           |
| l <sup>2</sup> tl   | <sup>2</sup> t for fusing   | t = 10  ms  | -             |                                  | 200                              |                     | A <sup>2</sup> s |
| dl <sub>⊤</sub> /dt   | Repetitive rate of rise of on-state current after triggering                                | $I_{TM} = 50 \text{ A}; I_G = 0.2 \text{ A}; \\ dI_G/dt = 0.2 \text{ A}/\mu s$  | -             |                                  | 200                              |                     | A/μs             |
| I <sub>GM</sub>   | Peak gate current   |   | -             |                                  | 5                                |                     | A                |
| I <sub>GM</sub><br>V <sub>GM</sub>                            | Peak gate voltage   |   | -             |                                  | 5<br>5                           |                     | V                |
| V <sub>RGM</sub>  | Peak reverse gate voltage   |   | -             |                                  |                                  |                     | V                |
| I P <sub>GM</sub>   | Peak gate power   |   | -             |                                  | 20                               |                     | W                |
| P <sub>G(AV)</sub><br>T <sub>stg</sub><br>T <sub>j</sub>      | Average gate power<br>Storage temperature<br>Operating junction<br>temperature              | over any 20 ms period   | -<br>-40<br>- |                                  | 0.5<br>150<br>125                |                     | ວໍລໍ&            |

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 A/ $\mu$ s.

# BT152 series

## THERMAL RESISTANCES

| SYMBOL               | PARAMETER                                    | CONDITIONS  | MIN. | TYP. | MAX. | UNIT |
|----------------------|--|-------------|------|------|------|------|
| R <sub>th j-mb</sub> | Thermal resistance junction to mounting base |             | -    | -    | 1.1  | K/W  |
| R <sub>th j-a</sub>  | Thermal resistance<br>junction to ambient    | in free air | -    | 60   | -    | K/W  |

## STATIC CHARACTERISTICS

 $T_j = 25$  °C unless otherwise stated

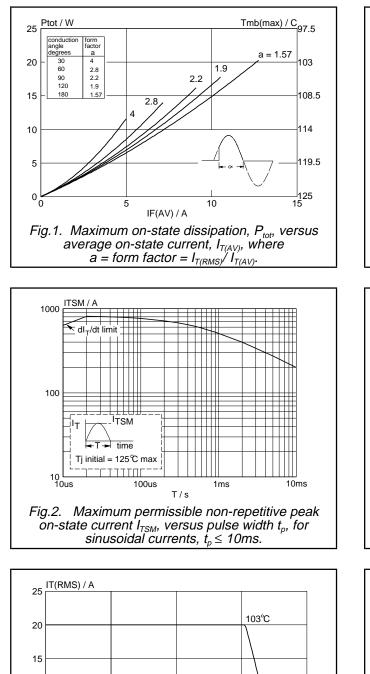
| SYMBOL                          | PARAMETER                 | CONDITIONS  | MIN. | TYP. | MAX. | UNIT |
|---------------------------------|---------------------------|---|------|------|------|------|
| I <sub>GT</sub>                 | Gate trigger current      | $V_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$                       | -    | 3    | 32   | mA   |
|                                 | Latching current          | $V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$                      | -    | 25   | 80   | mA   |
|                                 | Holding current           | $V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$                      | -    | 15   | 60   | mA   |
| İΫ <sub>T</sub>                 | On-state voltage          | $I_{T} = 40 \text{ A}$  | -    | 1.4  | 1.75 | V    |
| V <sub>GT</sub>                 | Gate trigger voltage      | $\dot{V}_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$                 | -    | 0.6  | 1.5  | V    |
| 0.                              |                           | $V_{D} = V_{DRM(max)}$ ; $I_{T} = 0.1 \text{ A}$ ; $T_{j} = 125 \text{ °C}$ | 0.25 | 0.4  | -    | V    |
| I <sub>D</sub> , I <sub>R</sub> | Off-state leakage current | $V_D = V_{DRM(max)}; V_R = V_{RRM(max)}; T_j = 125 \text{°C}$               | -    | 0.2  | 1.0  | mA   |

## **DYNAMIC CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise stated

| SYMBOL              | PARAMETER                                  | CONDITIONS   | MIN. | TYP. | MAX. | UNIT |
|---------------------|--|--|------|------|------|------|
| dV <sub>D</sub> /dt | Critical rate of rise of off-state voltage | $V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$<br>exponential waveform gate open circuit  | 200  | 300  | -    | V/µs |
| t <sub>gt</sub>     | Gate controlled turn-on                    | $V_D = V_{DRM(max)}$ ; $I_G = 0.1$ Å; $dI_G/dt = 5$ Å/µs;<br>$I_{TM} = 40$ Å   | -    | 2    | -    | μs   |
| t <sub>q</sub>      | Circuit commutated turn-off time           | $V_{D}^{V} = 67\% V_{DRM(max)}; T_{j} = 125 °C;$<br>$I_{TM} = 50 A; V_{R} = 25 V; dI_{TM}/dt = 30 A/\mu s;$<br>$dV_{D}/dt = 50 V/\mu s; R_{GK} = 100 \Omega$ | -    | 70   | -    | μs   |

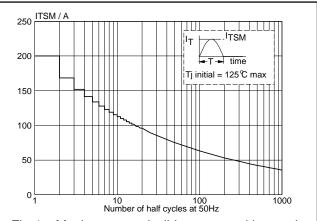
## BT152 series

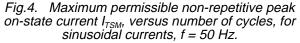


50 Tmb / C

Fig.3. Maximum permissible rms current  $I_{T(RMS)}$ , versus mounting base temperature  $T_{mb}$ .

100





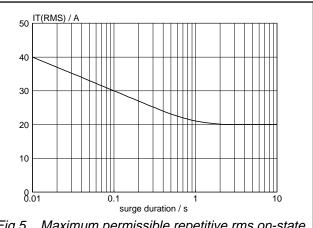
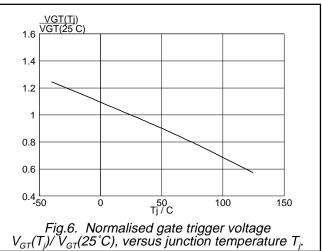


Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents, f = 50 Hz;  $T_{mb} \le 103$  °C.



10

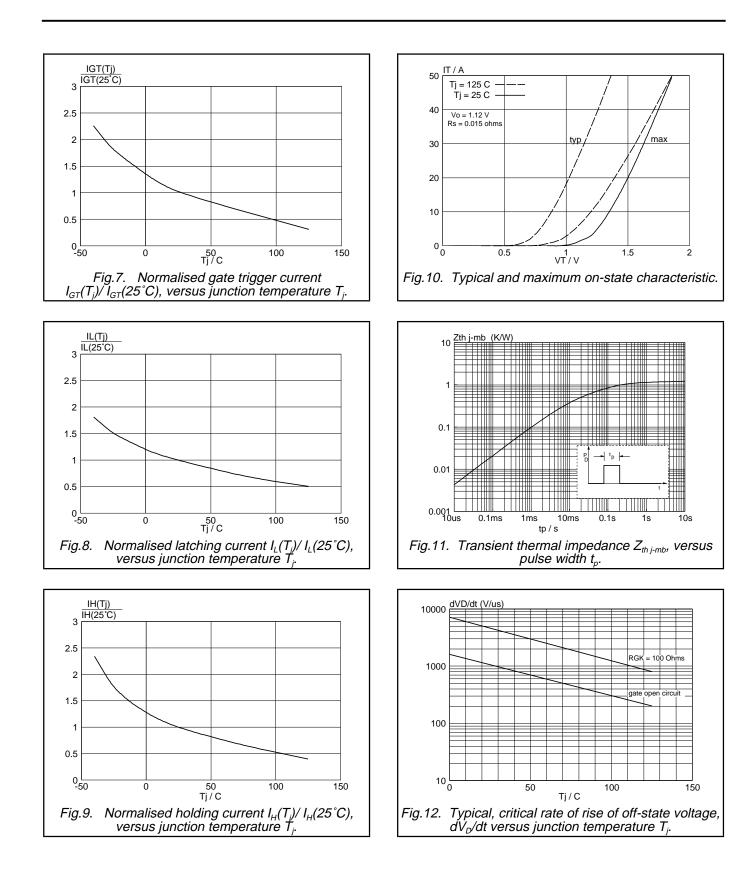
5

0 -50

0

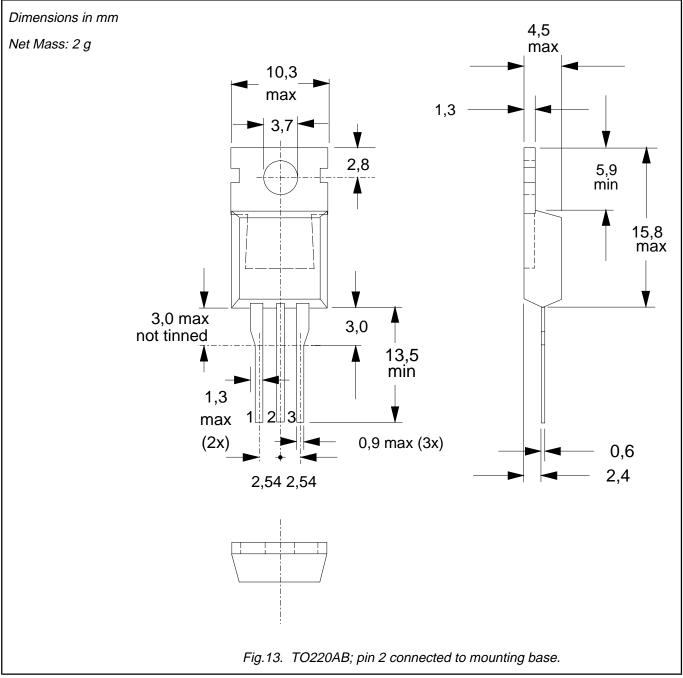
150

# BT152 series



## BT152 series

#### **MECHANICAL DATA**



#### Notes

Accessories supplied on request: refer to mounting instructions for TO220 envelopes.
Epoxy meets UL94 V0 at 1/8".

## DEFINITIONS

| Data sheet status  |   |  |  |  |  |
|--|---|--|--|--|--|
| Objective specification  | Objective specification This data sheet contains target or goal specifications for product development. |  |  |  |  |
| Preliminary specification  | ation This data sheet contains preliminary data; supplementary data may be published later.             |  |  |  |  |
| Product specification  | This data sheet contains final product specifications.  |  |  |  |  |
| Limiting values  |   |  |  |  |  |
| Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability. |   |  |  |  |  |
| Application information  |   |  |  |  |  |
| Where application information is given, it is advisory and does not form part of the specification.  |   |  |  |  |  |
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