

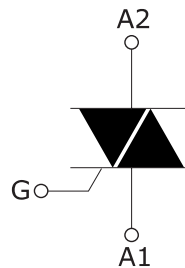
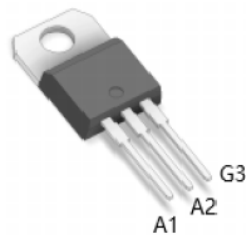
1.Description

NPNN five-layer structure of silicon bidirectional devices; with independent intellectual property rights of single-sided digging technology, table glass passivation process; multi-layer metallized electrodes on the back; with high blocking voltage and high temperature stability.

2.Features

vacuum cleaners, power tools and other motor speed controllers; solid state relays; heating controllers (temperature regulation); other phase control circuits.

3.Pinning Information



TO-220A



4. Absolute maximum ratings ($T_J=25^{\circ}\text{C}$ unless otherwise stated)

Parameter			Symbol	Values	Units
RMS on-state current (full sine wave)	BTA	T _C =80°C	I _{T(RMS)}	24	A
Non repetitive surge peak on-state current (full cycle, T _J initial=25°C)	F=50HZ, tp=20ms		I _{TSM}	240	A
I ² t value for fusing	tp=10ms		I ² t	288	A ² S
Critical rate of rise of on-state current I _G =2 x I _{GT} , tr≤100ns	T _J =125°C		di/dt	50	A/us
Off state repetitive peak voltage Reverse repetitive peak voltage	T _J =25°C		V _{DRM} /V _{RRM}	800	V
Peak gate current	tp=20us	T _J =150°C	I _{GM}	4	A
Average gate power dissipation		T _J =150°C	P _{G(AV)}	1	W
Storage junction temperature range			T _{STG}	-40 to 150	°C
Operating junction temperature range			T _J	-40 to 125	°C



5.1 Electrical characteristics (3 quadrants)

Parameter	Quadrant	Range	Symbol	Values		Units
$V_D=12V$ $R_L=100\Omega$	I	MAX	I_{GT}	≤ 50		mA
	II	MAX	V_{GT}	1.5		V
$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^\circ C$	III	MIN	V_{GD}	0.2		V
$I_T=100mA$		MAX	I_H	80		mA
$I_G=1.2 \times I_{GT}$		MAX	I_L	I - III	80	mA
		MAX		II	100	mA
$V_D = 67\% V_{DRM}$, gate open, mA, $T_J=125^\circ C$		MIN	dv/dt	500		V/us
Critical rise rate of commutation voltage $T_J=150^\circ C$		MIN	(dv/dt) _c	10		V/us

5.2 Electrical characteristics (4 quadrants)

Parameter	Quadrant	Range	Symbol	value		Units
$V_D=12V$ $R_L=100\Omega$	I - II - III	MAX	I_{GT}	I II III	IV	mA
		MAX		≤ 50	≤ 120	mA
$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^\circ C$	IV	MAX	V_{GT}	1.5		V
		MIN	V_{GD}	0.2		V
$I_T=500mA$		MAX	I_H	80		mA
$I_G=1.2 \times I_{GT}$		MAX	I_L	80		mA
		MAX		100		mA
$V_D = 67\% V_{DRM}$, gate open, mA, $T_J=125^\circ C$		MIN	dv/dt	500		V/us
Critical rise rate of commutation voltage $T_J=150^\circ C$		MIN	(dv/dt) _c	10		V/us



6.Static Parameters

Parameter			Symbol	Values	Units
$I_{TM}=50A$	$T_J=25^{\circ}C$	MAX	V_{TM}	1.55	V
threshold on-state voltage	$T_J=150^{\circ}C$	MAX	V_{T0}	0.87	V
Dynamic resistance	$T_J=150^{\circ}C$	MAX	R_d	14.6	mΩ
$V_{DRM}=V_{RRM}$	$T_J=25^{\circ}C$	MAX	I_{DRM}, I_{RRM}	5	uA
	$T_J=150^{\circ}C$	MAX		1	mA
Junction to ambient	BTA	MAX	$R_{th(j-c)}$	2.05	°C/W



7. Typical Characteristic

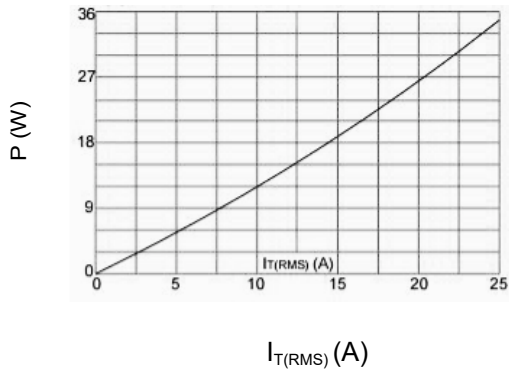


Figure 1: Maximum power dissipation versus RMS on-state current

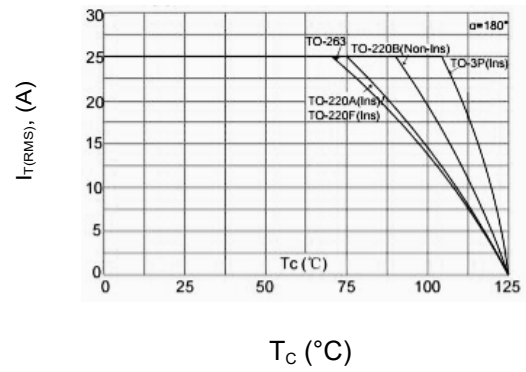


Figure 2: RMS on-state current versus case temperature

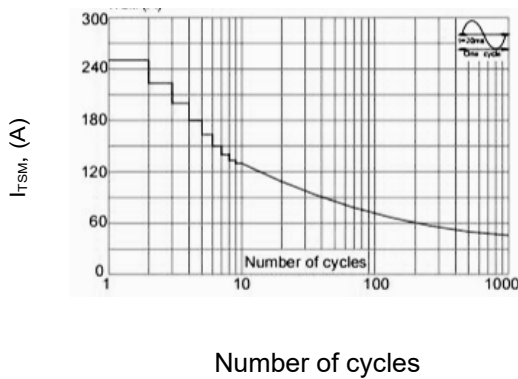


Figure 3: Surge peak on-state current versus number of cycles

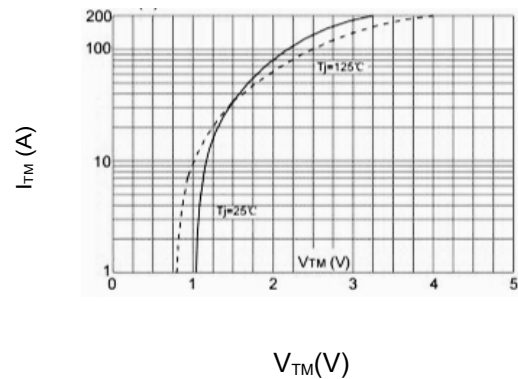


Figure 4: On-state characteristics (maximum values)

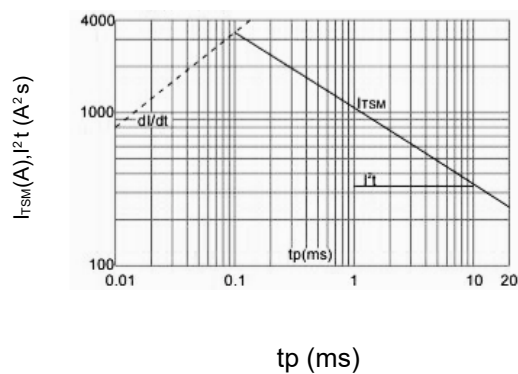


Figure 5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$

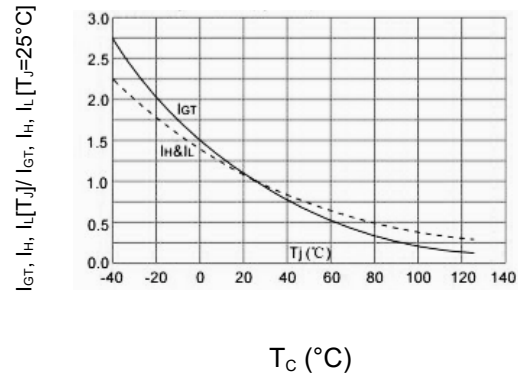
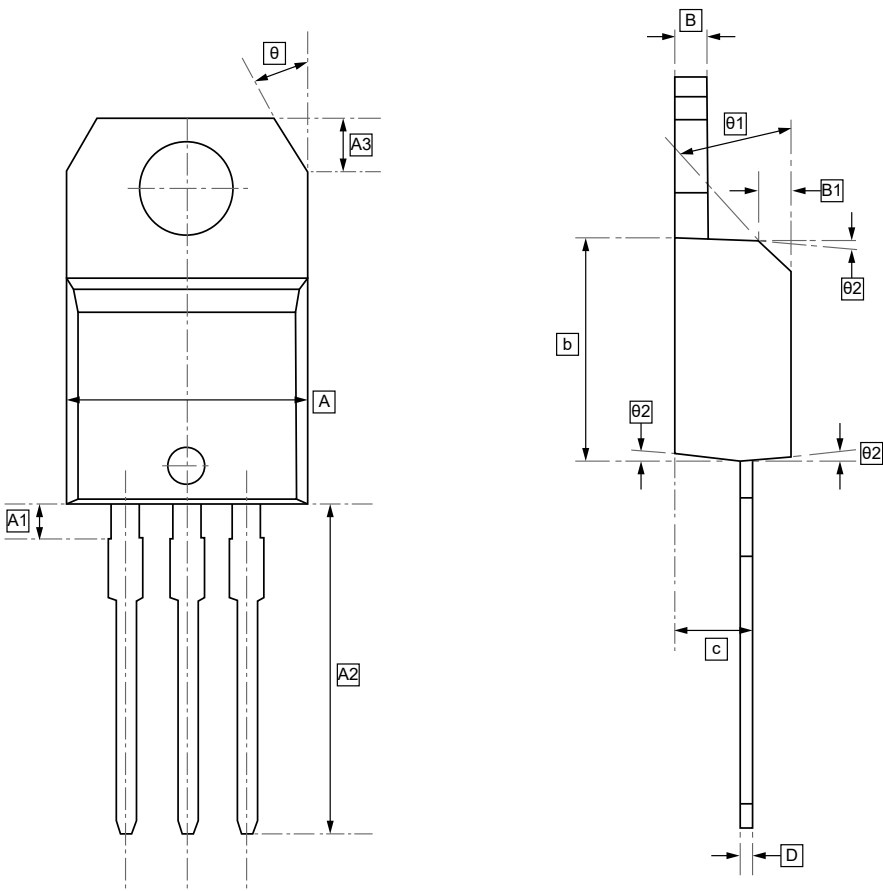


Figure 6: Relative variations of gate trigger current



9.TO-220A Package Outline Dimensions



DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	A3	B	B1	b	c	D	θ	θ1	θ2
Min	10.05	1.45	1.18	2.05	1.235	1.0	8.90	2.50	0.435	30°	45°	5°
Max	10.25		1.48	2.35	1.265		9.10	2.60	0.465			



10.Ordering information

BTA24-600B
UMW yyww

yy: Year Code
ww: Week Code

Order Code	Marking	Package	Base QTY	Delivery Mode
UMW BTA24-600CRG	BTA24-600C	TO-220A	1000	Tube and box
UMW BTA24-600BWRG	BTA24-600BW	TO-220A	1000	Tube and box
UMW BTA24-600CWRG	BTA24-600CW	TO-220A	1000	Tube and box
UMW BTA24-800CRG	BTA24-800C	TO-220A	1000	Tube and box
UMW BTA24-800BRG	BTA24-800B	TO-220A	1000	Tube and box
UMW BTA24-800BWRG	BTA24-800BW	TO-220A	1000	Tube and box
UMW BTA24-800CWRG	BTA24-800CW	TO-220A	1000	Tube and box



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