

# isc Triacs

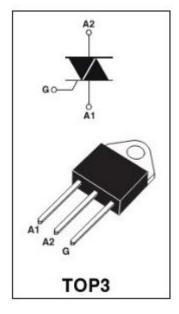
BTA440Z-800BT

## FEATURES

- With TOP3 insulated package
- Suitable for general purpose where high surge current capability is required. Application such as phase control and tatic switching on inductive or resistive load.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	800	V
V <sub>RRM</sub>	Repetitive peak reverse voltage	800	V
I <sub>T(RMS)</sub>	RMS on-state current (full sine wave)Tj=80 $^\circ\!\!\!\mathrm{C}$	40	А
I <sub>TSM</sub>	Non-repetitive peak on-state current tp=20ms	440	А
Tj	Operating junction temperature	150	°C
T <sub>stg</sub>	Storage temperature	-40~150	°C
$P_{G(AV)}$	Average gate power dissipation	1	W
R <sub>th(j-c)</sub>	Thermal resistance, junction to case	0.9	°C/W
R <sub>th(j-a)</sub>	Thermal resistance, junction to ambient	50	°C/W

#### ABSOLUTE MAXIMUM RATINGS(Ta=25°C)



### ELECTRICAL CHARACTERISTICS (Tc=25 $^{\circ}$ C unless otherwise specified)

SYMBOL	PARAMETER		CONDITIONS	МАХ	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current		V <sub>R</sub> =V <sub>RRM</sub>	5	μA
I <sub>DRM</sub>	Repetitive peak off-state current		V <sub>D</sub> =V <sub>DRM</sub>	5	μA
I <sub>GT</sub>	Gate trigger current		50		
		II	V <sub>D</sub> =12V; R <sub>L</sub> = 33 Ω	50	mA
		III		50	
l <sub>H</sub>	Holding current		I <sub>GT</sub> = 0.5A, Gate Open	80	mA
V <sub>GT</sub>	Gate trigger voltage all quadrant		V <sub>D</sub> =12V; R <sub>L</sub> = 33 Ω	1.3	V
V <sub>TM</sub>	On-state voltage		I <sub>TM</sub> = 60A; t <sub>p</sub> = 380 μ s	1.55	V

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